

# TLC696x1 16-Channel, Local Dimming Backlight LED Driver

## 1 Features

- Operating voltage  $V_{CC}$  range: 3V to 5.5V
- 16 constant current sinks with high precision:
  - Maximum output current / voltage:
    - 30mA / 20V: TLC69601
    - 60mA / 20V: TLC69611
    - 30mA / 50V: TLC69651
    - 60mA / 50V: TLC69661
  - Device-to-device error:  $\pm 2\%$  (typ.)
  - Channel-to-channel error:  $\pm 2\%$  (typ.)
- Flexible dimming control:
  - Global 8-bit Maximum Current (MC) setting
  - Brightness resolution: up to 15-bit
  - PWM / Hybrid control mode
- High speed daisy chain interface:
  - I/O voltage compatible with: 1.8V / 3.3V
  - Data transfer rate: up to 20MHz
- High system efficiency:
  - Adaptive headroom voltage control (AHVC)
  - Ultra-low device power consumption:
    - Standby mode:  $I_{CC} \leq 200\mu A$
    - Normal mode:  $I_{CC} \leq 3.5mA$
- EMI enhancement:
  - Programmable interface driving capability
  - Integrated 4 phase-shifting schemes
- Display quality improvement:
  - Minimum brightness update latency
  - Programmable black insertion
  - Variable refresh rate (VRR) without flickering
- Diagnostics:
  - LED open / short detection for each zone
  - Device thermal shutdown detection
  - Report interface option:
    - UART and interrupt pin (INT)
    - Two-wire output: CLK\_O and SOUT

## 2 Applications

- LCD local dimming backlight:
  - TV
  - Monitor
  - Notebook
  - Tablet

## 3 Description

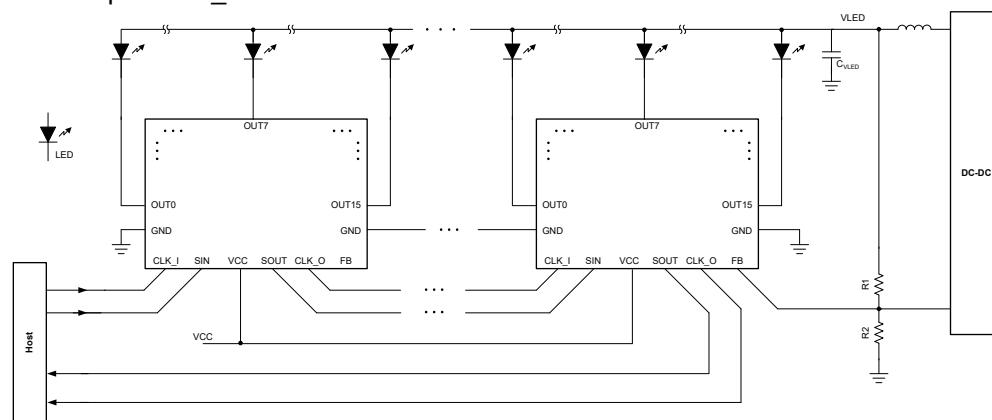
TLC696x1 is a 16-channel, constant current sink driver. Each device integrates 16 constant current sinks with SRAM for brightness storage. The device connects to each other by two-wire serial interface in daisy chain topology and supports up to 1024 devices for 16,000 local dimming zones.

To optimize system efficiency, the device equips with adaptive headroom voltage control scheme to directly control DC/DC. Only the FB pin from last device in serial chain should be connected to DC/DC to achieve simplified system layout. The device also integrates minimum brightness update latency, black insertion and VRR features to improve display quality.

TLC696x1 has three error flags: LED open detection (LOD), LED short detection (LSD) and thermal shutdown detection (TSD) for diagnostic. The device implements two options for readback including UART/INT and SOUT/CLK\_O which is programmable by register.

## Device Information

PART NUMBER	PACKAGE <sup>(1)</sup>	BODY SIZE (NOM)
TLC696x1	DSBGA (24)	1.58mm x 2.43mm
	WQFN (24)	4mm x 4mm



Simplified Schematic



An IMPORTANT NOTICE at the end of this data sheet addresses availability, warranty, changes, use in safety-critical applications, intellectual property matters and other important disclaimers. PRODUCTION DATA.

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## 4 Device and Documentation Support

TI offers an extensive line of development tools. Tools and software to evaluate the performance of the device, generate code, and develop solutions are listed below.

### 4.1 Receiving Notification of Documentation Updates

To receive notification of documentation updates, navigate to the device product folder on [ti.com](http://ti.com). Click on *Notifications* to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

### 4.2 Support Resources

[TI E2E™ support forums](#) are an engineer's go-to source for fast, verified answers and design help — straight from the experts. Search existing answers or ask your own question to get the quick design help you need.

Linked content is provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's [Terms of Use](#).

### 4.3 Trademarks

TI E2E™ is a trademark of Texas Instruments.

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### 4.4 Electrostatic Discharge Caution



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

### 4.5 Glossary

[TI Glossary](#) This glossary lists and explains terms, acronyms, and definitions.

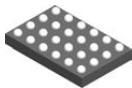
## 5 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

DATE	REVISION	NOTES
April 2023	*	Initial Release

## 6 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

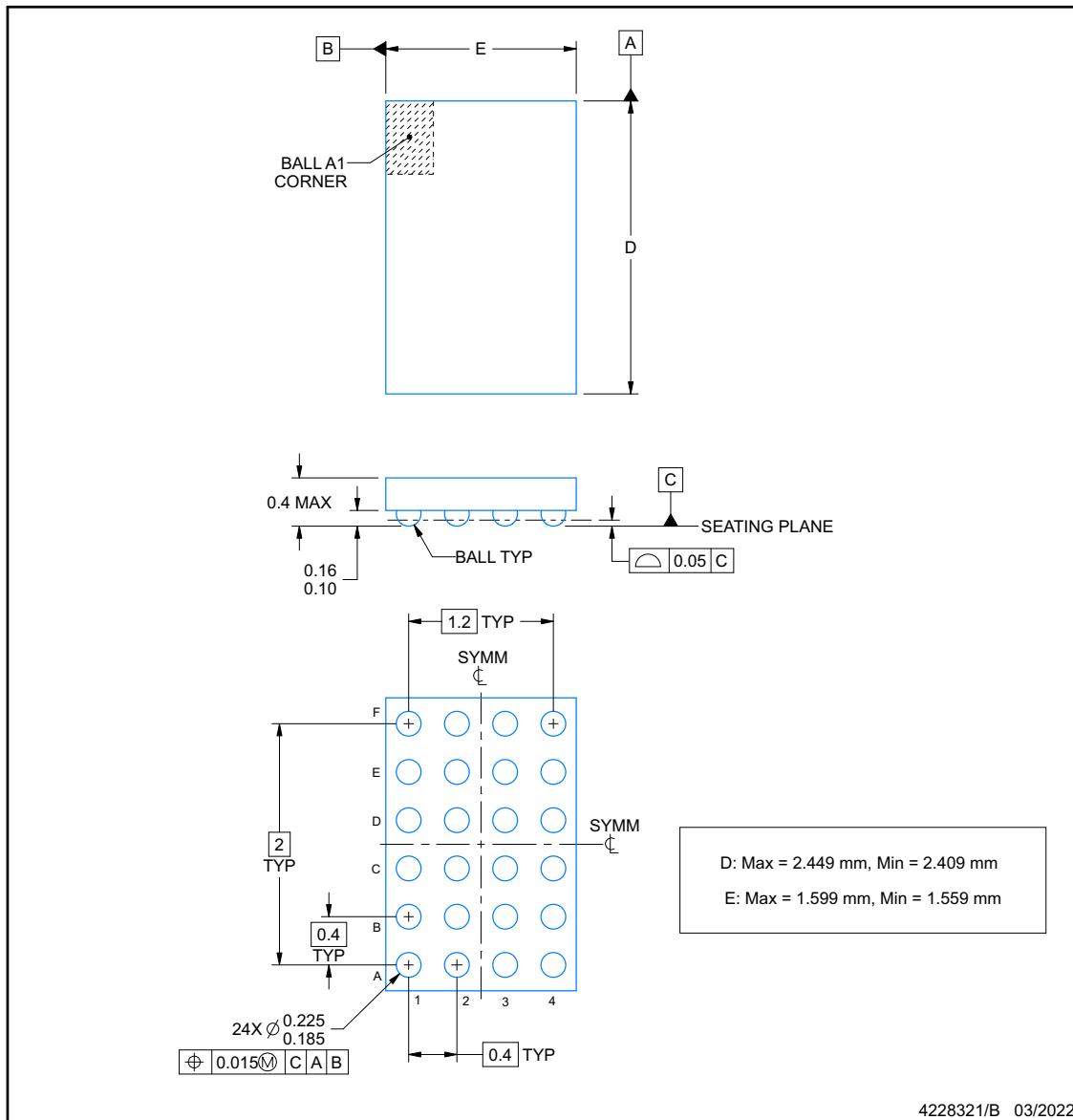


## PACKAGE OUTLINE

**YBH0024-C01**

**DSBGA - 0.4 mm max height**

DIE SIZE BALL GRID ARRAY



NOTES:

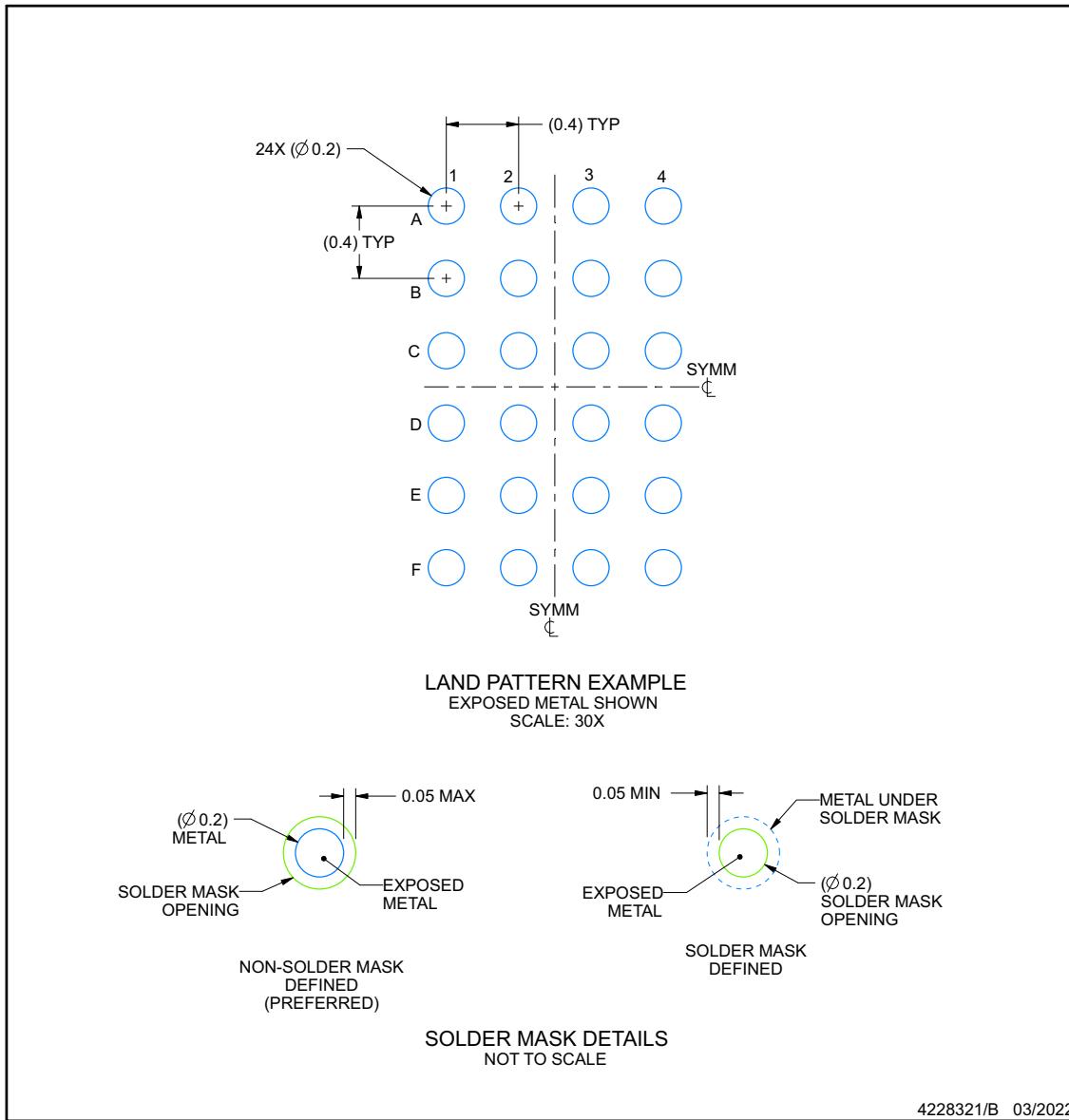
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.

## EXAMPLE BOARD LAYOUT

YBH0024-C01

DSBGA - 0.4 mm max height

DIE SIZE BALL GRID ARRAY



NOTES: (continued)

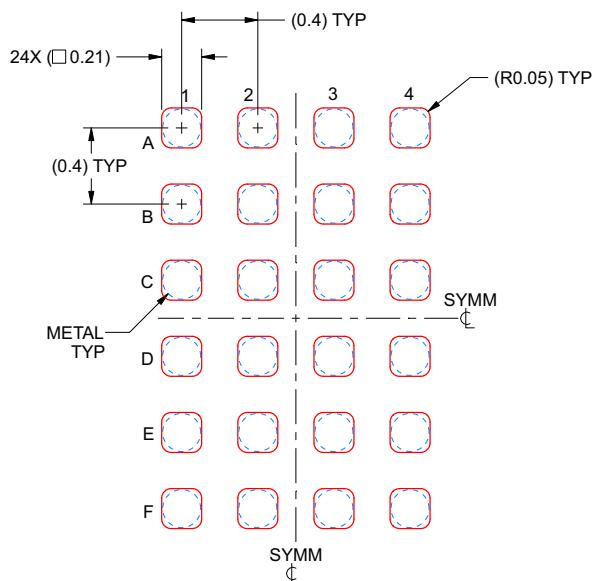
3. Final dimensions may vary due to manufacturing tolerance considerations and also routing constraints.  
See Texas Instruments Literature No. SNVA009 ([www.ti.com/lit/snva009](http://www.ti.com/lit/snva009)).

## EXAMPLE STENCIL DESIGN

YBH0024-C01

### DSBGA - 0.4 mm max height

## DIE SIZE BALL GRID ARRAY



**SOLDER PASTE EXAMPLE  
BASED ON 0.075 mm THICK STENCIL  
SCALE: 30X**

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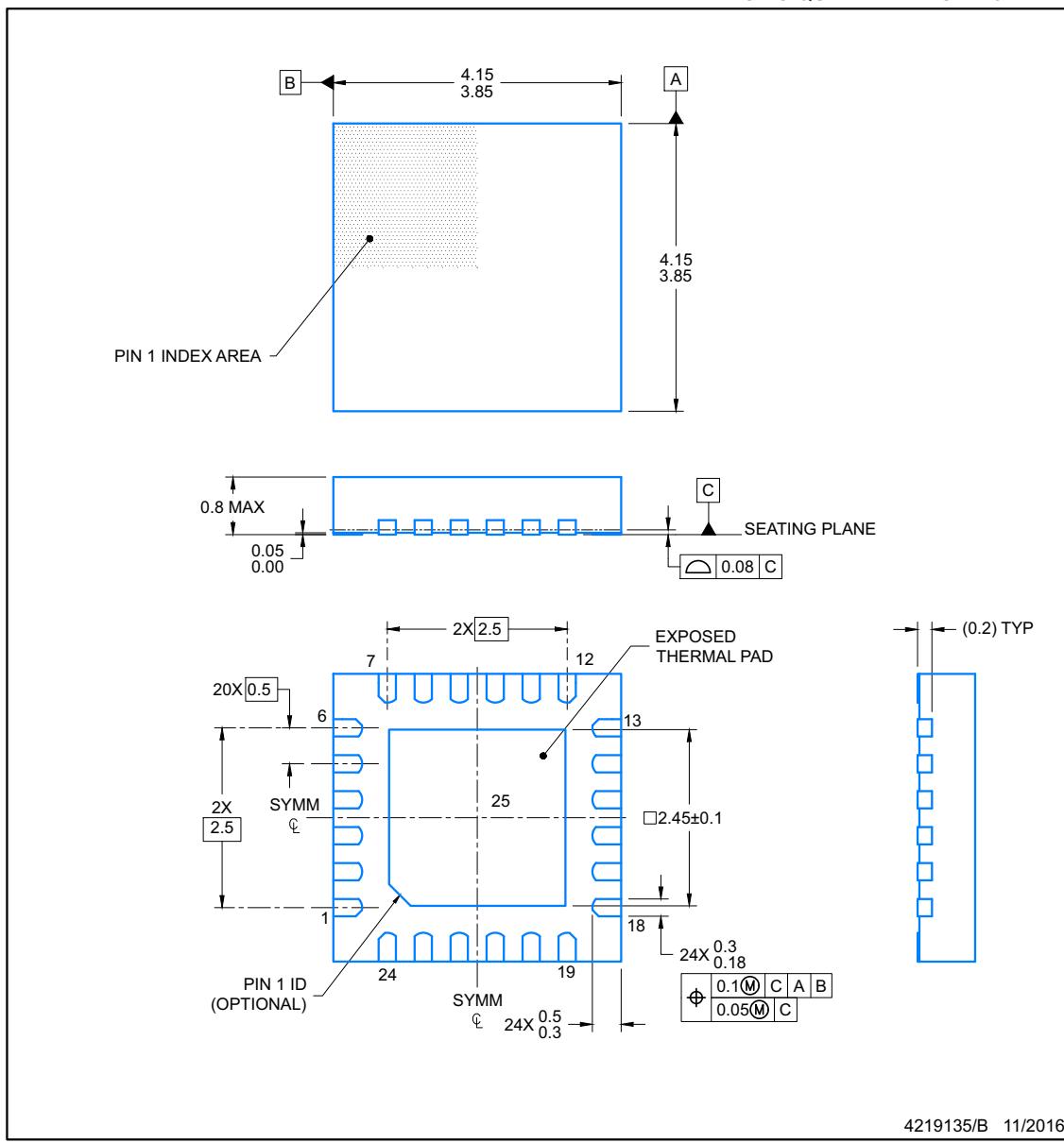
**NOTES: (continued)**

4. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release.



**PACKAGE OUTLINE**  
**RTW0024B**  
**WQFN - 0.8 mm max height**

PLASTIC QUAD FLATPACK-NO LEAD



NOTES:

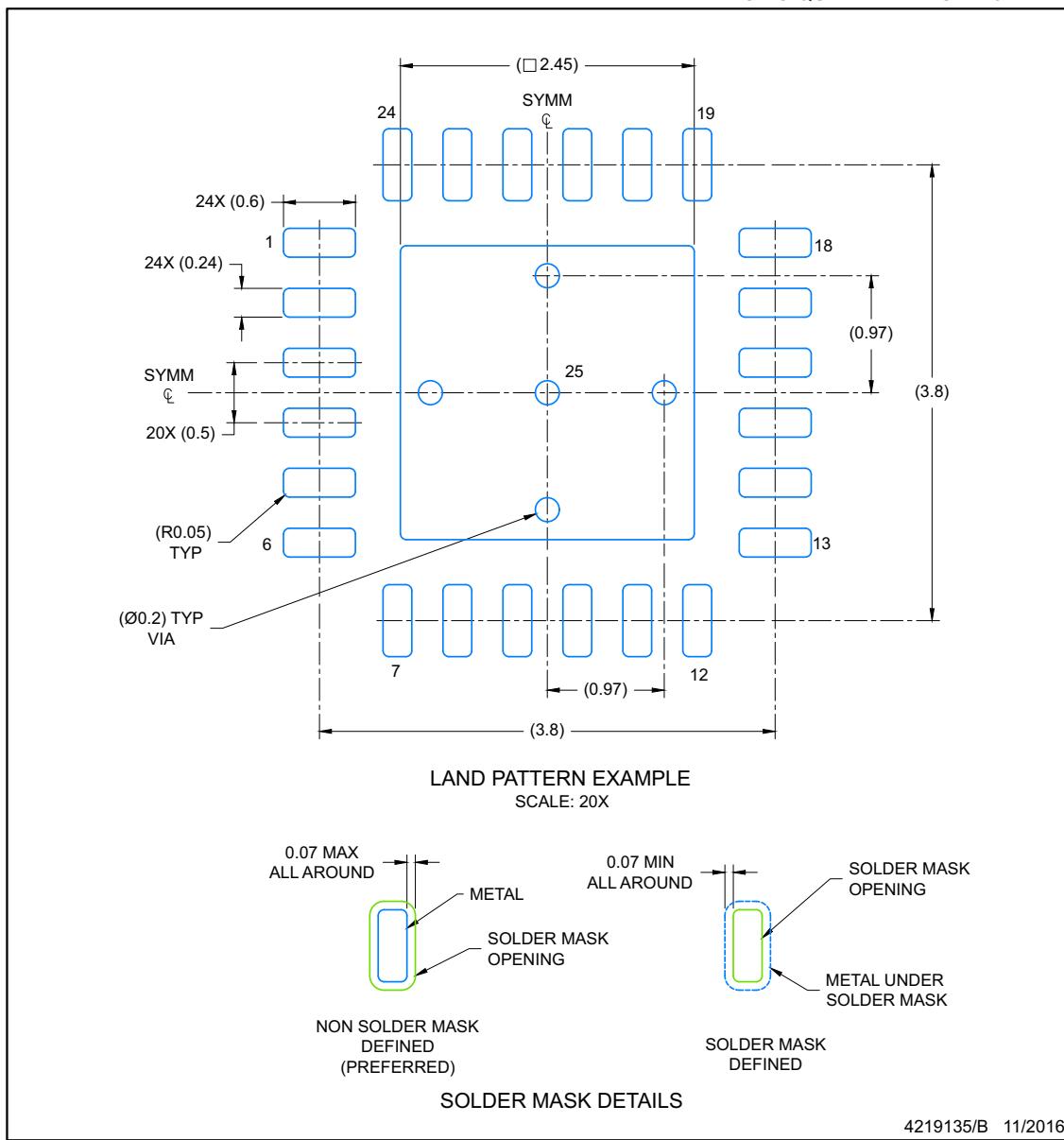
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.

## EXAMPLE BOARD LAYOUT

### RTW0024B

WQFN - 0.8 mm max height

PLASTIC QUAD FLATPACK-NO LEAD



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NOTES: (continued)

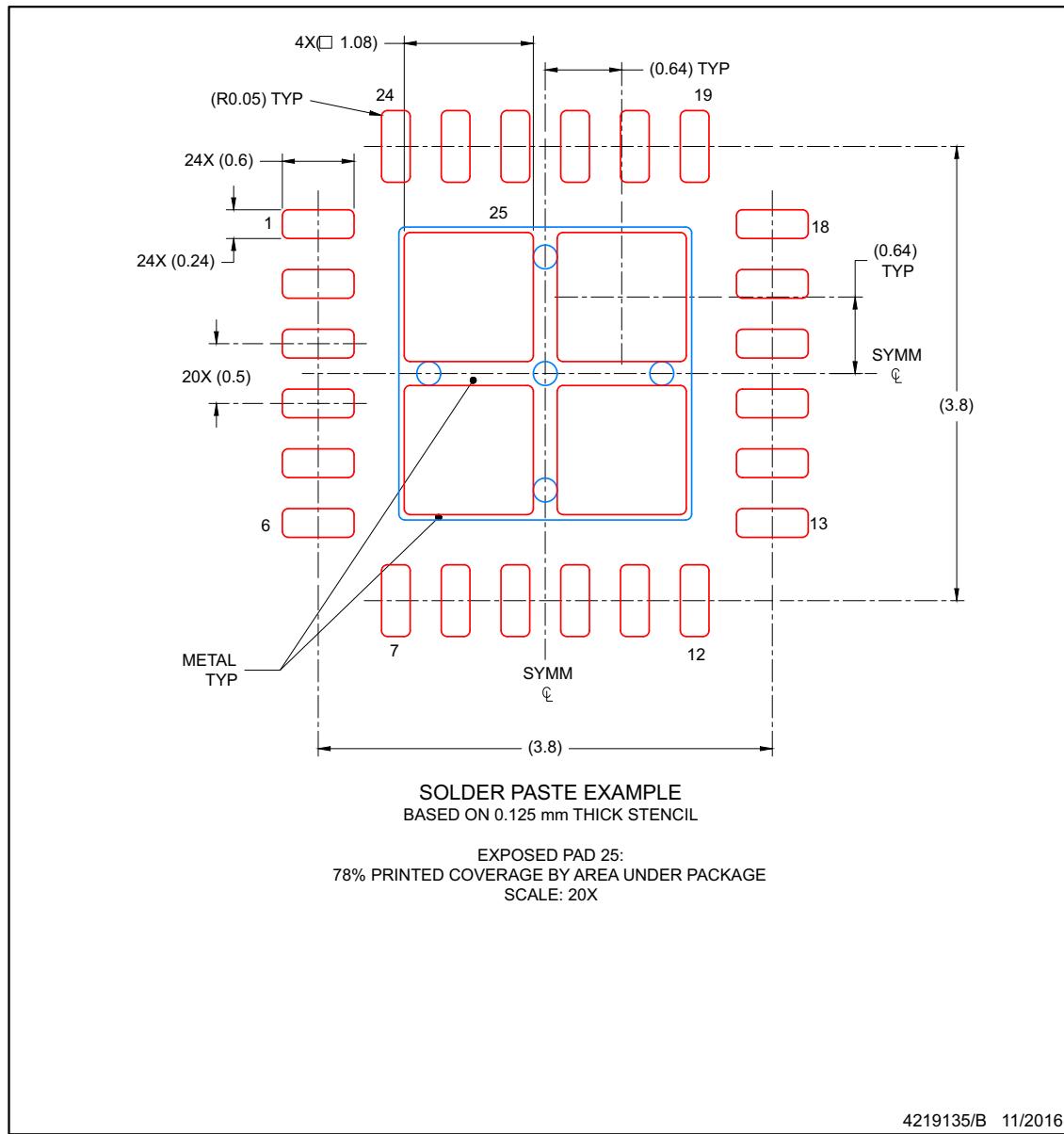
3. For more information, see Texas Instruments literature number SLUA271 ([www.ti.com/lit/slua271](http://www.ti.com/lit/slua271)).

## EXAMPLE STENCIL DESIGN

RTW0024B

## **WQFN - 0.8 mm max height**

**PLASTIC QUAD FLATPACK-NO LEAD**



#### NOTES: (continued)

4. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

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**PACKAGING INFORMATION**

Orderable part number	Status	Material type	Package   Pins	Package qty   Carrier	RoHS	Lead finish/ Ball material	MSL rating/ Peak reflow	Op temp (°C)	Part marking
(1)	(2)				(3)	(4)	(5)		(6)
TLC69601RTWR	Active	Production	WQFN (RTW)   24	3000   LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 85	69601
TLC69601YBHR	Active	Production	DSBGA (YBH)   24	3000   LARGE T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TLC69601
TLC69611RTWR	Active	Production	WQFN (RTW)   24	3000   LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 85	69611
TLC69611YBHR	Active	Production	DSBGA (YBH)   24	3000   LARGE T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TLC69611
TLC69651RTWR	Active	Production	WQFN (RTW)   24	3000   LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 85	69651
TLC69651YBHR	Active	Production	DSBGA (YBH)   24	3000   LARGE T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TLC69651
TLC69661RTWR	Active	Production	WQFN (RTW)   24	3000   LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 85	69661
TLC69661YBHR	Active	Production	DSBGA (YBH)   24	3000   LARGE T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TLC69661

(1) **Status:** For more details on status, see our [product life cycle](#).

(2) **Material type:** When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

(3) **RoHS values:** Yes, No, RoHS Exempt. See the [TI RoHS Statement](#) for additional information and value definition.

(4) **Lead finish/Ball material:** Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

(5) **MSL rating/Peak reflow:** The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

(6) **Part marking:** There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

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In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

**OTHER QUALIFIED VERSIONS OF TLC69601, TLC69611, TLC69651, TLC69661 :**

- Automotive : [TLC69601-Q1](#), [TLC69611-Q1](#), [TLC69651-Q1](#), [TLC69661-Q1](#)

NOTE: Qualified Version Definitions:

- Automotive - Q100 devices qualified for high-reliability automotive applications targeting zero defects

# GENERIC PACKAGE VIEW

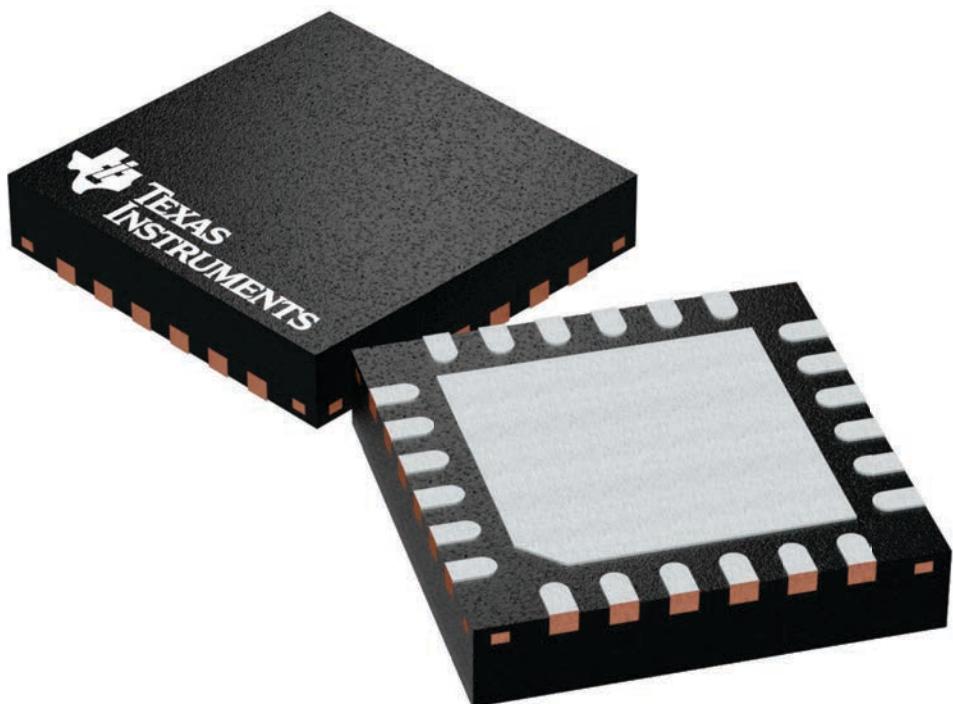
## RTW 24

## WQFN - 0.8 mm max height

4 x 4, 0.5 mm pitch

PLASTIC QUAD FLATPACK - NO LEAD

This image is a representation of the package family, actual package may vary.  
Refer to the product data sheet for package details.

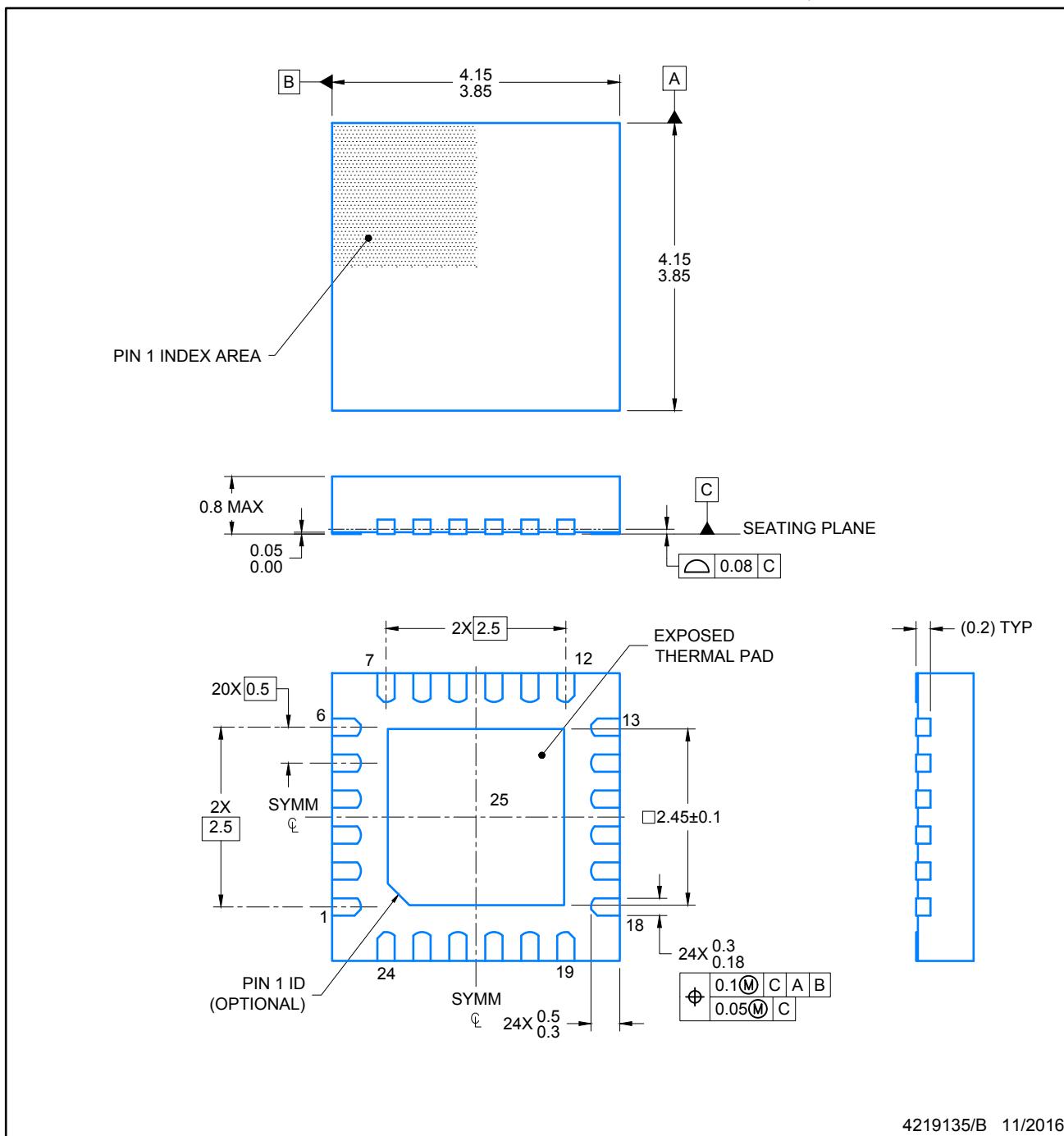


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# PACKAGE OUTLINE

## RTW0024B WQFN - 0.8 mm max height

PLASTIC QUAD FLATPACK-NO LEAD



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### NOTES:

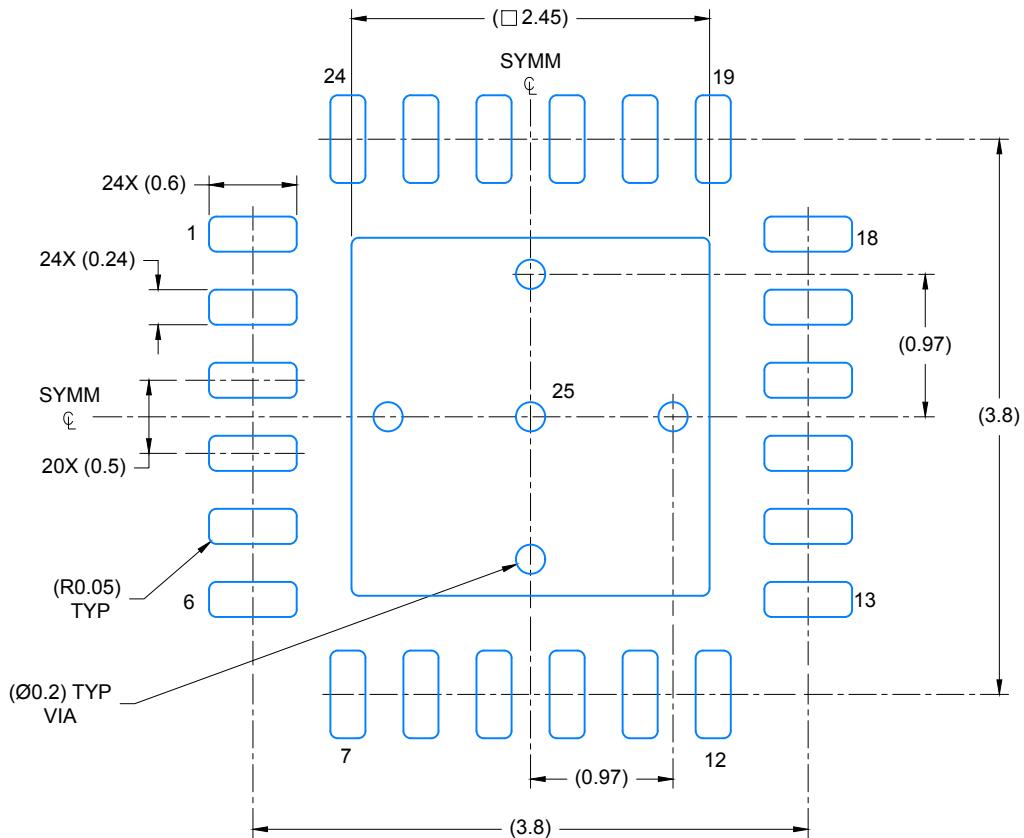
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## EXAMPLE BOARD LAYOUT

RTW0024B

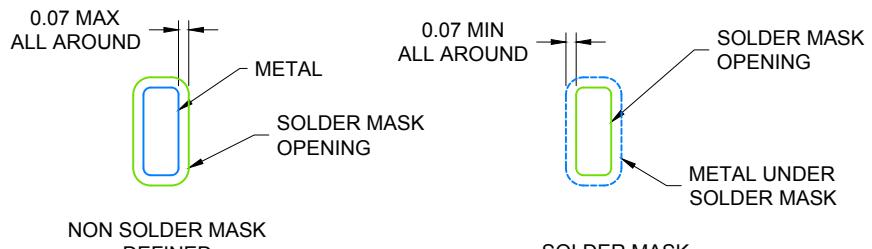
## **WQFN - 0.8 mm max height**

## PLASTIC QUAD FLATPACK-NO LEAD



## LAND PATTERN EXAMPLE

SCALE: 20X



## SOI DFR MASK DETAILS

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#### NOTES: (continued)

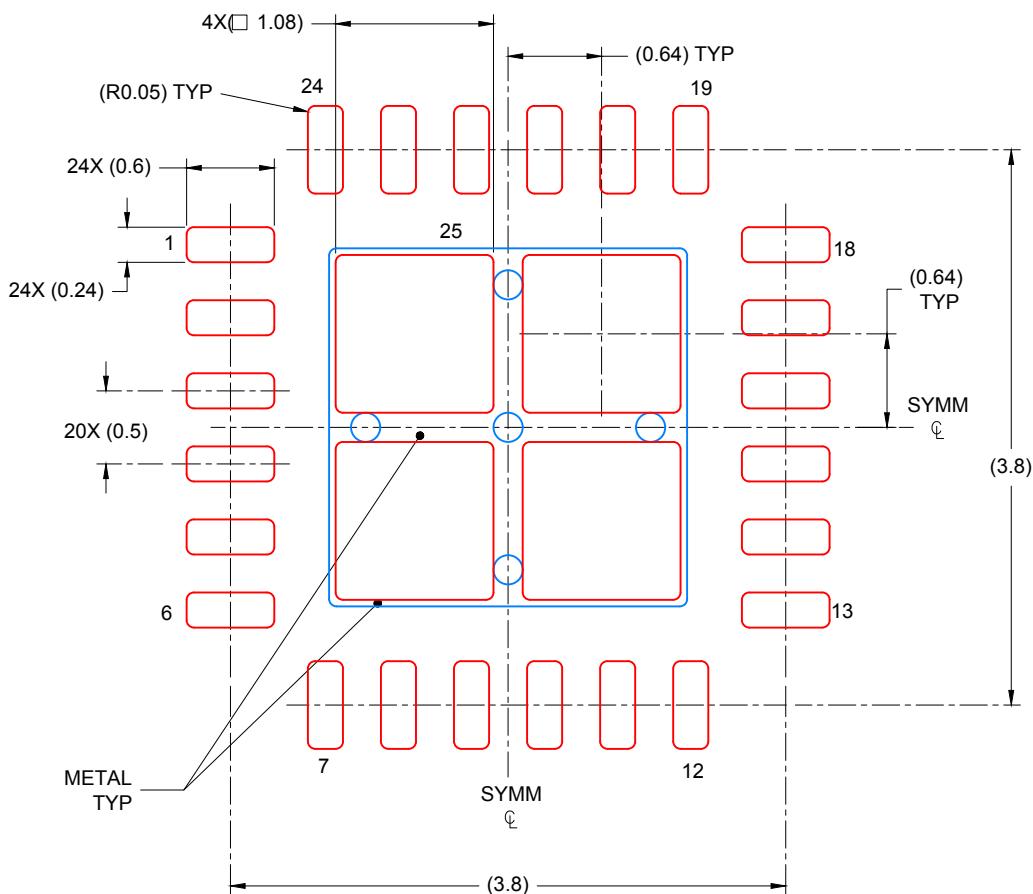
3. For more information, see Texas Instruments literature number SLUA271 ([www.ti.com/lit/slua271](http://www.ti.com/lit/slua271)).

# EXAMPLE STENCIL DESIGN

RTW0024B

WQFN - 0.8 mm max height

PLASTIC QUAD FLATPACK-NO LEAD



SOLDER PASTE EXAMPLE  
BASED ON 0.125 mm THICK STENCIL

EXPOSED PAD 25:  
78% PRINTED COVERAGE BY AREA UNDER PACKAGE  
SCALE: 20X

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NOTES: (continued)

4. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

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