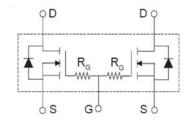


# Trench<sup>™</sup> Power MOSFET Common-Gate Pair

# IXTL2x180N10T

(Electrically Isolated Back Surface)

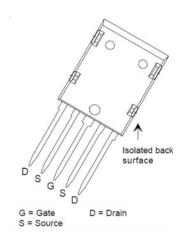
N-Channel Enhancement Mode Avalanche Rated Fast Intrinsic Rectifier



Symbol	Test Conditions	Maximum Ratings			
V <sub>DSS</sub> V <sub>DGR</sub>	$T_J = 25^{\circ}\text{C to } 175^{\circ}\text{C}$ $T_J = 25^{\circ}\text{C to } 175^{\circ}\text{C}, R_{GS} = 1M\Omega$	100 100	V		
V <sub>GSS</sub> V <sub>GSM</sub>	Continuous Transient	± 20 ± 30	V V		
I <sub>D25</sub> I <sub>L(RMS)</sub>	$T_{c} = 25^{\circ}C$ External Lead Current Limit $T_{c} = 25^{\circ}C$ , Pulse Width Limited by $T_{JM}$	100 75 450	A A A		
I <sub>A</sub> E <sub>AS</sub>	$T_{c} = 25^{\circ}C$ $T_{c} = 25^{\circ}C$	25 750	A mJ		
$\mathbf{P}_{_{\mathrm{D}}}$	$T_{c} = 25^{\circ}C$	150	W		
dv/dt	$I_{_{S}} \le I_{_{DM}}, V_{_{DD}} \le V_{_{DSS}}, T_{_{J}} \le 175^{\circ}C$	3	V/ns		
T <sub>J</sub> T <sub>JM</sub> T <sub>stg</sub>		-55 +175 175 -55 +175	O° O° O°		
T <sub>L</sub> T <sub>SOLD</sub>	1.6mm (0.062 in.) from Case for 10s Plastic Body for 10s	300 260	°C		
F <sub>c</sub>	Mounting Force	20120 /927	N/lb.		
Weight		8	g		

Symbol	Symbol Test Conditions Chara			cteristic Values			
$(T_J = 25^{\circ}C U)$	nless Otherwise Specified)		Min.	Тур.	Max	<u>.                                    </u>	
BV <sub>DSS</sub>	$V_{GS} = 0V, I_{D} = 250\mu A$		100			V	
$V_{\rm GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		2.5		4.5	V	
l <sub>gss</sub>	$V_{GS} = \pm 20V, V_{DS} = 0V$				± 200	nA	
I <sub>DSS</sub>	$V_{DS} = V_{DSS}, V_{GS} = 0V$				5	μΑ	
		$T_J = 150^{\circ}C$			250	μΑ	
R <sub>DS(on)</sub>	$V_{GS} = 10V, I_{D} = 50A, Note 1$				9	mΩ	

 $V_{DSS} = 100V$   $I_{D25} = 2x100A$   $R_{DS(cp)} \le 9m\Omega$ 



### **Features**

- Silicon Chip on Direct-Copper Bond (DCB) Substrate
- Isolated Mounting Surface
- 2500V~ Electrical Isolation
- 175°C Operating Temperature
- Avalanche Rated
- . High Current Handling Capability
- Fast Intrinsic Rectifier
- $\bullet$  Low  $\boldsymbol{R}_{\text{DS(on)}}$  and  $\boldsymbol{Q}_{\text{G}}$

## **Advantages**

- High Power Density
- Easy to Mount
- Space Savings

# **Applications**

- Automotive
  - Motor Drives
  - DC/DC Conversion
  - 42V Power Bus
  - ABS Systems
- DC/DC Converters and Off-Line UPS
- Primary Switch for 24V and 48V Systems
- High Current Switching Applications
- Distributed Power Architechtures and VRMs
- Electronic Valve Train Systems
- · High Voltage Synchronous Recifier



Symbol	Test Conditions	<b>Characteristic Values</b>		
$(T_{J} = 25^{\circ}C, U)$	Inless Otherwise Specified)	Min.	Тур.	Max.
$g_{fs}$	$V_{DS} = 10V, I_{D} = 60A, \text{ Note } 1$	70	110	S
C <sub>iss</sub>			6900	pF
C <sub>oss</sub>	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$		923	pF
C <sub>rss</sub>			162	pF
$\mathbf{R}_{Gi}$	Gate Input Resistance		3.0	Ω
t <sub>d(on)</sub>			33	ns
t <sub>r</sub>	Resistive Switching Times		54	ns
t <sub>d(off)</sub>	$V_{GS} = 10V$ , $V_{DS} = 0.5 \cdot V_{DSS}$ , $I_{D} = 25A$ $R_{G} = 3.3\Omega$ (External)		42	ns
t <sub>f</sub>			31	ns
$Q_{g(on)}$			151	nC
Q <sub>gs</sub>	$V_{GS} = 10V, V_{DS} = 0.5 \cdot V_{DSS}, I_{D} = 25A$		39	nC
$Q_{gd}$			45	nC
$\mathbf{R}_{thJC}$				1.0 °C/W
R <sub>thCS</sub>			0.15	°C/W

# ISOPLUS I5-Pak<sup>TM</sup> (IXTL) Outline 1,5 = Drain 2,4 = Source 3 = Gate 6 = Isolated

SYM	INCHES		MILLIMETERS			
	MIN	MAX	MIN	MAX		
A	.190	.205	4,63	5.21		
A1	.102	.118	2,59	3,00		
A2	.046	.055	1.17	1,40		
ь	.045	.055	1.14	1.40		
b1	.063	.072	1.60	1.83		
b2	.100	.110	2.54	2.79		
b3	.058	.068	1.47	1.73		
С	.020	.029	0.51	0.74		
D	1.020	1.040	25.91	26.42		
E	.770	.799	19.56	20.29		
e	.150 BSC		3.81	ISC		
L	.780	.820	19.81	20.83		
L1	.080	.102	2.03	2.59		
Q	.210	.235	5.33	5.97		
Q1	.490	.513	12.45	13.03		
R	.150	.1BD	3.B1	4.57		
R1	.100	.130	2,54	3,30		
S	.668	.69D	16_97	17.53		
Т	<b>.8</b> 01	.821	20,34	20.85		
U	.065	.DBO.	1.85	2.03		

### Source-Drain Diode

SymbolTest ConditionsChara $(T_J = 25^{\circ}C, Unless Otherwise Specified)$ Min.		cteristic Typ.	Values Max.		
I <sub>s</sub>	$V_{GS} = 0V$			180	Α
I <sub>SM</sub>	Repetitive, Pulse Width Limited by $T_{_{\rm JM}}$			450	Α
V <sub>SD</sub>	$I_{F} = 50A, V_{GS} = 0V, Note 1$			1.0	V
t <sub>rr</sub>	$I_F = 25A, V_{GS} = 0V$ -di/dt = 100A/ $\mu$ s, $V_R = 50V$		60		ns

Note 1. Pulse test,  $t \le 300\mu s$ , duty cycle,  $d \le 2\%$ .

# PRELIMINARY TECHNICAL INFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from data gathered during objective characterizations of preliminary engineering lots; but also may yet contain some information supplied during a pre-production design evaluation. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

 $\ensuremath{\mathsf{IXYS}}$  Reserves the Right to Change Limits, Test Conditions, and Dimensions.

IXYS MOSFETs and IGBTs are covered 4.835.592 4,931,844 5,049,961 5,237,481 6,162,665 6,404,065 B1 7.005.734 B2 7,157,338B2 6.683.344 by one or more of the following U.S. patents: 4,860,072 5,063,307 5,381,025 6,259,123 B1 6,534,343 6,710,405 B2 6,759,692 7,063,975 B2 5,017,508 5,034,796 5,187,117 5,486,715 6,306,728 B1 6,771,478 B2 7,071,537



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