

3A, 100V - 200V Ultra Fast Surface Mount Rectifier

FEATURES

- Planar technology
- Low power loss, high efficiency
- Ideal for automated placement
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

APPLICATIONS

- High frequency switching
- DC/DC
- Snubber

KEY PARAMETERS		
PARAMETER	VALUE	UNIT
I_F	3	A
V_{RRM}	100 - 200	V
I_{FSM}	85	A
$T_{J MAX}$	175	°C
Package	DO-214AA (SMB)	
Configuration	Single die	



MECHANICAL DATA

- Case: DO-214AA (SMB)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.087g (approximately)



DO-214AA (SMB)



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)				
PARAMETER	SYMBOL	PU3BB	PU3DB	UNIT
Marking code on the device		PU3BB	PU3DB	
Repetitive peak reverse voltage	V_{RRM}	100	200	V
Reverse voltage, total rms value	$V_{R(\text{RMS})}$	70	140	V
Forward current	I_F	3		A
Surge peak forward current single half sine-wave superimposed on rated load	$t = 8.3\text{ms}$	I_{FSM}	85	A
	$t = 1.0\text{ms}$		180	
Junction temperature	T_J	-55 to +175		°C
Storage temperature	T_{STG}	-55 to +175		°C

THERMAL PERFORMANCE

PARAMETER	SYMBOL	TYP	UNIT
Junction-to-lead thermal resistance	$R_{\Theta JL}$	14	°C/W
Junction-to-ambient thermal resistance	$R_{\Theta JA}$	71	°C/W
Junction-to-case thermal resistance	$R_{\Theta JC}$	20	°C/W

Thermal Performance Note: Units mounted on PCB (10mm x 10mm Cu pad test board)

ELECTRICAL SPECIFICATIONS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

PARAMETER	CONDITIONS	SYMBOL	TYP	MAX	UNIT
Forward voltage ⁽¹⁾	$I_F = 1.5\text{A}, T_J = 25^\circ\text{C}$	V_F	0.81	-	V
	$I_F = 3.0\text{A}, T_J = 25^\circ\text{C}$		0.86	0.93	V
	$I_F = 1.5\text{A}, T_J = 125^\circ\text{C}$		0.66	-	V
	$I_F = 3.0\text{A}, T_J = 125^\circ\text{C}$		0.73	-	V
Reverse current @ rated V_R ⁽²⁾	$T_J = 25^\circ\text{C}$	I_R	-	2	μA
	$T_J = 125^\circ\text{C}$		-	10	μA
Junction capacitance	1MHz, $V_R = 4.0\text{V}$	C_J	47	-	pF
Reverse recovery time	$I_F = 0.5\text{A}, I_R = 1.0\text{A}, I_{rr} = 0.25\text{A}$	t_{rr}	-	25	ns
	$I_F = 1.0\text{A}, dI/dt = 50\text{A}/\mu\text{s}, V_R = 30\text{V}$		31	-	
Reverse recovery current	$I_F = 3.0\text{A}, dI/dt = 200\text{A}/\mu\text{s}, V_R = 100\text{V}$	I_{RM}	4.9	-	A
Reverse recovery charge		Q_{rr}	51	-	nC
Reverse recovery time		t_{rr}	23	-	ns

Notes:

1. Pulse test with PW = 0.3ms
2. Pulse test with PW = 30ms

ORDERING INFORMATION

ORDERING CODE ⁽¹⁾	PACKAGE	PACKING
PU3xB	DO-214AA (SMB)	3,000/ Tape & Reel

Notes:

1. "x" defines voltage from 100V(PU3BB) to 200V(PU3DB)

CHARACTERISTICS CURVES

($T_A = 25^\circ\text{C}$ unless otherwise noted)

Fig.1 Forward Current Derating Curve

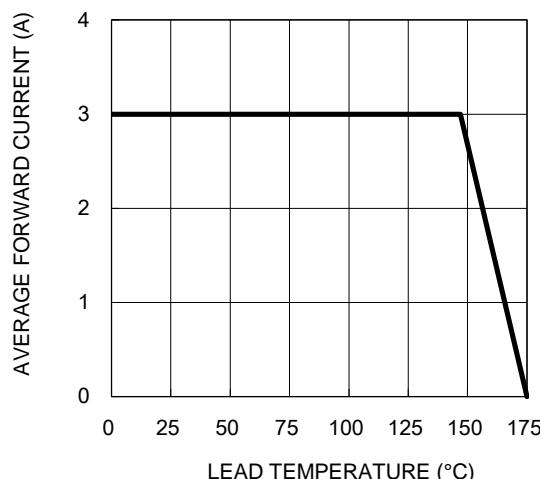


Fig.2 Typical Junction Capacitance

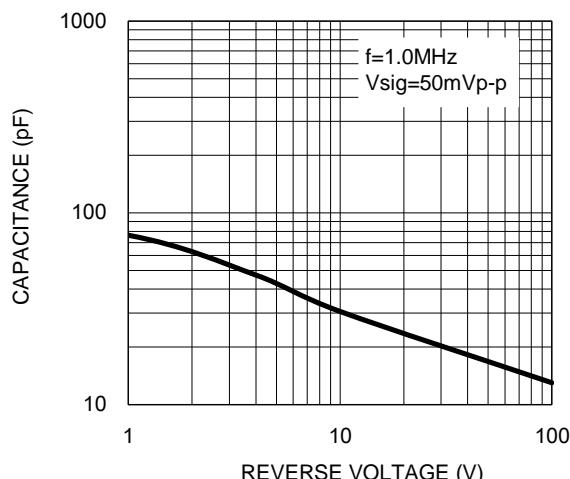


Fig.3 Typical Reverse Characteristics

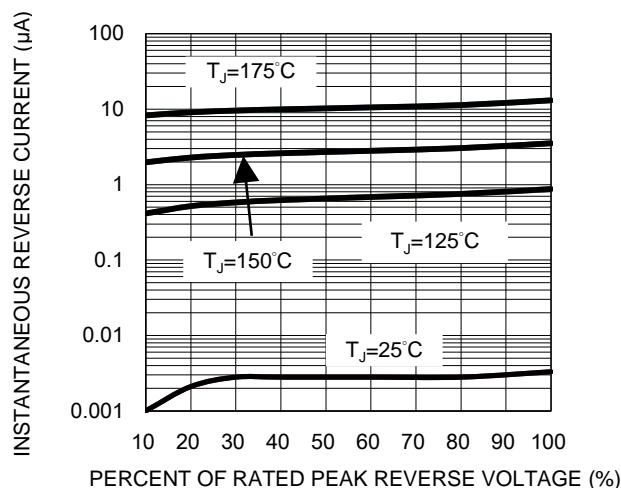


Fig.4 Typical Forward Characteristics

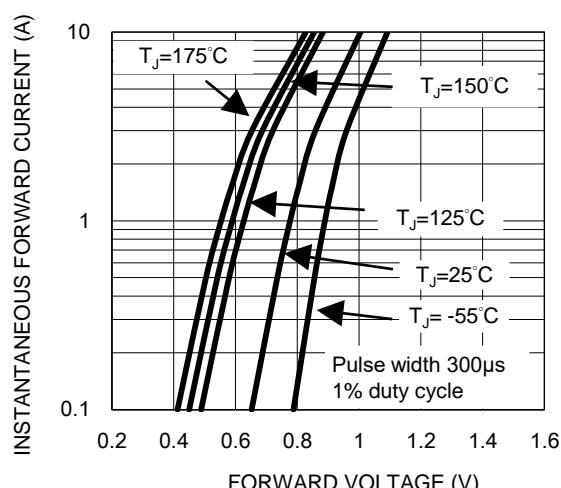
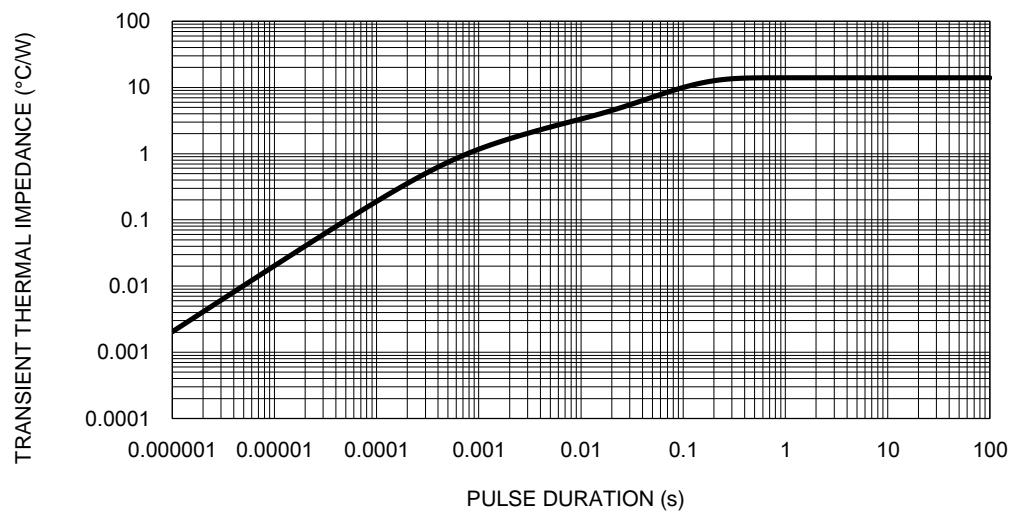
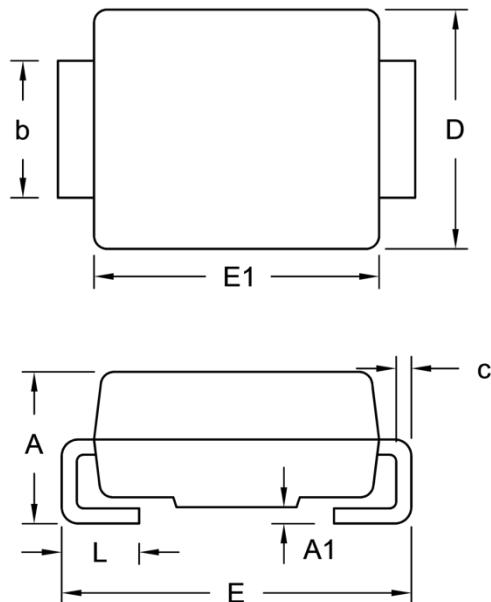


Fig.5 Typical Transient Thermal Impedance

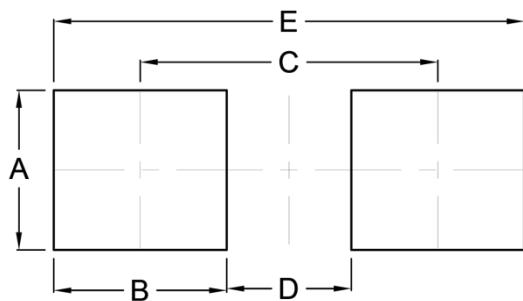


PACKAGE OUTLINE DIMENSIONS

DO-214AA (SMB)


DIM.	Unit (mm)		Unit (inch)	
	Min.	Max.	Min.	Max.
A	1.95	2.65	0.077	0.104
A1	0.05	0.20	0.002	0.008
b	1.95	2.20	0.077	0.087
c	0.15	0.31	0.006	0.012
D	3.30	3.95	0.130	0.156
E	5.10	5.60	0.201	0.220
E1	4.05	4.60	0.159	0.181
L	0.75	1.60	0.030	0.063

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	2.30	0.091
B	2.50	0.098
C	4.30	0.169
D	1.80	0.071
E	6.80	0.268

MARKING DIAGRAM



P/N = Marking Code
 G = Green Compound
 YW = Date Code
 F = Factory Code

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