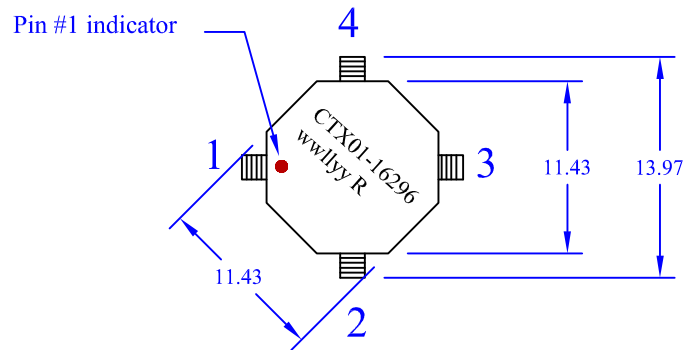
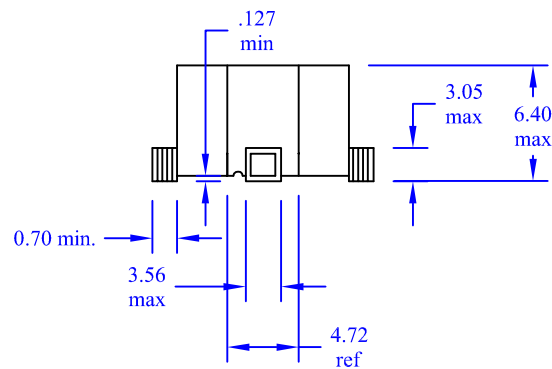


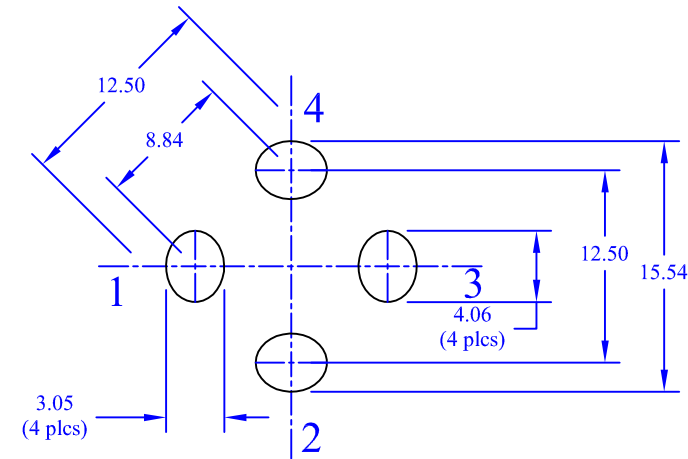
TOP VIEW



FRONT VIEW



RECOMMENDED PCB LAYOUT

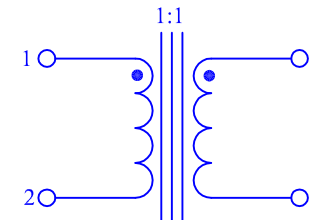


Component Side

ELECTRICAL CHARACTERISTICS

OCL @ 100 kHz, 0.10Vrms, 0.0A_{dc} pins (1 - 2). 22.7 uH min
 OCL @ 100 kHz, 0.10Vrms, 0.0A_{dc} pins (4 - 3). 22.7 uH min
 DC Resistance @ 20°C pins (1-2). 3.1 mOhm max.
 DC Resistance @ 20°C pins (4-3). 3.1 mOhm max.
 Hypot: Winding to winding: 300 Vdc for 1 second

SCHEMATIC



Notes:

- 1) All Dimensions are in millimeters unless otherwise specified.
- 2) Tolerances are .xx +/- 0.508, .xxx +/- 0.254 unless stated otherwise.
- 3) All soldering surfaces to be coplanar within 0.10 millimeters.
- 4) wwllyy = (Date Code) R= (Revision Level)
- 5) PCB tolerances are +/- 0.1 unless stated otherwise.
- 6) * Inductor should not exceed 130°C in order to maintain OCL rating. Wire is rated for 155°C. Temperature rise is (37°C max at 8A) or (59°C max at 10.0 A) in free standing air. Increased current ratings are acceptable for improved heat sinking and/or air flow. Temperature rise measured needs to be increased by the factor of specified max DCR/measured DCR.
 The system designer should provide corresponding heat transfer to limit internal temperature to 130 degree or less

Cooper Bussmann

1225 Broken Sound Parkway, Boca Raton, FL 33487

Inductor Common Mode

22.7 uH min 10.0A*
 3.1 mOhm max
 4 term, SMT

Size A	Drawing Number: CTX01-16296
	Revision Level: D