

# ZXMS6004DT8

## 60V N-CHANNEL SELF PROTECTED ENHANCEMENT MODE

### INTELLIFET™ MOSFET

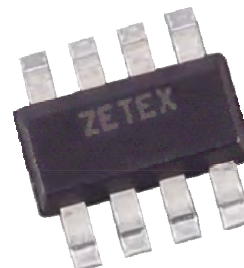
#### SUMMARY

Continuous drain source voltage 60 V

On-state resistance 500 mΩ

Nominal load current ( $V_{IN} = 5V$ ) 1.2 A

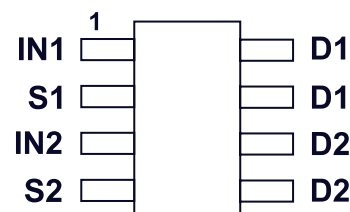
Clamping Energy 210 mJ



SM8 Package

#### DESCRIPTION

The ZXMS6004DT8 is a dual self protected low side MOSFET with logic level input. It integrates over-temperature, over-current, over-voltage (active clamp) and ESD protected logic level functionality independently per channel. The ZXMS6004DT8 is ideal as a general purpose switch driven from 3.3V or 5V microcontrollers in harsh environments where standard MOSFETs are not rugged enough.



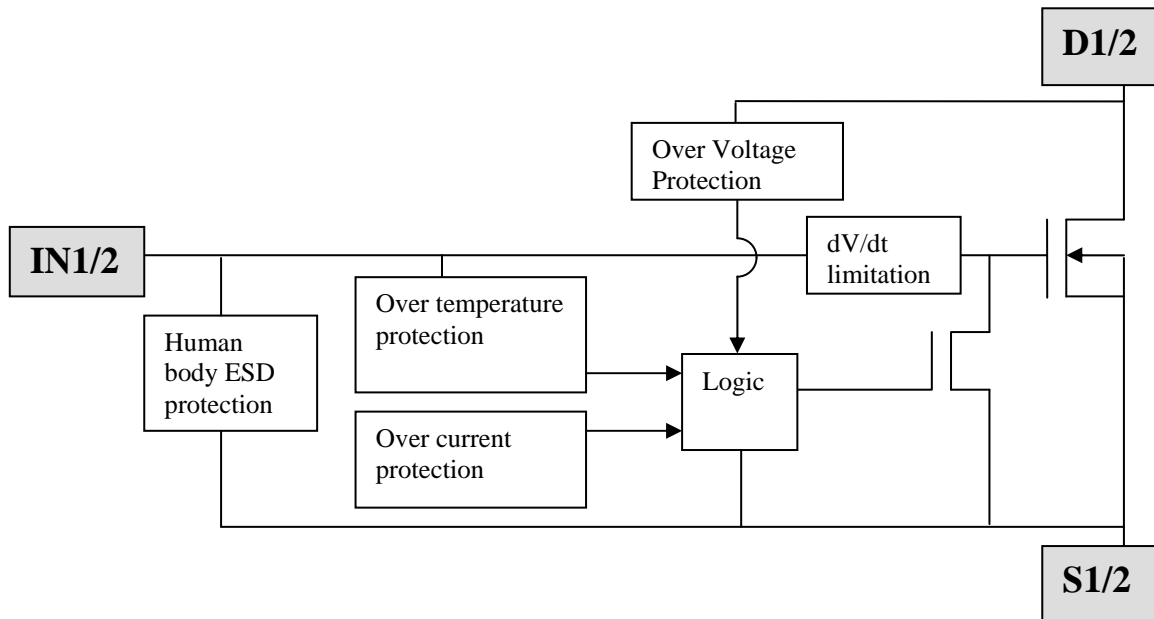
#### FEATURES

- Compact dual package
- Low input current
- Logic Level Input (3.3V and 5V)
- Short circuit protection with auto restart
- Over voltage protection (active clamp)
- Thermal shutdown with auto restart
- Over-current protection
- Input Protection (ESD)
- High continuous current rating

#### ORDERING INFORMATION

DEVICE	PART MARK	REEL SIZE (inches)	TAPE WIDTH (mm)	QUANTITY PER REEL
ZXMS6004DT8TA	ZXMS 6004D	7	12 embossed	1,000 units

## FUNCTIONAL BLOCK DIAGRAM



## APPLICATIONS AND INFORMATION

- Two completely isolated independent channels
- Especially suited for loads with a high in-rush current such as lamps and motors.
- All types of resistive, inductive and capacitive loads in switching applications.
- $\mu\text{C}$  compatible power switch for 12V and 24V DC applications.
- Automotive rated.
- Replaces electromechanical relays and discrete circuits.
- Linear Mode capability - the current-limiting protection circuitry is designed to de-activate at low  $V_{\text{DS}}$  to minimise on state power dissipation. The maximum DC operating current is therefore determined by the thermal capability of the package/board combination, rather than by the protection circuitry. This does not compromise the product's ability to self-protect at low  $V_{\text{DS}}$ .

# ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Continuous Drain-Source Voltage	$V_{DS}$	60	V
Drain-Source Voltage for short circuit protection	$V_{DS(SC)}$	36	V
Continuous Input Voltage	$V_{IN}$	-0.5 ... +6	V
Continuous Input Current -0.2V ≤ $V_{IN}$ ≤ 6V $V_{IN}$ < -0.2V or $V_{IN}$ > 6V	$I_{IN}$	No limit $ I_{IN}  \leq 2$	mA
Operating Temperature Range	$T_j$	-40 to +150	°C
Storage Temperature Range	$T_{stg}$	-55 to +150	°C
Power Dissipation at $T_A = 25^\circ\text{C}$ (a)(d) Linear Derating Factor	$P_D$	1.16 9.28	W mW/°C
Power Dissipation at $T_A = 25^\circ\text{C}$ (a)(e) Linear Derating Factor	$P_D$	1.67 13.3	W mW/°C
Power Dissipation at $T_A = 25^\circ\text{C}$ (b)(d) Linear Derating Factor	$P_D$	2.13 17	W mW/°C
Pulsed Drain Current @ $V_{IN} = 3.3\text{V}$ (c)	$I_{DM}$	2	A
Pulsed Drain Current @ $V_{IN} = 5\text{V}$ (c)	$I_{DM}$	2.5	A
Continuous Source Current (Body Diode) (a)	$I_S$	1	A
Pulsed Source Current (Body Diode) (c)	$I_{SM}$	5	A
Unclamped single pulse inductive energy, $T_j = 25^\circ\text{C}$ , $I_D = 0.5\text{A}$ , $V_{DD} = 24\text{V}$	$E_{AS}$	210	mJ
Electrostatic Discharge (Human Body Model)	$V_{ESD}$	4000	V
Charged Device Model	$V_{CDM}$	1000	V

# THERMAL RESISTANCE

PARAMETER	SYMBOL	VALUE	UNIT
Junction to Ambient (a)(d)	$R_{\theta JA}$	108	°C/W
Junction to Ambient (a)(e)	$R_{\theta JA}$	75	°C/W
Junction to Ambient (b)(d)	$R_{\theta JA}$	58.7	°C/W
Junction to Case (f)	$R_{\theta JC}$	26.5	°C/W

# NOTES

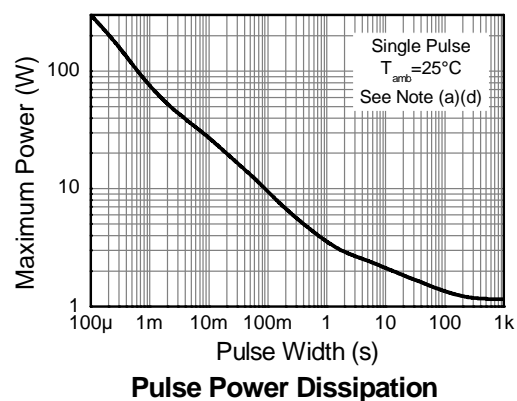
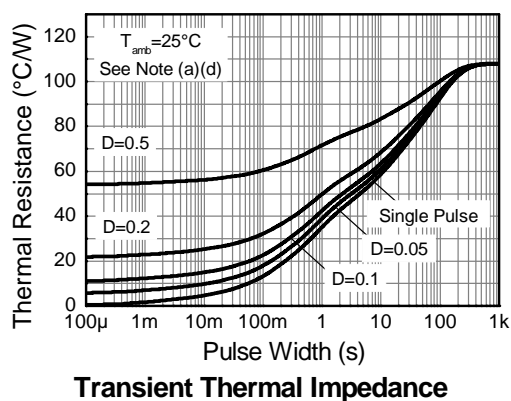
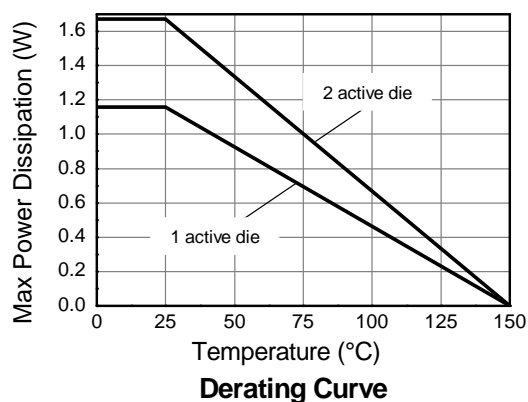
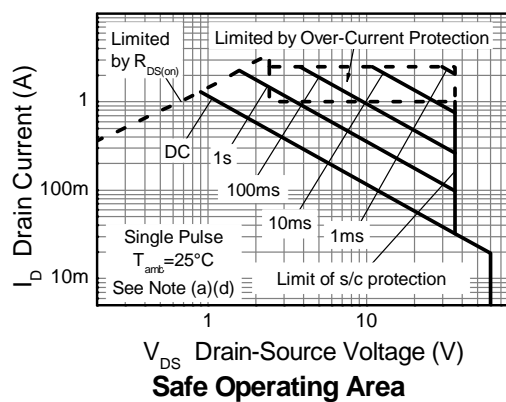
- For a dual device surface mounted on a 25mm x 25mm FR4 PCB single sided 1oz weight copper split down the middle on 1.6mm FR4 board, in still air conditions.
- For a dual device surface mounted on FR4 PCB measured at  $t \leq 10\text{sec}$
- Repetitive rating 25mm x 25mm FR4 PCB,  $D = 0.02$  pulse width = 300μs – pulse width limited by junction temperature. Refer to transient Thermal Impedance Graph.
- For a dual device with one active die.
- For dual device with 2 active die running at equal power.
- Thermal resistance from junction to solder-point (at the end of the drain lead)

## RECOMMENDED OPERATING CONDITIONS

The ZXMS6004DT8 is optimised for use with  $\mu\text{C}$  operating from 3.3V and 5V supplies.

Symbol	Description	Min	Max	Units
$V_{\text{IN}}$	Input voltage range	0	5.5	V
$T_{\text{A}}$	Ambient temperature range	-40	125	$^{\circ}\text{C}$
$V_{\text{IH}}$	High level input voltage for MOSFET to be on	3	5.5	V
$V_{\text{IL}}$	Low level input voltage for MOSFET to be off	0	0.7	V
$V_{\text{P}}$	Peripheral supply voltage (voltage to which load is referred)	0	36	V

## CHARACTERISTICS



**ELECTRICAL CHARACTERISTICS (at  $T_{amb} = 25^{\circ}\text{C}$  unless otherwise stated).**

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	CONDITIONS
<b>Static Characteristics</b>						
Drain-Source Clamp Voltage	$V_{DS(AZ)}$	60	65	70	V	$I_D=10\text{mA}$
Off state Drain Current	$I_{DSS}$			500	nA	$V_{DS}=12\text{V}, V_{IN}=0\text{V}$
Off state Drain Current	$I_{DSS}$			1	$\mu\text{A}$	$V_{DS}=36\text{V}, V_{IN}=0\text{V}$
Input Threshold Voltage	$V_{IN(th)}$	0.7	1	1.5	V	$V_{DS}=V_{GS}, I_D=1\text{mA}$
Input Current	$I_{IN}$		60	100	$\mu\text{A}$	$V_{IN}=+3\text{V}$
Input Current	$I_{IN}$		120	200	$\mu\text{A}$	$V_{IN}=+5\text{V}$
Input Current while over temperature active				220	$\mu\text{A}$	$V_{IN}=+5\text{V}$
Static Drain-Source On-State Resistance	$R_{DS(on)}$		400	600	$\text{m}\Omega$	$V_{IN}=+3\text{V}, I_D=0.5\text{A}$
Static Drain-Source On-State Resistance	$R_{DS(on)}$		350	500	$\text{m}\Omega$	$V_{IN}=+5\text{V}, I_D=0.5\text{A}$
Continuous Drain Current (a)(e)	$I_D$	0.9			A	$V_{IN}=3\text{V}; T_A=25^{\circ}\text{C}$
Continuous Drain Current (a)(e)	$I_D$	1.0			A	$V_{IN}=5\text{V}; T_A=25^{\circ}\text{C}$
Continuous Drain Current (a)(d)	$I_D$	1.1			A	$V_{IN}=3\text{V}; T_A=25^{\circ}\text{C}$
Continuous Drain Current (a)(d)	$I_D$	1.2			A	$V_{IN}=5\text{V}; T_A=25^{\circ}\text{C}$
Current Limit (g)	$I_{D(LIM)}$	0.7	1.7		A	$V_{IN}=+3\text{V},$
Current Limit (g)	$I_{D(LIM)}$	1	2.2		A	$V_{IN}=+5\text{V}$
<b>Dynamic Characteristics</b>						
Turn On Delay Time	$t_{d(on)}$		5		$\mu\text{s}$	$V_{DD}=12\text{V}, I_D=0.5\text{A}, V_{GS}=5\text{V}$
Rise time	$t_r$		10		$\mu\text{s}$	
Turn Off Delay Time	$t_{d(off)}$		45		$\mu\text{s}$	
Fall Time	$t_f$		15		$\mu\text{s}$	

**Notes:**

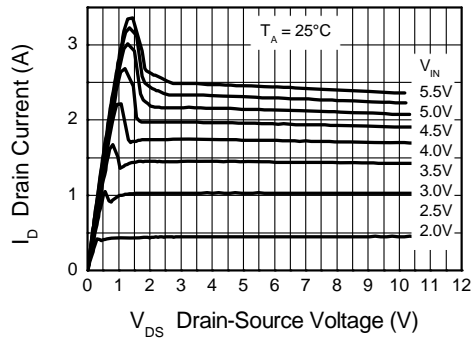
- (g) The drain current is restricted only when the device is in saturation (see graph 'typical output characteristic'). This allows the device to be used in the fully on state without interference from the current limit. The device is fully protected at all drain currents, as the low power dissipation generated outside saturation makes current limit unnecessary.

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	CONDITIONS
<b>Over-temperature Protection</b>						
Thermal Overload Trip Temperature (h)	T <sub>JT</sub>	150	175		°C	
Thermal hysteresis (h)			10		°C	

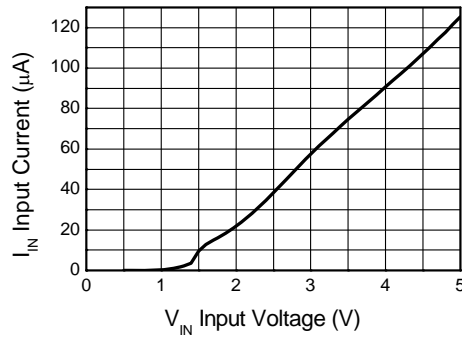
Note:

- (h) Over-temperature protection is designed to prevent device destruction under fault conditions. Fault conditions are considered as “outside” normal operating range, so this part is not designed to withstand over-temperature for extended periods..

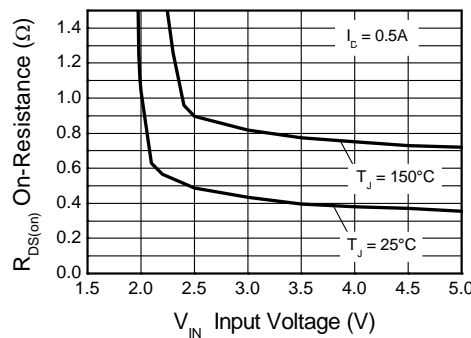
## TYPICAL CHARACTERISTICS



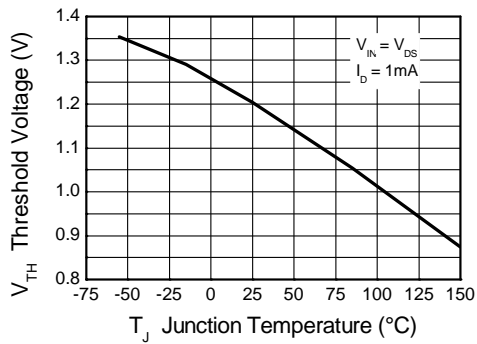
Typical Output Characteristic



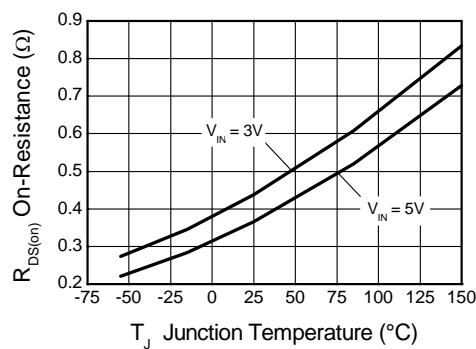
Input Current vs Input Voltage



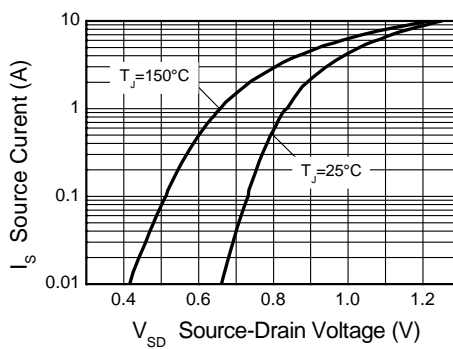
On-Resistance vs Input Voltage



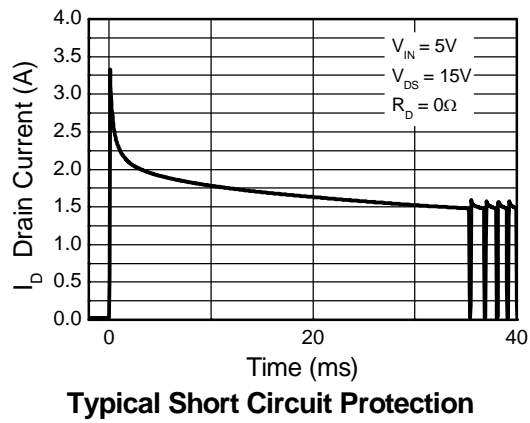
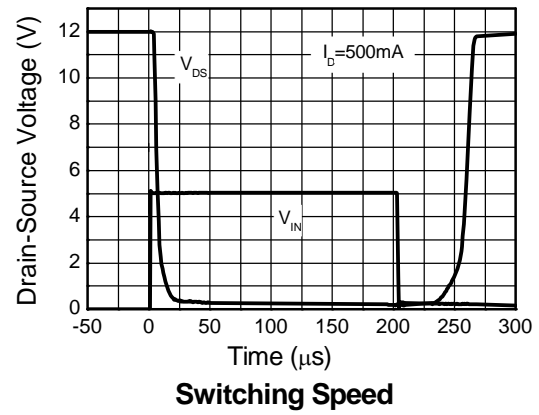
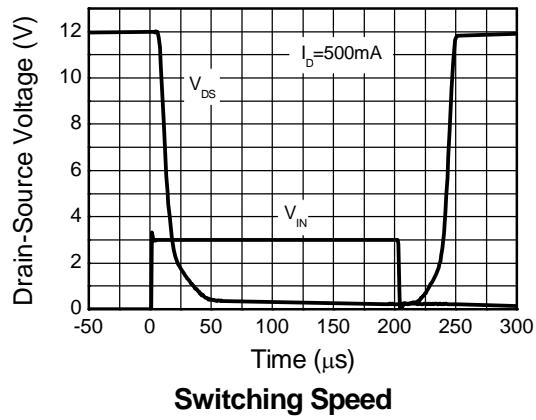
Threshold Voltage vs Temperature



On-Resistance vs Temperature



Reverse Diode Characteristic





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