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Features

- ♦ Glass passivated chip junction
- ♦ High efficiency, Low VF
- ♦ High current capability
- ♦ High reliability
- High surge current capability
- For use in low voltage, high frequency inventor, free wheeling, and polarity protection application.
- Green compound with suffix "G" on packing code & prefix "G" on datecode

Mechanical Data

- Epoxy: UL 94V-0 rate flame retardant
- Lead: Pure tin plated, lead free, solderable per MIL-STD-202, Method 208 guaranteed
- ♦ Polarity: Color band denotes cathode
- → High temperature soldering guaranteed: 260°C/10s /.375", (9.5mm) lead lengths at 5 lbs, (2.3kg) tension
- ♦ Mounting position: Any
- ♦ Weight: 0.20 grams

Maximum Ratings and Electrical Characteristics

Rating at 25 °C ambient temperature unless otherwise specified.

Single phase, half wave, 60 Hz, resistive or inductive load.

For capacitive load, derate current by 20%

1.0 (25.4) MIN. .107 (2.7) .080 (2.0) DIA. .130 (3.3) .118 (3.0) 1.0 (25.4) MIN. .130 (3.4) MIN.

1.0AMP Glass Passivated High Efficient Rectifiers

TS-1

Dimensions in inches and (millimeters)

Marking Diagram HT1XG = Specific Device Code G = Green Compound Y = Year HT1XG SGYM M = Work Month

Type Number	Symbo I	HT 11G	HT 12G	HT 13G	HT 14G	HT 15G	HT 16G	HT 17G	HT 18G	Units
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	300	400	600	800	1000	V
Maximum RMS Voltage	V_{RMS}	35	70	140	210	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	300	400	600	800	1000	V
Maximum Average Forward Rectified Current .375 (9.5mm) Lead Length @ T_A =55 $^{\circ}$ C	I _{F(AV)}	1								Α
Peak Forward Surge Current, 8.3 ms Single Half Sinewave Superimposed on Rated Load (JEDEC method)	I _{FSM}	30								Α
Maximum Instantaneous Forward Voltage (Note 1) @ 1 A	V _F	1.0 1.3				1.7			V	
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	I _R	5 150							uA uA	
Maximum Reverse Recovery Time (Note 2)	Trr	50				75			nS	
Typical Junction Capacitance (Note 3)	Cj	15				10			pF	
Typical Thermal Resistance (Note 4)	$R_{\theta JA}$	95							°C/W	
Operating Temperature Range	TJ	- 65 to + 150							οС	
Storage Temperature Range	T _{STG}	- 65 to + 150							οС	

Note 1: Pulse Test with PW=300 usec, 1% Duty Cycle

Note 2: Reverse Recovery Test Conditions: IF=0.5A, IR=1.0A, IRR=0.25A

Note 3: Measured at 1 MHz and Applied Reverse Voltage of 4.0V D.C.

Note 4: Mount on Cu-Pad Size 5mm x 5mm on PCB

Version:C10



RATINGS AND CHARACTERISTIC CURVES (HT11G THRU HT18G)















