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Overview



Part Number: VSC8490EV

VSC8490 Evaluation Board ☆

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Overview

The VSC8489/8490/8491 evaluation board (EVB) enables the user to observe the signal quality of the SFI I/Os through SMA connectors. AC-coupling caps serves as jumpers to switch the 10 G signals between SMAs and the SFP+ connector. The (R)XAUI side can be looped back through the VSC3316 crosspoint. The VSC3316 can optionally route the (R)XAUI signals to SMA connectors for external connections. A smaller VSC3308 crosspoint facilitates routing of multiple input and output clock signals.

For device variants that support IEEE 1588 and MACsec, the EVB can be used to demonstrate simple features. For IEEE 1588 these basic features include load/save of the time to the LTC, read the egress timestamp (TS) through MDIO or 1588_SPI and place the TS into the packet in one-step mode.

A GUI is provided for the user to control the EVB from a PC via a USB cable. The GUI allows the user to configure devices, access registers, and run scripts. The GUI may be used without an EVB in a demonstration mode to provide an introduction to the device features. An external 5 V power supply is required to power to the EVB.

All Application Notes



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