

# MOSFET – Power, N-Channel, SUPERFET® III, Easy Drive

**650 V, 75 A, 29 mΩ**

**FCH029N65S3**

## Description

SUPERFET III MOSFET is onsemi's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provides superior switching performance, and withstand extreme dv/dt rate. Consequently, SUPERFET III MOSFET Easy drive series helps manage EMI issues and allows for easier design implementation.

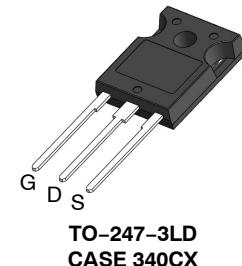
## Features

- 700 V @  $T_J = 150^\circ\text{C}$
- Typ.  $R_{DS(on)} = 23.7 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ.  $Q_g = 201 \text{ nC}$ )
- Low Effective Output Capacitance (Typ.  $C_{oss(\text{eff.})} = 1615 \text{ pF}$ )
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

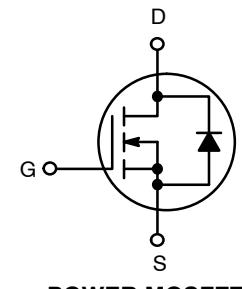
## Applications

- Telecom / Server Power Supplies
- Industrial Power Supplies
- UPS / Solar

$V_{DSS}$	$R_{DS(\text{ON}) \text{ MAX}}$	$I_D \text{ MAX}$
650 V	29 mΩ @ 10 V	75 A



TO-247-3LD  
CASE 340CX



POWER MOSFET

## MARKING DIAGRAM



\$Y = onsemi Logo  
 &Z = Assembly Plant Code  
 &3 = Data Code (Year & Week)  
 &K = Lot  
 FCH029N65S3 = Specific Device Code

## ORDERING INFORMATION

See detailed ordering and shipping information on page 8 of this data sheet.

ABSOLUTE MAXIMUM RATINGS ( $T_C = 25^\circ\text{C}$ , Unless otherwise noted)

Symbol	Parameter		Value	Unit
$V_{DSS}$	Drain to Source Voltage		650	V
$V_{GSS}$	Gate to Source Voltage	– DC	±30	V
		– AC ( $f > 1 \text{ Hz}$ )	±30	
$I_D$	Drain Current	– Continuous ( $T_C = 25^\circ\text{C}$ )	75	A
		– Continuous ( $T_C = 100^\circ\text{C}$ )	50.8	
$I_{DM}$	Drain Current	– Pulsed (Note 1)	200	A
$E_{AS}$	Single Pulsed Avalanche Energy (Note 2)		503	mJ
$I_{AS}$	Avalanche Current (Note 2)		11.5	A
$E_{AR}$	Repetitive Avalanche Energy (Note 1)		4.63	mJ
$dv/dt$	MOSFET $dv/dt$		100	V/ns
	Peak Diode Recovery $dv/dt$ (Note 3)		20	
$P_D$	Power Dissipation	( $T_C = 25^\circ\text{C}$ )	463	W
		– Derate Above $25^\circ\text{C}$	3.7	W/ $^\circ\text{C}$
$T_J, T_{STG}$	Operating and Storage Temperature Range		–55 to +150	$^\circ\text{C}$
$T_L$	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds		300	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive rating: pulse width limited by maximum junction temperature.
2.  $I_{AS} = 11.5 \text{ A}$ ,  $R_G = 25 \Omega$ , starting  $T_J = 25^\circ\text{C}$ .
3.  $I_{SD} \leq 37.5 \text{ A}$ ,  $di/dt \leq 200 \text{ A}/\mu\text{s}$ ,  $V_{DD} \leq 400 \text{ V}$ , starting  $T_J = 25^\circ\text{C}$ .

## THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.27	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	40	

ELECTRICAL CHARACTERISTICS ( $T_C = 25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
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## OFF CHARACTERISTICS

$BV_{DSS}$	Drain to Source Breakdown Voltage	$V_{GS} = 0 \text{ V}$ , $I_D = 1 \text{ mA}$ , $T_J = 25^\circ\text{C}$	650	–	–	V
		$V_{GS} = 0 \text{ V}$ , $I_D = 1 \text{ mA}$ , $T_J = 150^\circ\text{C}$	700	–	–	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 1 \text{ mA}$ , Referenced to $25^\circ\text{C}$	–	0.72	–	$\text{V}/^\circ\text{C}$
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 650 \text{ V}$ , $V_{GS} = 0 \text{ V}$	–	–	1	$\mu\text{A}$
		$V_{DS} = 520 \text{ V}$ , $T_C = 125^\circ\text{C}$	–	6.2	–	
$I_{GSS}$	Gate to Body Leakage Current	$V_{GS} = \pm 30 \text{ V}$ , $V_{DS} = 0 \text{ V}$	–	–	±100	nA

## ON CHARACTERISTICS

$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$ , $I_D = 7.0 \text{ mA}$	2.5	–	4.5	V
$R_{DS(\text{on})}$	Static Drain to Source On Resistance	$V_{GS} = 10 \text{ V}$ , $I_D = 37.5 \text{ A}$	–	23.7	29	$\text{m}\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS} = 20 \text{ V}$ , $I_D = 37.5 \text{ A}$	–	48	–	S

# FCH029N65S3

## ELECTRICAL CHARACTERISTICS ( $T_C = 25^\circ\text{C}$ unless otherwise noted) (continued)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
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### DYNAMIC CHARACTERISTICS

$C_{iss}$	Input Capacitance	$V_{DS} = 400\text{ V}$ , $V_{GS} = 0\text{ V}$ , $f = 1\text{ MHz}$	-	6340	-	pF
$C_{oss}$	Output Capacitance		-	166	-	pF
$C_{oss(\text{eff.})}$	Effective Output Capacitance	$V_{DS} = 0\text{ V}$ to $400\text{ V}$ , $V_{GS} = 0\text{ V}$	-	1615	-	pF
$C_{oss(\text{er.})}$	Energy Related Output Capacitance	$V_{DS} = 0\text{ V}$ to $400\text{ V}$ , $V_{GS} = 0\text{ V}$	-	287	-	pF
$Q_{g(\text{tot})}$	Total Gate Charge at 10 V	$V_{DS} = 400\text{ V}$ , $I_D = 37.5\text{ A}$ , $V_{GS} = 10\text{ V}$ (Note 4)	-	201	-	nC
$Q_{gs}$	Gate to Source Gate Charge		-	46	-	nC
$Q_{gd}$	Gate to Drain "Miller" Charge		-	81	-	nC
ESR	Equivalent Series Resistance	$f = 1\text{ MHz}$	-	0.85	-	$\Omega$

### SWITCHING CHARACTERISTICS

$t_{d(\text{on})}$	Turn-On Delay Time	$V_{DD} = 400\text{ V}$ , $I_D = 37.5\text{ A}$ , $V_{GS} = 10\text{ V}$ , $R_g = 2\Omega$ (Note 4)	-	35	-	ns
$t_r$	Turn-On Rise Time		-	49	-	ns
$t_{d(\text{off})}$	Turn-Off Delay Time		-	120	-	ns
$t_f$	Turn-Off Fall Time		-	29.5	-	ns

### SOURCE-DRAIN DIODE CHARACTERISTICS

$I_S$	Maximum Continuous Drain to Source Diode Forward Current	-	-	75	A	
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	200	A	
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{ V}$ , $I_{SD} = 37.5\text{ A}$	-	-	1.2	V
$t_{rr}$	Reverse Recovery Time	$V_{DD} = 400\text{ V}$ , $I_{SD} = 37.5\text{ A}$ , $dl_F/dt = 100\text{ A}/\mu\text{s}$	-	516	-	ns
$Q_{rr}$	Reverse Recovery Charge		-	12.2	-	$\mu\text{C}$

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

## TYPICAL CHARACTERISTICS

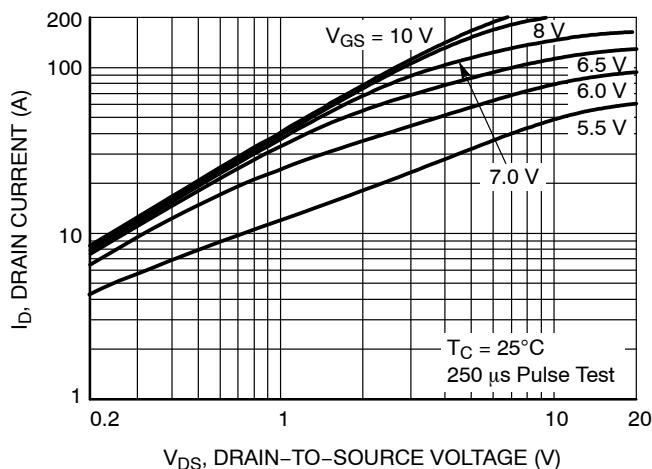


Figure 1. On-Region Characteristics

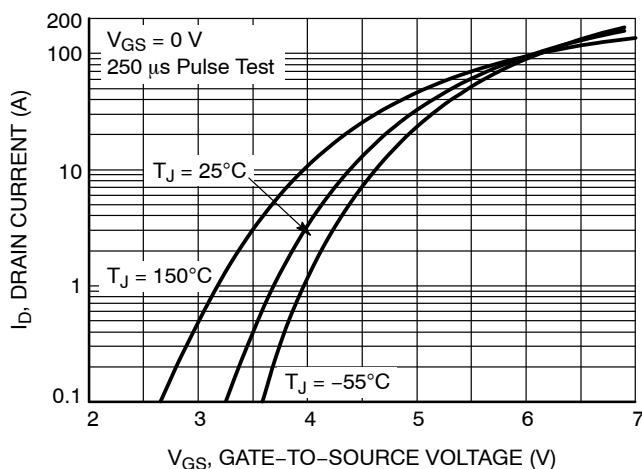


Figure 2. Transfer Characteristics

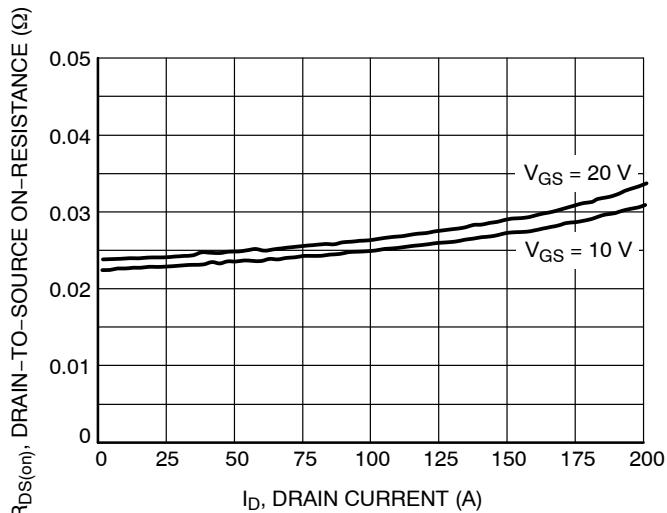


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

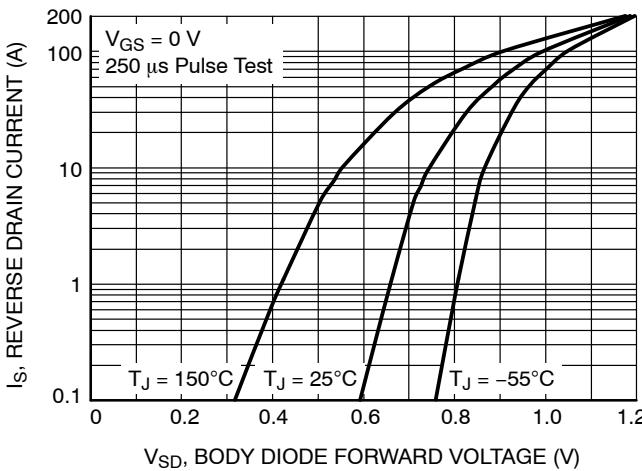


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

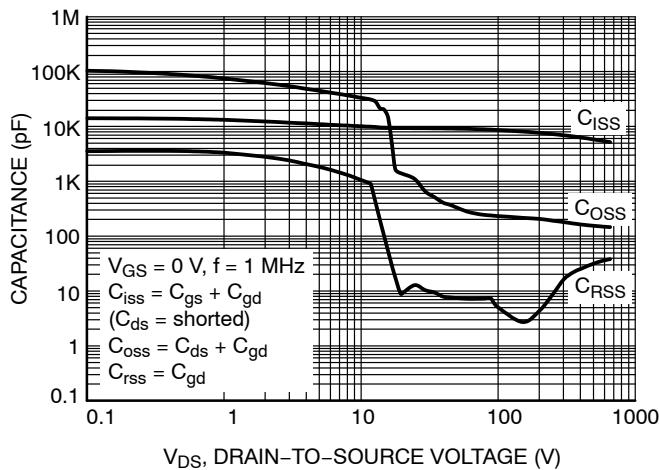


Figure 5. Capacitance Characteristics

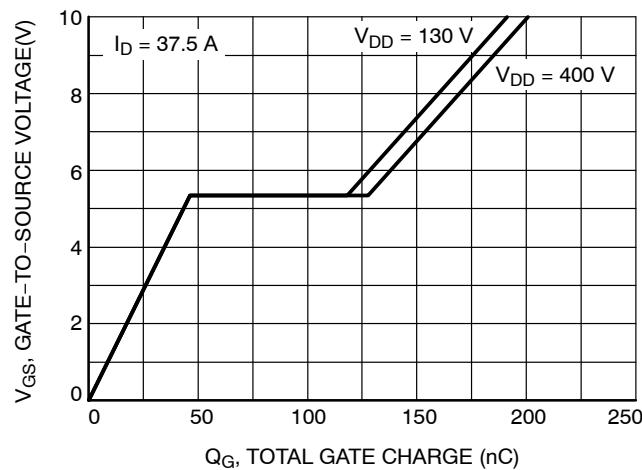
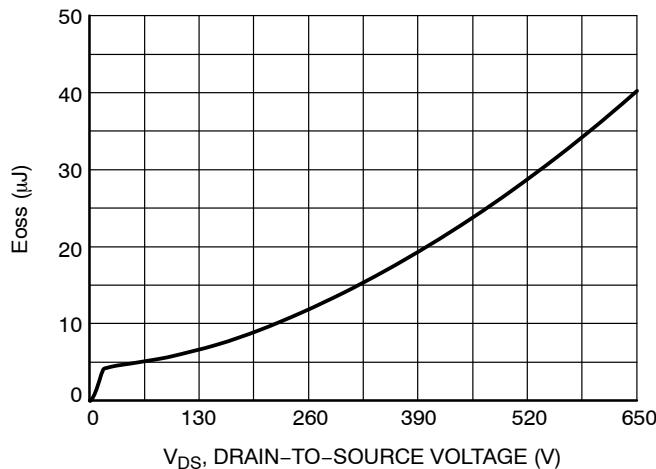
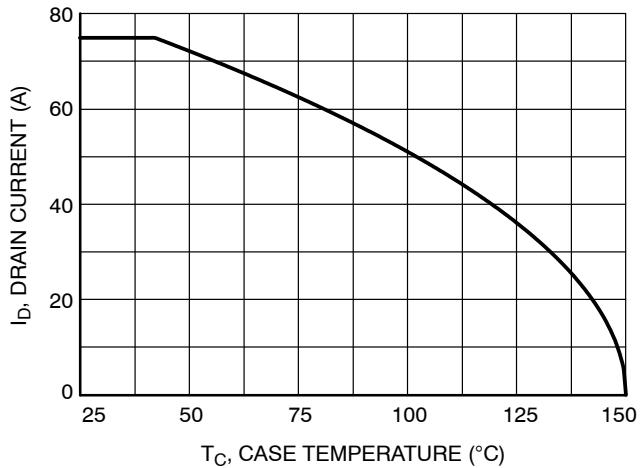
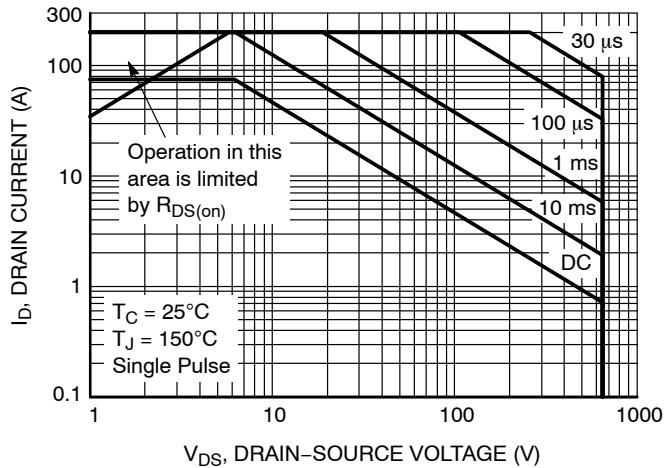
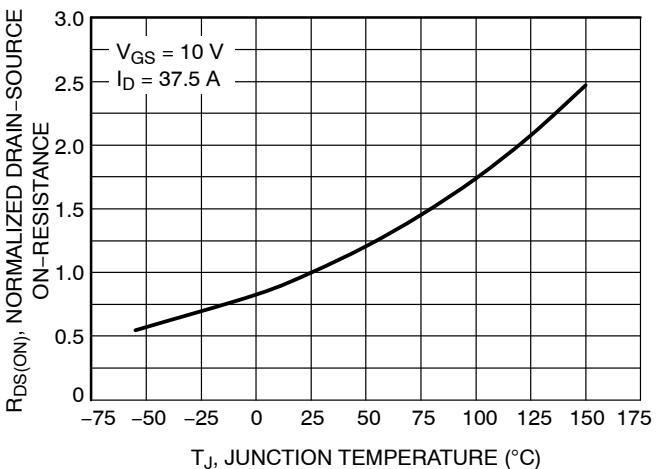
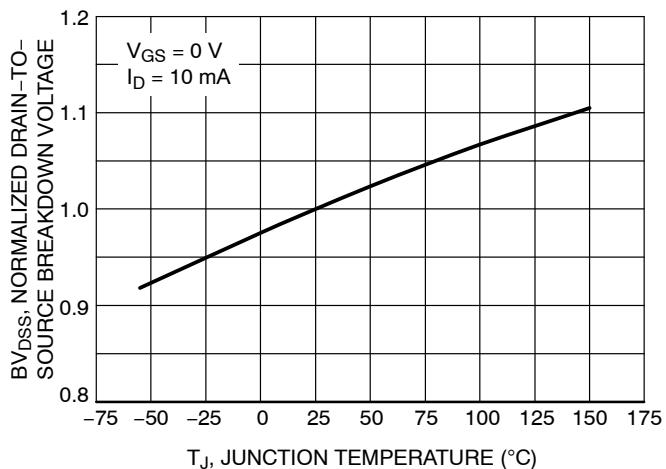


Figure 6. Gate Charge Characteristics

## TYPICAL CHARACTERISTICS



## TYPICAL CHARACTERISTICS

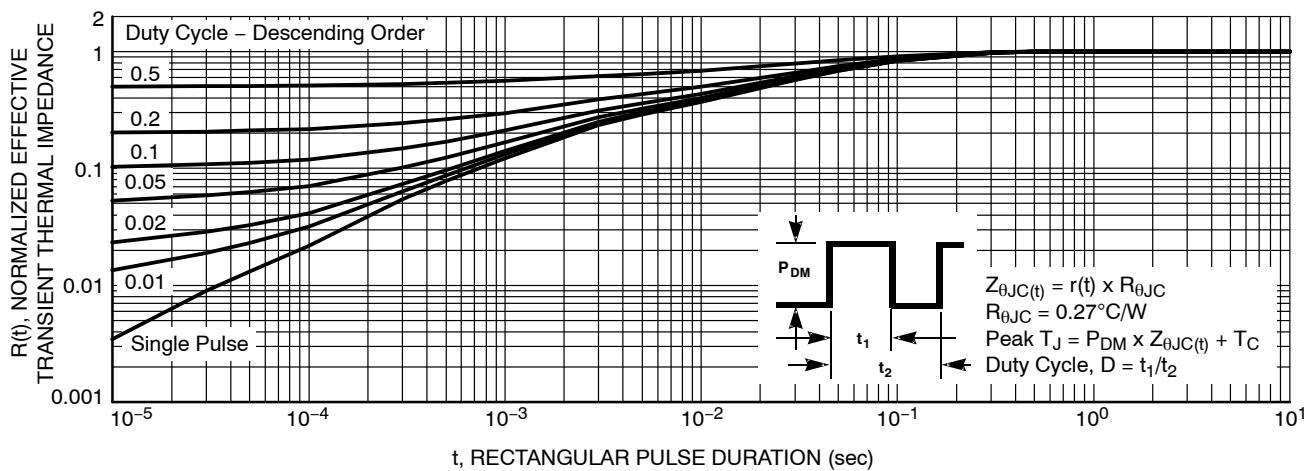


Figure 12. Transient Thermal Response Curve

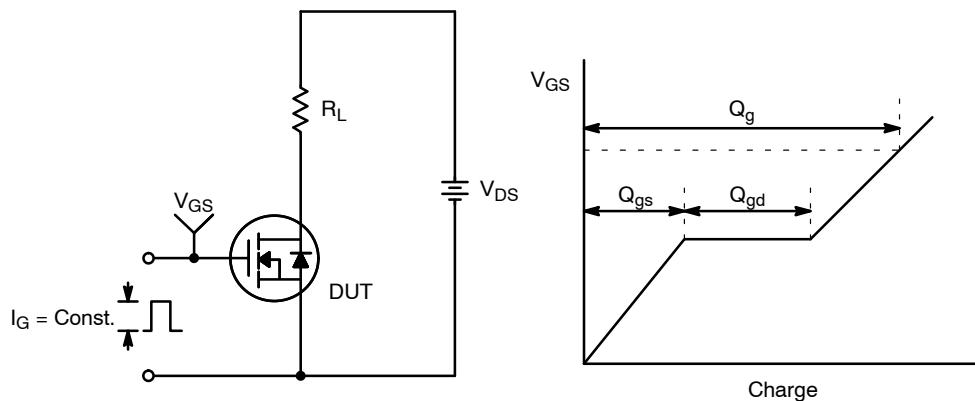


Figure 13. Gate Charge Test Circuit & Waveform

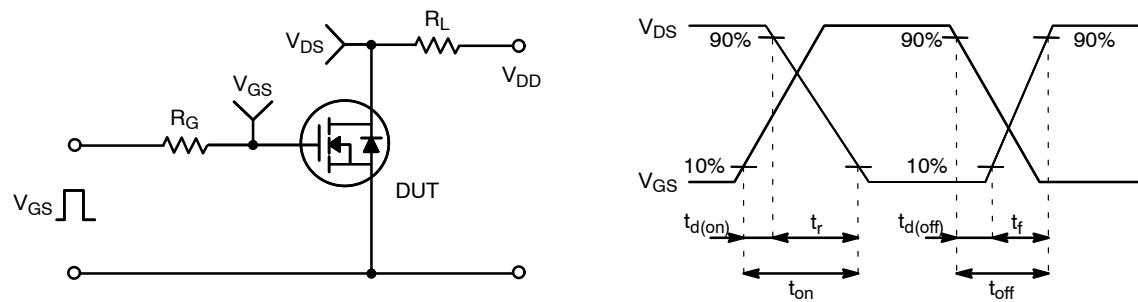


Figure 14. Resistive Switching Test Circuit & Waveforms

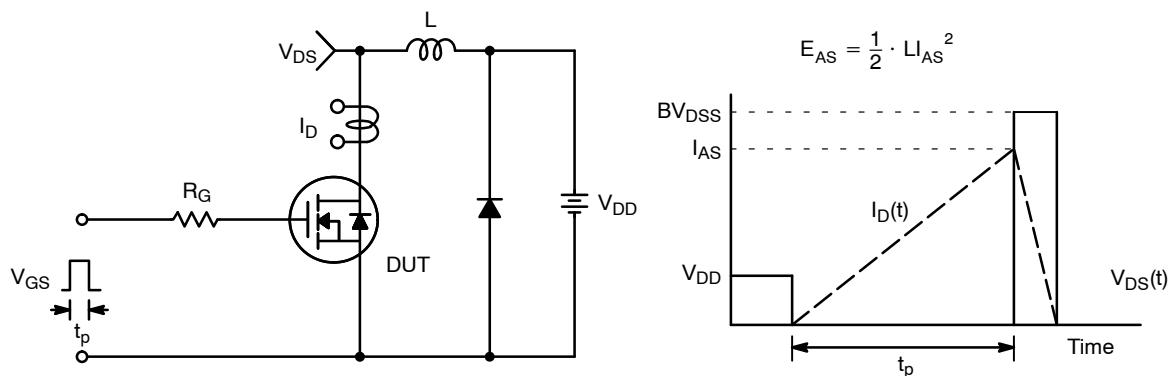


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

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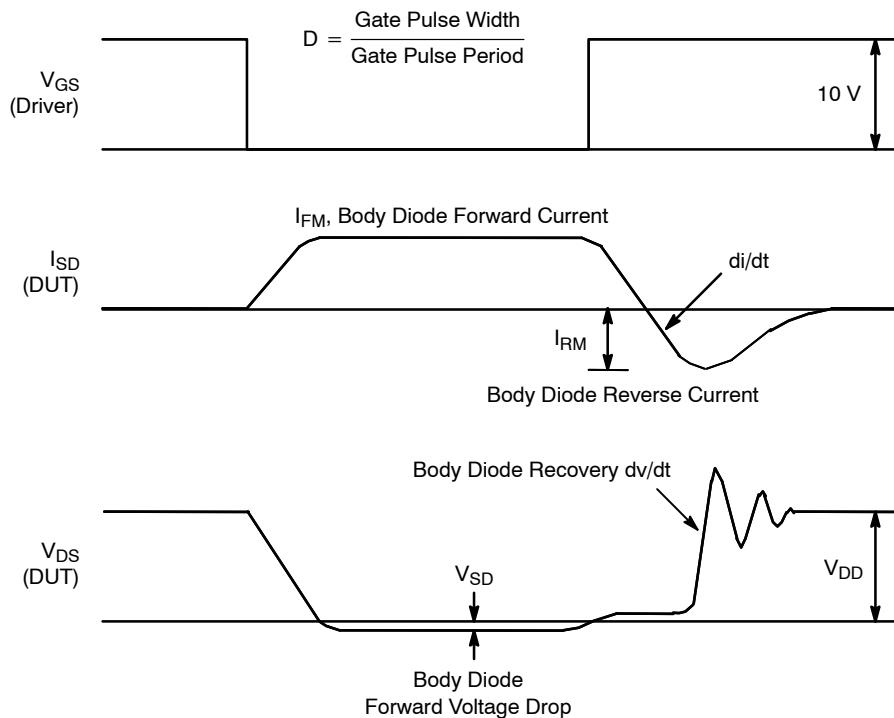
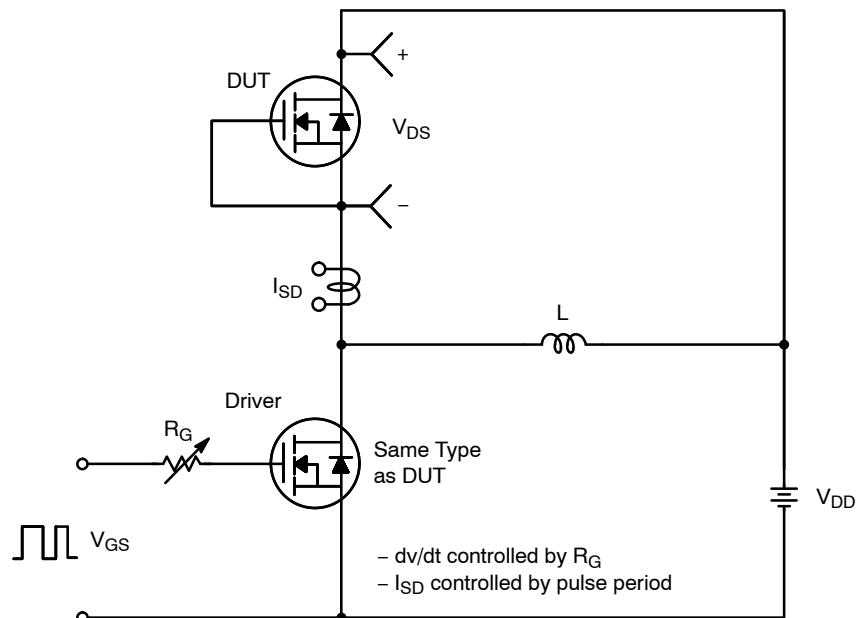
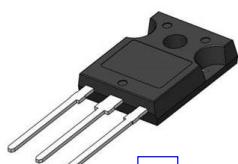


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

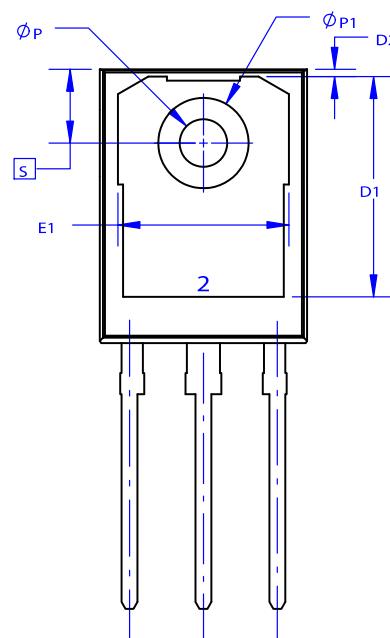
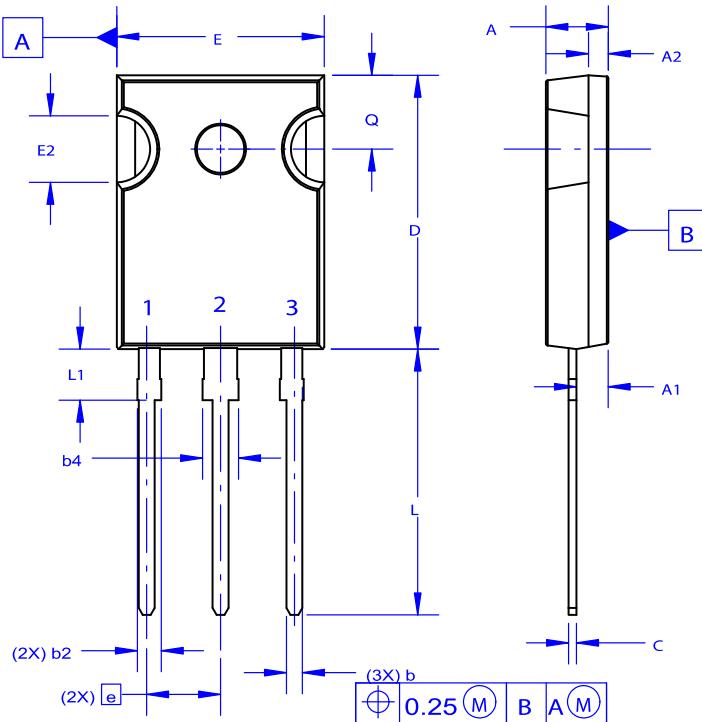
## PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
FCH029N65S3-F155	FCH029N65S3	TO-247 G03	Tube	N/A	N/A	30 Units

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TO-247-3LD  
CASE 340CX  
ISSUE A

DATE 06 JUL 2020



## NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 - 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.58	4.70	4.82
A1	2.20	2.40	2.60
A2	1.40	1.50	1.60
D	20.32	20.57	20.82
E	15.37	15.62	15.87
E2	4.96	5.08	5.20
e	~	5.56	~
L	19.75	20.00	20.25
L1	3.69	3.81	3.93
ØP	3.51	3.58	3.65
Q	5.34	5.46	5.58
S	5.34	5.46	5.58
b	1.17	1.26	1.35
b2	1.53	1.65	1.77
b4	2.42	2.54	2.66
c	0.51	0.61	0.71
D1	13.08	~	~
D2	0.51	0.93	1.35
E1	12.81	~	~
ØP1	6.60	6.80	7.00

GENERIC  
MARKING DIAGRAM\*

XXXXX = Specific Device Code  
 A = Assembly Location  
 Y = Year  
 WW = Work Week  
 G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

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