TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7PA14FU

Dual Schmitt Inverter

Features

- Operating voltage range: V_{CC} = 1.8 to 3.6 V
- High-speed operation: t_{pd} = 4.0 ns (max) at V_{CC} = 3.0 to 3.6 V

 t_{pd} = 4.3 ns (max) at V_{CC} = 2.3 to 2.7 V

 t_{pd} = 8.6 ns (max) at V_{CC} = 1.8 V

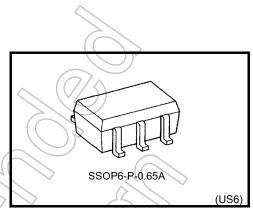
· High-level output current:

 I_{OH}/I_{OL} = ±24 mA (min) at V_{CC} = 3.0 V

 I_{OH}/I_{OL} = ±18 mA (min) at V_{CC} = 2.3 V

 I_{OH}/I_{OL} = ±6 mA (min) at V_{CC} = 1.8 V

- 3.6-V tolerant inputs.
- 3.6-V power down protection outputs



Weight: 0.0068 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	-0.5 to 4.6	V
DC input voltage	VIN	-0.5 to 4.6	7/h
DC output voltage	(7/	-0.5 to 4.6 (Note 1)	> v
DC output voltage	Vout	-0.5 to V _{CC} + 0.5 (Note 2)	√ V
Input diode current		-50	mA
Output diode current	lok	-50 (Note 3)	mA
DC output current	JOUT	±50	mA
Power dissipation	P_{D}	200	mW
DC V _{CC} /ground current	Icc	±100	mA
Storage temperature	T _{stg}	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

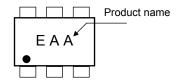
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0 V$

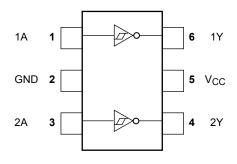
Note 2: High or Low State. IOUT absolute maximum rating must be observed.

Note 3: V_{OUT} < GND

Marking



Pin Assignment (top view)



Start of commercial production 2002-12

IEC Logic Symbol



Truth Table

А	Y
L	Н
Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	1.8 to 3.6	W
Supply voltage	VCC	1.2 to 3.6 (Note 4)) y
Input voltage	V _{IN}	-0.3 to 3.6	V
Output voltage	V _{OUT}	0 to 3.6 (Note 5)	V
Culput voltage	VO01	0 to V _{CC} (Note 6)	V
		±24 (Note 7)	\Diamond
Output current	I _{OH} /I _{OL}	±18 (Note 8)	mA
		±6 (Note 9)	
Operating temperature	T _{opr}	-40 to 85	·e/

Note 4: Data retention only

Note 5: $V_{CC} = 0 V$

Note 6: High or Low state

Note 7: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 8: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 9: $V_{CC} = 1.8 \text{ V}$



Electrical Characteristics

DC Characteristics (2.7 V < V_{CC} \le 3.6 V)

Characteristics		Symbol Test Condition		Ta = 40 to 85°C		to 85°C	Unit	
Characteris	Sucs	Syllibol	rest condition		V _{CC} (V)	Min	Max	Offic
High level		V _P			3.6	_	2.2	
Threshold Voltage	riigirievei	VP		_	3.0		2.0	V
Tilleshold Voltage	Low level	V _N			3.6	0.8		v
Low level	LOW IEVEI	٧N		_	3.0	0.7		
Hysteresis Voltage		VH			3.6	0.3	1.2	V
Trysteresis voltage		VН			3.0	0.3	1.2	v
н				I _{OH} = -100 μA	2.7 to 3.6	V _{CC} - 0.2	_	
	High level	V _{OH}	$V_{IN} = V_{IL}$	$I_{OH} = -12 \text{ mA}$	2.7	2.2	\rightarrow	
				I _{OH} = -18 mA	3.0	2.4	> -	
Output Voltage				I _{OH} = √24 mA	3.0	2.2	<u> </u>	V
			V _{IN} = V _{IH}	I _{OL} = 100 μA	2.7 to 3.6	74/	0.2	
	Low level	V _{OL}		I _{OL} = 12 mA	2.7	\ <u>\</u>	0.4	
	2011 10101	VOL.		I _{OL} = 18 mA	3.0) —	0.4	
				I _{OL} = 24 mA	3.0	_	0.55	
Input Leakage Current		I _{IN}	$V_{IN} = 0 \text{ to } 3.$	6 V	2.7 to 3.6	_	±5.0	μΑ
Power-off Leakage Curr	ent	loff	V _{IN} , V _{OUT} =	0 to 3.6 V	0	_	10.0	μΑ
Quiescent Supply Current		loo	$V_{IN} = V_{CC}$ or GND		2.7 to 3.6	_	20.0	
Quicocciii ouppiy ourie	III,	Icc	$V_{CC} \le (V_{IN}, V_{OUT}) \le 3.6 \text{ V}$		2.7 to 3.6	_	±20.0	μА
Increase in I _{CC} per Inpu	t	ΔlCC	V _{IH} = V _{CC} -	0.6 0	2.7 to 3.6	_	750	

DC Characteristics (2.3 V ≤ V_{CC} ≤ 2.7 V)

Characteristics		Symbol Test Condition			Ta = 40	to 85°C	Unit	
		Syllibol		est Condition	V _{CC} (V)	Min	Max	Offic
Threshold Voltage High level Low level	High level	VP		_	2.3	_	1.8	V
	V _N		_	2.3	0.5		٧	
Hysteresis Voltage	\mathcal{D}	VH		_	2.3	0.3	1.0	٧
High level				$I_{OH} = -100 \mu A$	2.3 to 2.7	V _{CC} - 0.2		
	High level	Voн	H V _{IN} = V _{IL}	$I_{OH} = -6 \text{ mA}$	2.3	2.0		
				$I_{OH} = -12 \text{ mA}$	2.3	1.8	_	
Output Voltage	~ //			I _{OH} = -18 mA	2.3	1.7	_	V
*		V _{OL}	$V_{IN} = V_{IH}$	$I_{OL} = 100 \mu A$	2.3 to 2.7		0.2	İ
	Low level			$V_{IN} = V_{IH}$	I _{OL} = 12 mA	2.3	_	0.4
				I _{OL} = 18 mA	2.3	_	0.6	
Input Leakage Current		I _{IN}	$V_{IN} = 0 \text{ to } 3$.6 V	2.3 to 2.7	_	±5.0	μΑ
Power-off Leakage Curre	nt	loff	V _{IN} , V _{OUT} =	= 0 to 3.6 V	0	_	10.0	μΑ
Ouissant Supply Current			$V_{IN} = V_{CC}$	or GND	2.3 to 2.7	_	20.0	Δ
Quiescent Supply Curren		I _{CC}	V _{CC} ≤ (V _{IN} ,	V _{OUT}) ≤ 3.6 V	2.3 to 2.7	_	±20.0	μА

DC Characteristics (1.8 V \leq V_{CC} < 2.3 V)

Characteristics Symbo		Cumbal	Tor	Test Condition		Ta = 40 to 85°C		Linit
		Symbol	rest Condition		V _{CC} (V)	Min	Max	Unit
Threshold Voltage	High level	V _P		_	1.8	_	1.4	V
Threshold Voltage	Low level	V _N		_	1.8	0.25		V
Hysteresis Voltage		V _H	_		1.8	0.2	0.95	V
High le	High level	V _{OH}	DH VIN = VIL	I _{OH} = -100 μA	1.8	V _{CC} - 0.2		
				I _{OH} = -6 mA	7.8	1.4		V
	Low lovel	Low level V _{OL}	V_{OI} $V_{IN} = V_{IH}$	I _{OL} = 100 μA	(1.8)	_	0.2	
	Low level			I _{OL} = 6 mA	1.8	_	0.3	
Input Leakage Current		I _{IN}	V _{IN} = 0 to 3.6 V		<u>)</u> 1.8	_	±5.0	μА
Power-off Leakage Curre	ent	l _{OFF}	OFF V _{IN} , V _{OUT} = 0 to 3.6 V		0	4	10.0	μА
Quiescent Supply Current		Icc	V _{IN} = V _{CC} or GND		1.8	170	20.0	μА
Quicocon Supply Curren	111	100	V _{CC} ≤ (V _{IN} , V _{OUT}) ≤ 3.6 V		1.8	5	> ±20.0	μΛ

AC Characteristics (Input $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500$ Ω)

Characteristics	Symbol Test Condition			Ta = 40 to 85°C		Unit
Characteristics	Syllibol	rest condition	V _{CC} (V)	Min	Max	Offic
	t <		1.8	1.0	8.6	
Propagation delay time	t _{pLH}	(Figure 1 and 2)	2.5 ± 0.2	8.0	4.3	ns
	фпц		3.3 ± 0.3	0.6	4.0	

For $C_L = 50$ pF, add approximately 300 ps to the AC maximum specification.



Dynamic Switching Characteristics (Input $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Cumbal	Test Co		Ta = 25°C	Lloit	
Characteristics	Symbol	Test Co	oridition	V _{CC} (V)	Тур	Unit
		V _{IN} = 1.8 V, V _{IL} = 0 V	(Note 10)	1.8	0.25	
Quiet Output Maximum Dynamic V _{OL}	V _{OLP}	V _{IN} = 2.5 V, V _{IL} = 0 V	(Note 10)	2.5	0.6	ns
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 10)	3.3	0.8	
		$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 10)	1.8	-0.25	
Quiet Output Minimum Dynamic V _{OL}	V_{OLV}	$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 10)	2.5	-0.6	ns
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 10)	3.3	-0.8	
		V _{IN} = 1.8 V, V _{IL} = 0 V	(Note 10)	1.8	1.5	
Quiet Output Minimum Dynamic V _{OH}	V _{OLP}	V _{IN} = 2.5 V, V _{IL} = 0 V	(Note 10)	2.5	1.9	ns
		V _{IN} = 3.3 V, V _{IL} = 0 V	(Note 10)	3.3	2.2	

Note 10: Characteristics guaranteed by design.

Capacitive Characteristics

Characteristics	Symbol	Test Condition		Ta = 25°C	Unit
Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур	Offic
Input Capacitance	C _{IN}		1.8, 2.5, 3.3	4	pF
Power Dissipation Capacitance	C_{PD}	f _N = 10 MHz	(Note 11) 1.8, 2.5, 3.3	27	pF

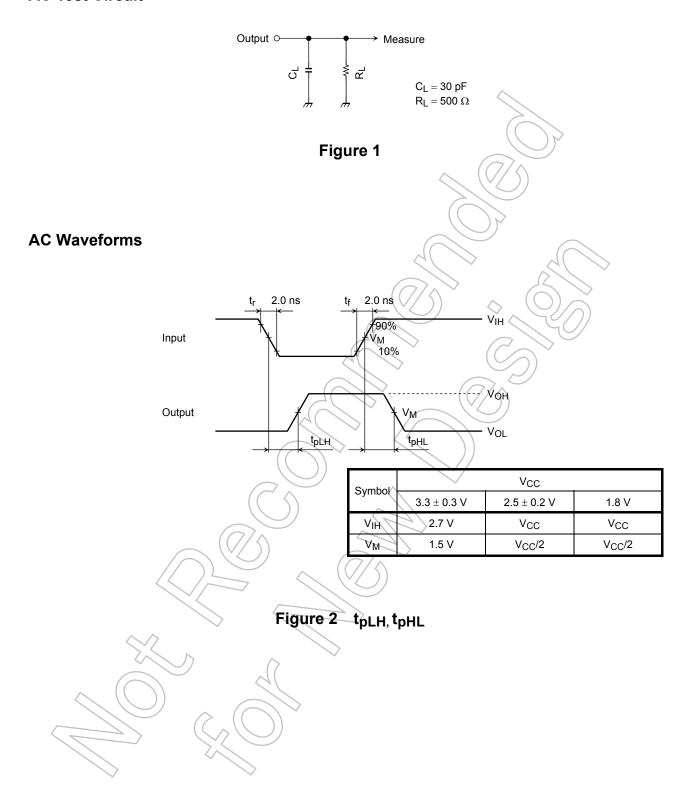
Note 11: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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Average operating current can be obtained by the equation:

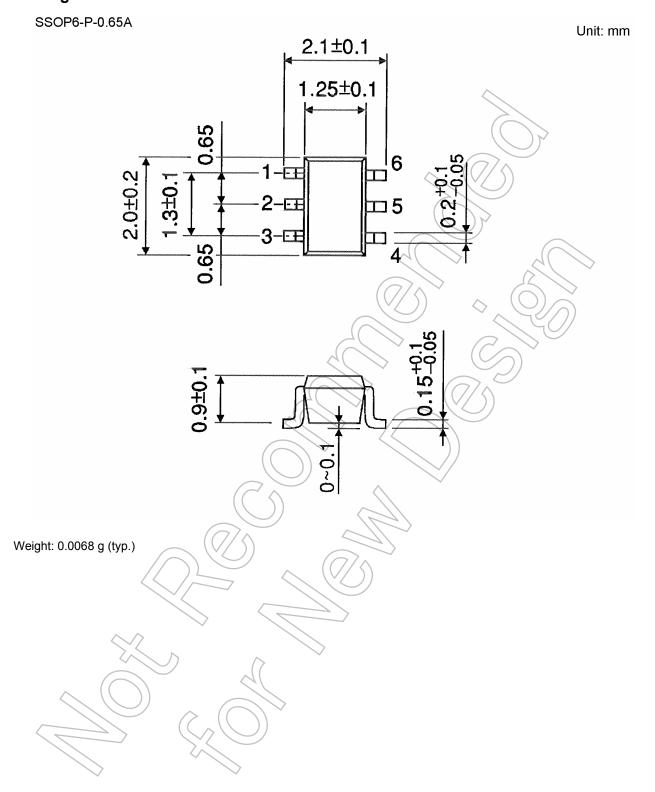
 $I_{CC \text{ (opr.)}} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

AC Test Circuit





Package Dimensions



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