



Intel® Xeon® Platinum 8354H Processor
24.75M Cache, 3.10 GHz

Specifications

Ordering and Compliance

Ordering and spec information

Intel® Xeon® Platinum 8354H Processor (24.75M Cache, 3.10 GHz) FC-LGA14A, Tray

Spec Code	SRK5Y
Ordering Code	CD8070604481002
Shipping Media	TRAY
Stepping	A1
Recommended Customer Price	\$3500.00

Trade compliance information

ECCN	5A992C
CCATS	G077159
US HTS	8542310001

PCN/MDDS Information

SRK5Y

99A677	PCN	MDDS
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Product Images

Compatible Products

Drivers and Software

Technical Documentation

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Refer to Datasheet for formal definitions of product properties and features.

‡ This feature may not be available on all computing systems. Please check with the system vendor to determine if your system delivers this feature, or reference the system specifications (motherboard, processor, chipset, power supply, HDD, graphics controller, memory, BIOS, drivers, virtual machine monitor-VMM, platform software, and/or operating system) for feature compatibility. Functionality, performance, and other benefits of this feature may vary depending on system configuration.

Some products can support AES New Instructions with a Processor Configuration update, in particular, i7-2630QM/i7-2635QM, i7-2670QM/i7-2675QM, i5-2430M/i5-2435M, i5-2410M/i5-2415M. Please contact OEM for the BIOS that includes the latest Processor configuration update.

"Announced" SKUs are not yet available. Please refer to the Launch Date for market availability.

Intel processor numbers are not a measure of performance. Processor numbers differentiate features within each processor family, not across different processor families. See <http://www.intel.com/content/www/us/en/processors/processor-numbers.html> for details.

See <http://www.intel.com/content/www/us/en/architecture-and-technology/hyper-threading/hyper-threading-technology.html?wapkw=hyper+threading>

for more information including details on which processors support Intel® HT Technology.

Processors that support 64-bit computing on Intel® architecture require an Intel 64 architecture-enabled BIOS.

System and Maximum TDP is based on worst case scenarios. Actual TDP may be lower if not all I/Os for chipsets are used.

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