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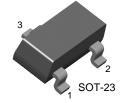
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### KST5086/5087

### **Low Noise Transistor**



### **PNP Epitaxial Silicon Transistor**

1. Base 2. Emitter 3. Collector

## **Absolute Maximum Ratings** $T_a=25$ °C unless otherwise noted

Symbol	Parameter	Value	Units
$V_{CBO}$	Collector-Base Voltage	-50	V
$V_{CEO}$	Collector-Emitter Voltage	-50	V
V <sub>EBO</sub>	Emitter-Base Voltage	-3	V
I <sub>C</sub>	Collector Current	-50	mA
P <sub>C</sub>	Collector Power Dissipation	350	mW
T <sub>STG</sub>	Storage Temperature	150	°C

### $\textbf{Electrical Characteristics} \ \, \textbf{T}_{a} \!\!=\!\! 25^{\circ} \textbf{C} \ \, \text{unless otherwise noted}$

Symbol	Parameter	Test Condition	Min.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = -100μA, I <sub>E</sub> =0	-50		V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -1mA, I <sub>B</sub> =0	-50		V
I <sub>CBO</sub>	Collector Cut-off Current	V <sub>CB</sub> = -20V, I <sub>E</sub> =0		-50	nA
h <sub>FE</sub>	DC Current Gain				
	: KST5086	V <sub>CE</sub> = -5V, I <sub>C</sub> = -100μA	150	500	
	:KST5087		250	800	
	: KST5086	$V_{CE}$ = -5V, $I_{C}$ = -1mA	150		
	: KST5087		250		
	: KST5086	V <sub>CE</sub> = -5V, I <sub>C</sub> = -10mA	150		
	: KST5087		250		
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -10mA, I <sub>B</sub> = -1mA		-0.3	V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	I <sub>C</sub> = -10mA, I <sub>B</sub> = -1mA		-0.85	V
f <sub>T</sub>	Current Gain Bandwidth Product	V <sub>CE</sub> = -5V, I <sub>C</sub> = -500μA f=20MHz	40		MHz
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> = -5V, I <sub>E</sub> =0 f=100MHz		4	pF
NF	Noise Figure				
	: KST5086	$I_{C}$ = -100 $\mu$ A, $V_{CE}$ = -5 $V$		3	dB
	: KST5087	$R_S=3K\Omega$ , $f=1KHz$		2	dB
	: KST5087	V <sub>CE</sub> = -5V, I <sub>C</sub> = -20mA		2	dB
		$R_S=10K\Omega$ , f=10Hz to 15.7KHz			

### **Marking Code**

Туре	KST5086	KST5087
Mark	2P	2Q

Marking



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## **Typical Characteristics**

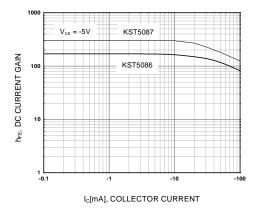


Figure 1. DC current Gain

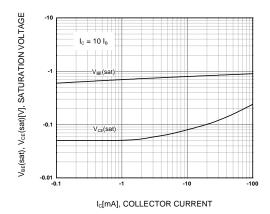


Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

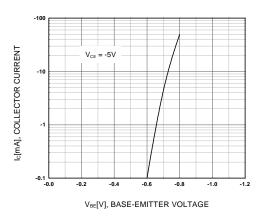


Figure 3. Base-Emitter On Voltage

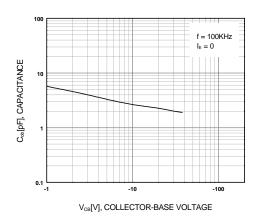


Figure 4. Output Capacitance

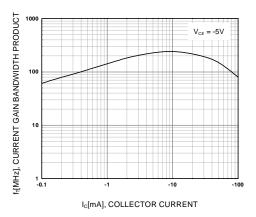
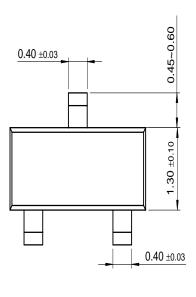


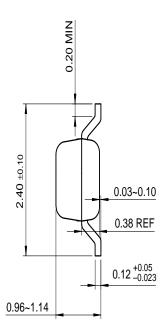
Figure 5. Current Gain Bandwidth Product

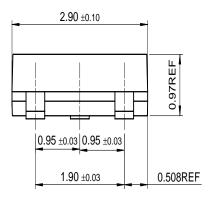
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# **Package Dimensions**

## **SOT-23**







Dimensions in Millimeters

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EnSigna™	$I^2C^{TM}$	OCX™	RapidConfigure™	UHC™ _
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Programmable Active Droop™		OPTOPLANAR™	SMART START™	

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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