DATA SHEET

Version 0.5 (2012-05-21)

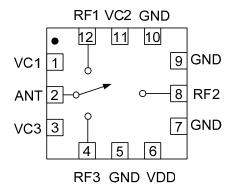
RDASW36 SP3T Switch for Wireless Communication

RDA SOI CMOS SP3T Switch

Features

- Low Insertion Loss
- Low Harmonics
- ESD Protection at all ports
- SOI CMOS Process

Functional Diagram ¹



Description

RDA's RDASW36 is a SOI CMOS single pole three throw (SP3T) high power switch. The RDASW36 is ideally suited for applications where high power, low insertion loss and small size are required. Typical applications are for handset systems that connect separate transmit and receive functions to a common antenna, as well as other related handset and general purpose applications.

Pin Definition

PIN No.	PIN Name	Description
1	VC1	Control RF Port 1
2	ANT	Antenna Port
3	VC3	Control RF Port 3
4	RF3	RF Port 3
5	GND	Ground
6	VDD	Power Supply
7	GND	Ground
8	RF2	RF Port2
9	GND	Ground
10	GND	Ground
11	VC2	Control RF Port 2
12	RF1	RF Port 1

Absolute Maximum Ratings

Parameter	Absolute
	Maximum
Max Input Power	+30dBm
Operating Voltage	+4.0 V
Operating Temperature	-40°C to +85°C
Storage Temperature	-65°C to +150°C

^{*} Current leakage and performance deterioration may increase at maximum rating conditions.

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