2A, 200V - 600V Super Fast Surface Mount Rectifier

FEATURES

- Glass passivated chip junction
- Ideal for automated placement
- Super fast recovery time for high efficiency
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

APPLICATIONS

- High frequency rectification
- Freewheeling application
- Switching mode converters and inverters in computer and telecommunication.

MECHANICAL DATA

- Case: DO-214AA (SMB)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.110g (approximately)

KEY PARAMETERS		
PARAMETER	**VALUE**	**UNIT**
I_F	2	A
V_{RRM}	200 - 600	V
I_{FSM}	50	A
$T_{J\ MAX}$	150	°C
Package	DO-214AA (SMB)	
Configuration	Single die	

Pb-Free RoHS COMPLIANT HALOGEN FREE



DO-214AA (SMB)



ABSOLUTE MAXIMUM RATINGS (T_A = 25°C unless otherwise noted)					
PARAMETER	**SYMBOL**	**ES2LD**	**ES2LG**	**ES2LJ**	**UNIT**
Marking code on the device		ES2LD	ES2LG	ES2LJ	
Repetitive peak reverse voltage	V_{RRM}	200	400	600	V
Reverse voltage, total rms value	V_{RMS}	140	280	420	V
Forward current	I_F	2			A
Surge peak forward current, 8.3ms single half sine-wave superimposed on rated load	I_{FSM}	50			A
Junction temperature	T_J	- 55 to +150			°C
Storage temperature	T_{STG}	- 55 to +150			°C

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THERMAL PERFORMANCE

PARAMETER	SYMBOL	TYP	UNIT
Junction-to-lead thermal resistance	$R_{\Theta JL}$	35	°C/W
Junction-to-ambient thermal resistance	$R_{\Theta JA}$	80	°C/W
Junction-to-case thermal resistance	$R_{\Theta JC}$	25	°C/W

Thermal Performance Note: Units mounted on PCB (16mm x 16mm Cu pad test board)

ELECTRICAL SPECIFICATIONS (T_A = 25°C unless otherwise noted)

PARAMETER		CONDITIONS	SYMBOL	TYP	MAX	UNIT
Forward voltage[1]	ES2LD	I_F = 2A, T_J = 25°C	V_F	-	0.94	V
	ES2LG			-	1.30	V
	ES2LJ			-	1.70	V
Reverse current @ rated V_R[2]		T_J = 25°C	I_R	-	10	μA
		T_J = 125°C		-	350	μA
Junction capacitance	ES2LD	1MHz, V_R = 4.0V	C_J	25	-	pF
	ES2LG ES2LJ			20	-	pF
Reverse recovery time		I_F = 0.5A, I_R = 1.0A, I_{rr} = 0.25A	t_{rr}	-	35	ns

Notes:

1. Pulse test with PW = 0.3ms
2. Pulse test with PW = 30ms

ORDERING INFORMATION

ORDERING CODE[1]	PACKAGE	PACKING
ES2Lx	DO-214AA (SMB)	3,000 / Tape & Reel

Notes:

1. "x" defines voltage from 200V(ES2LD) to 600V(ES2LJ)

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CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

Fig.1 Forward Current Derating Curve

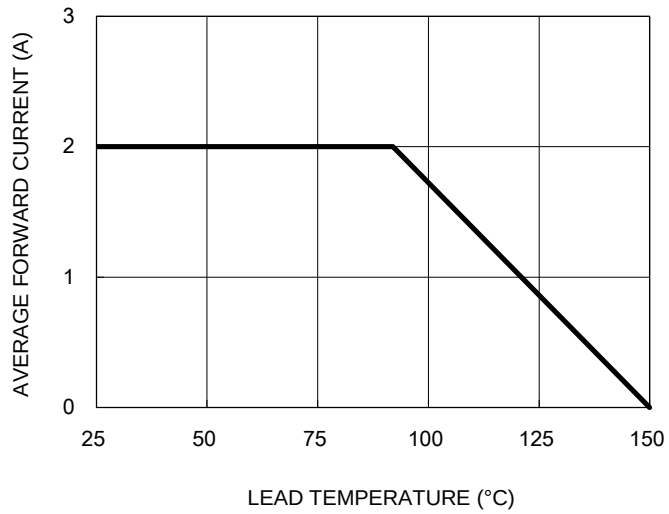


Fig.2 Typical Junction Capacitance

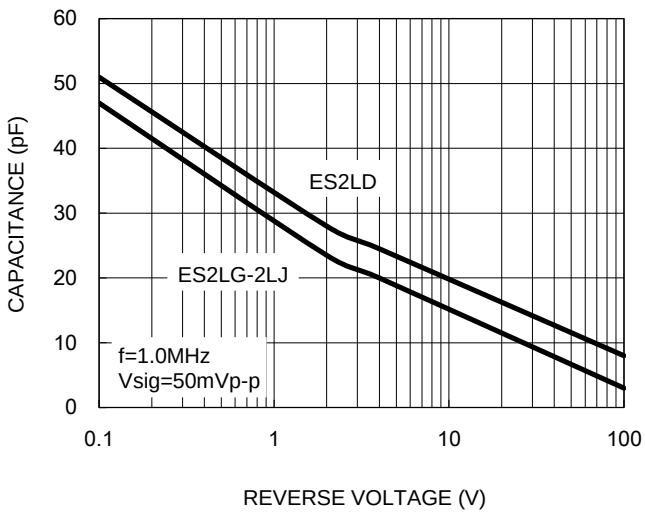


Fig.3 Typical Reverse Characteristics

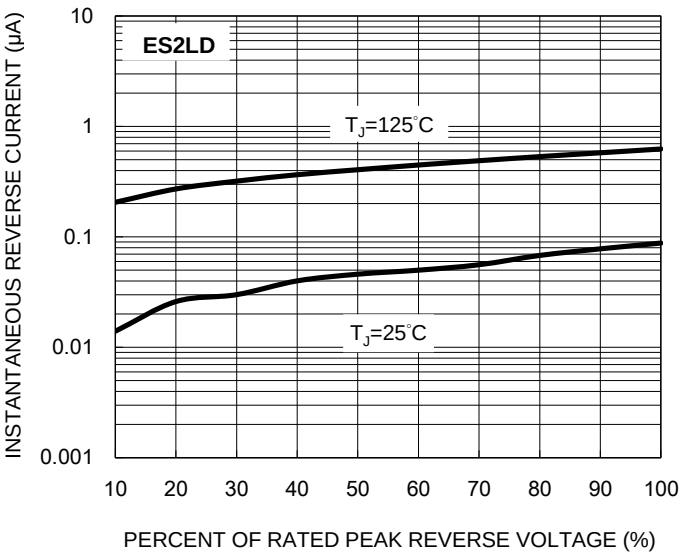
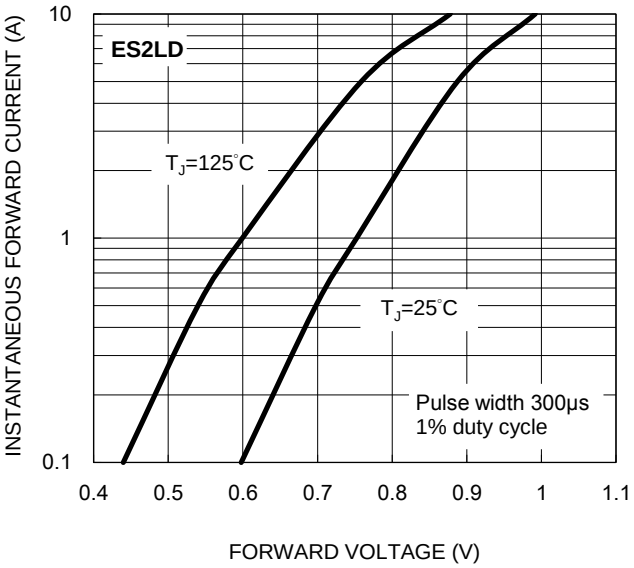


Fig.4 Typical Forward Characteristics



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CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

Fig.5 Typical Reverse Characteristics

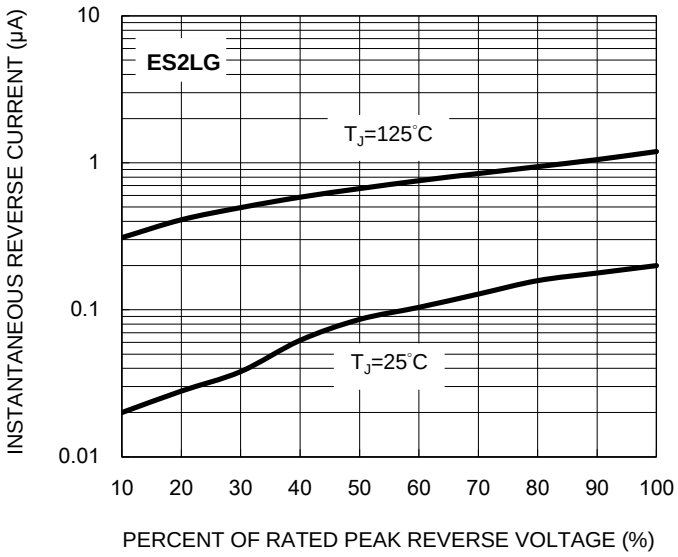


Fig.6 Typical Forward Characteristics

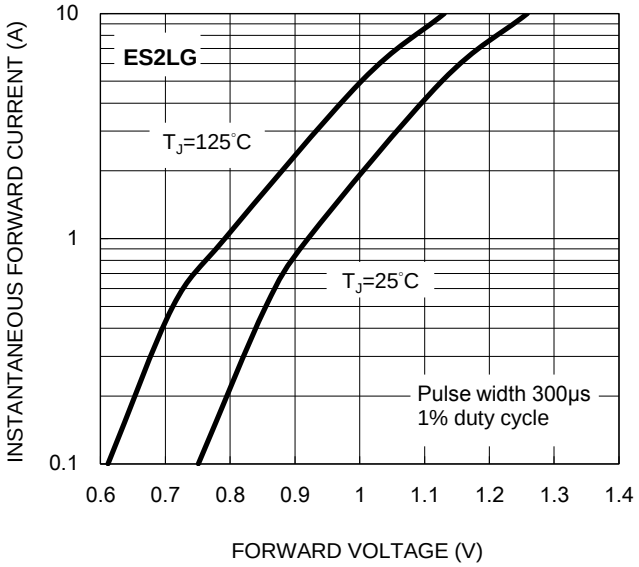


Fig.7 Typical Reverse Characteristics

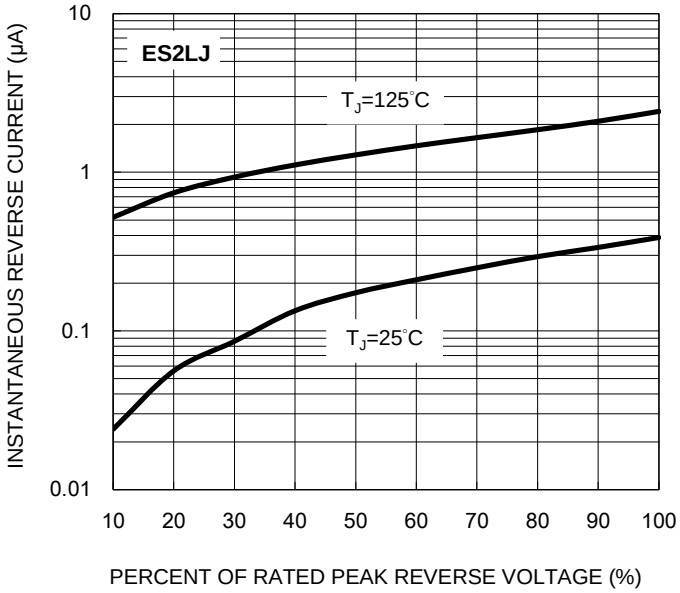
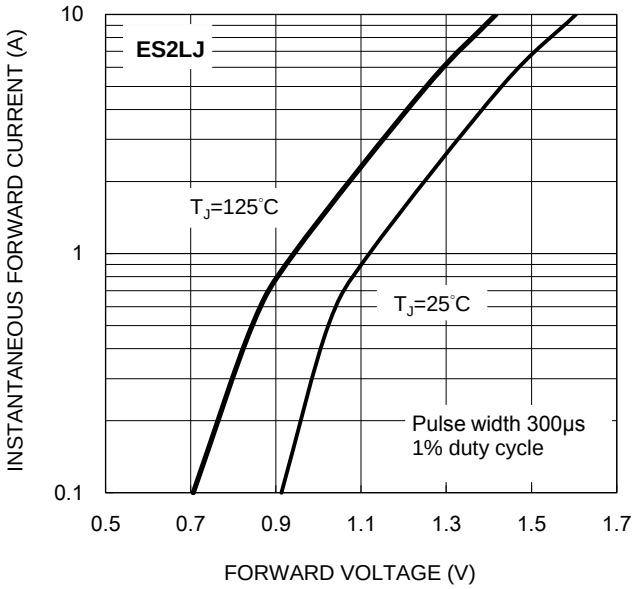


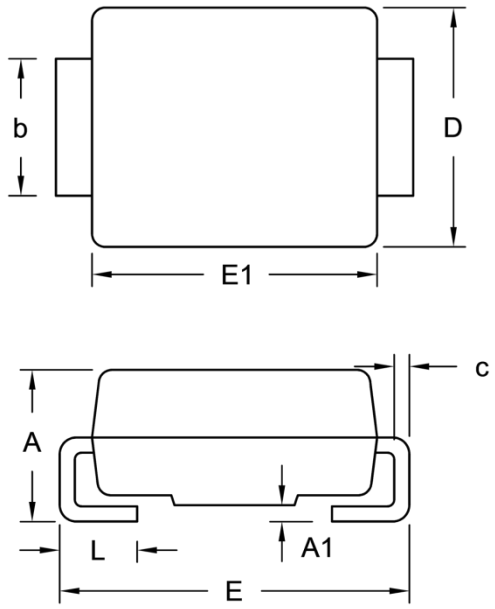
Fig.8 Typical Forward Characteristics



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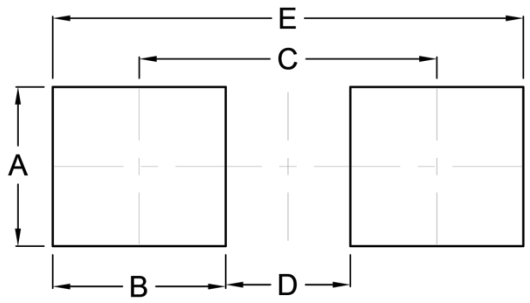
PACKAGE OUTLINE DIMENSIONS

DO-214AA (SMB)



DIM.	Unit (mm)		Unit (inch)	
	Min.	Max.	Min.	Max.
A	1.95	2.65	0.077	0.104
A1	0.05	0.20	0.002	0.008
b	1.95	2.20	0.077	0.087
c	0.15	0.31	0.006	0.012
D	3.30	3.95	0.130	0.156
E	5.10	5.60	0.201	0.220
E1	4.05	4.60	0.159	0.181
L	0.75	1.60	0.030	0.063

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	2.30	0.091
B	2.50	0.098
C	4.30	0.169
D	1.80	0.071
E	6.80	0.268

MARKING DIAGRAM



P/N = Marking Code
G = Green Compound
YW = Date Code
F = Factory Code

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