TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSIII)

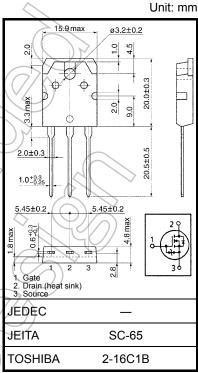
2SK2719

Chopper Regulator, DC-DC Converter and Motor Drive Applications

- Low drain-source ON resistance: $R_{DS}(ON) = 3.7 \Omega$ (typ.)
- High forward transfer admittance: |Y_{fS}| = 2.6 S (typ.)
- Low leakage current: $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 720 \text{ V)}$
- Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	900	$\langle \langle \psi \rangle \rangle$
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	900	<
Gate-source voltage		V_{GSS}	±30	V
Drain current	DC (Note 1)	ΙD	3	A
	Pulse (Note 1)	I _{DP}	9	A
Drain power dissipation	(Tc = 25°C)	PD	125	∠⟨w
Single pulse avalanche energy (Note 2)		Eas	295	mJ
Avalanche current		TAR	3	\ A
Repetitive avalanche en	ergy (Note 3)	EAR	12.5	mJ
Channel temperature	// T _{ch}	150	°C	
Storage temperature ran	ge	T _{stg}	-55 to 150	°C



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	1.0	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	50.0	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 25$ V, $T_{ch} = 25^{\circ}C$ (initial), L = 58 μH , $R_{G} = 25$ Ω , $I_{AR} = 45$ A

Note 3: Repetitive rating: pulse width limited by maximum junction temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

Start of commercial production 2007-10



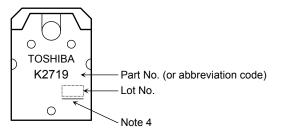
Electrical Characteristics (Ta = 25°C)

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage curr	ent	I _{GSS}	$V_{GS}=\pm 30~V,~V_{DS}=0~V$	_	_	±10	μА
Gate-source break	down voltage	V (BR) GSS	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30	_	_	V
Drain cut-off curre	nt	I _{DSS}	V _{DS} = 720 V, V _{GS} = 0 V	/	_	100	μА
Drain-source brea	kdown voltage	V (BR) DSS	$I_D = 10$ mA, $V_{GS} = 0$ V	900	_	_	V
Gate threshold vol	tage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0))	4.0	V
Drain-source ON r	esistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 1.5 A	7	3.7	4.3	Ω
Forward transfer a	dmittance	Y _{fs}	V _{DS} = 20 V, I _D = 1.5 A	0.65	2.6	_	S
Input capacitance		C _{iss}			750	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	10	_	pF
Output capacitance		Coss		_	70	_	pF
Switching time Fa	Rise time	t _r	V _{GS} 0 V СС ТО СС В В В В В В В В В В В В В В В В В В	- (15	<u> </u>	
	Turn-on time	t _{on}		7	55) —	
	Fall time	t _f	V _{DD} ≈ 200 V		30	_	
	Turn-off time	t _{off}	Duty ≤1%, t _w = 10 μs) –	110	_	
Total gate charge		Qg			25		nC
(gate-source plus gate-drain)			$V_{DD} \simeq 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3 \text{ A}$		20		110
Gate-source charge		Qgs	VDD - 400 V, VGS - 10 V, ID - 3 A	_	13	_	nC
Gate-drain ("miller") charge		Q _{gd}		_	12	_	nC

Source-Drain Diode Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR	-	ı	_	3	А
Pulse drain reverse current (Note 1)	I _{DRP}	_			9	А
Diode forward voltage	VDSF	$I_{DR} = 3 A$, $V_{GS} = 0 V$	_	_	-1.9	V
Reverse recovery time	trr	$I_{DR} = 3 A$, $V_{GS} = 0 V$		1100	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 100 A/μs	_	7.5	_	μС

Marking

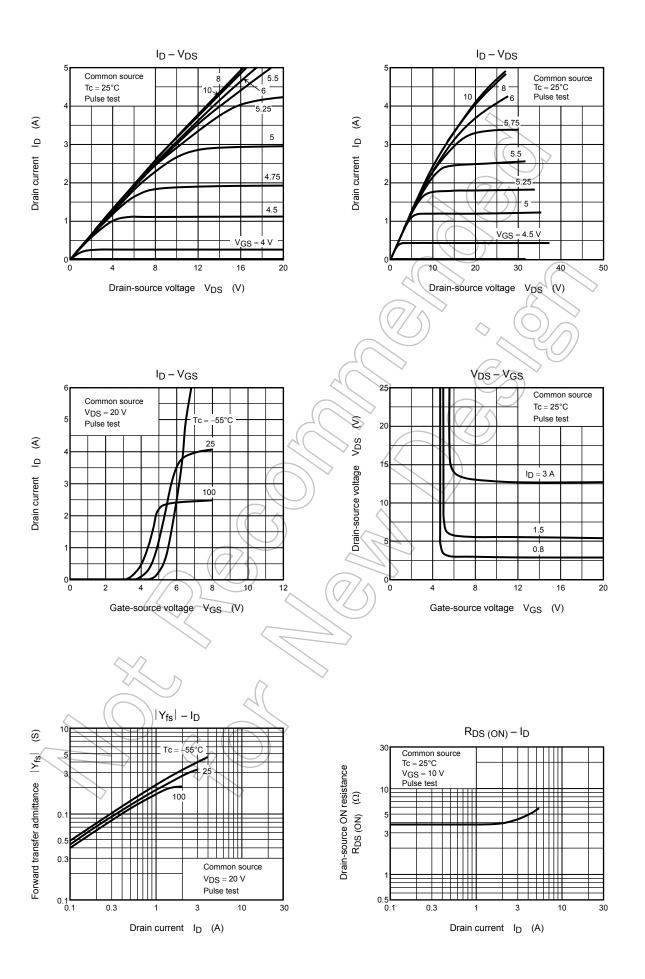


Note 4: A line under a Lot No. identifies the indication of product Labels.

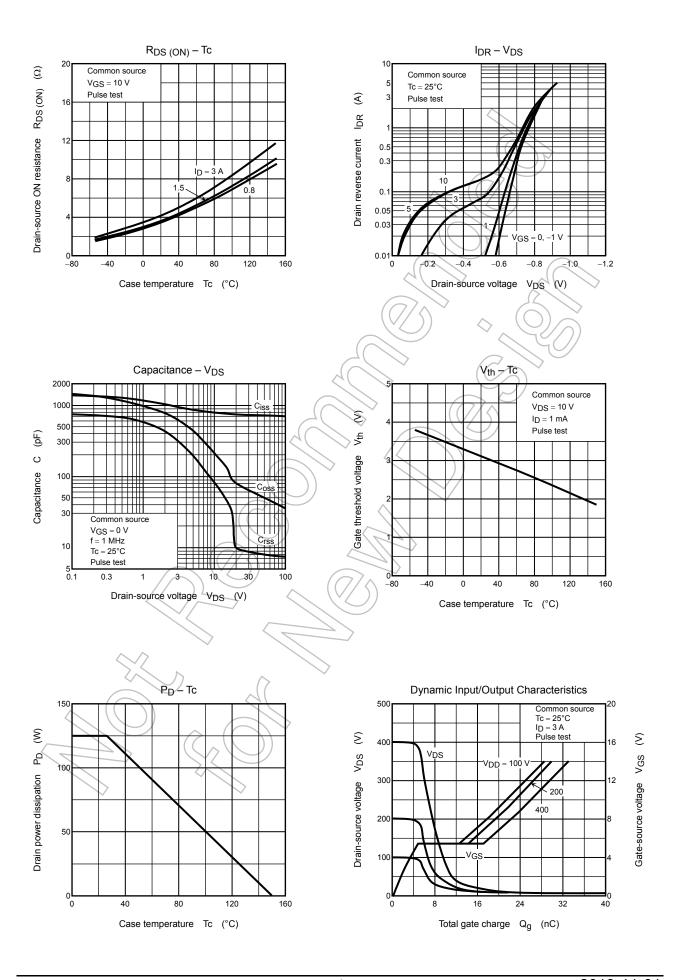
Not underlined: [[Pb]]/INCLUDES > MCV

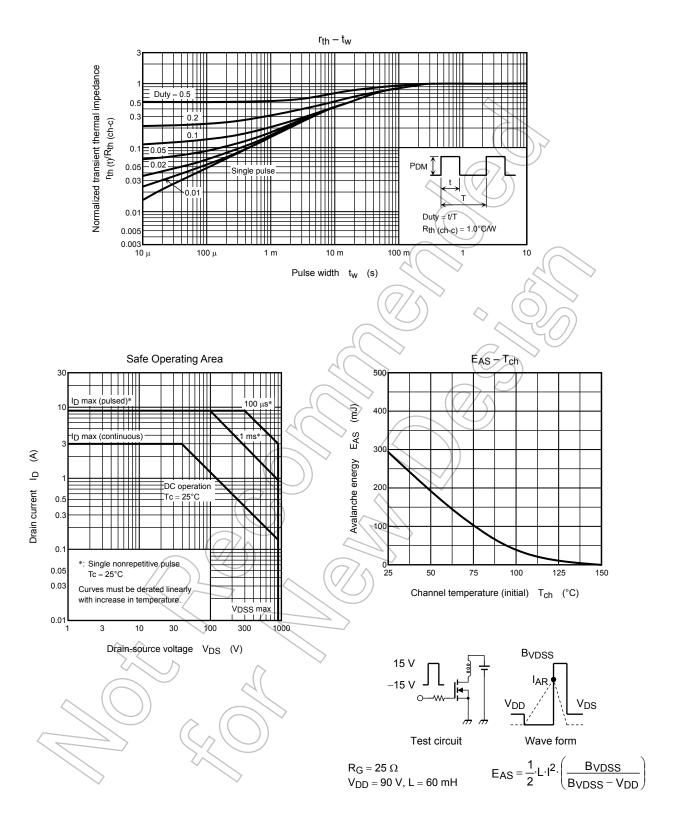
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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