

AUTOMOTIVE N-Channel 40V 175°C MOSFET

FEATURES

- AEC-Q101 Qualified
- 100% UIS and R_g Tested
- 175°C Operating Junction Temperature
- Wettable Flank Package
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

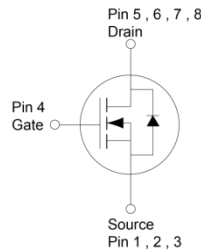
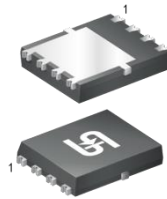
APPLICATIONS

- 12V Automotive Systems
- Solenoid and Motor Control
- Automotive Transmission Control
- DC-DC Converters

PRODUCT SUMMARY		
PARAMETER	VALUE	UNIT
V _{DS}	40	V
R _{DS(on)} (max)	V _{GS} = 10V	3.3
	V _{GS} = 7V	5.1
Q _g	87	nC



PDFN56U



Note: MSL 1 (Moisture Sensitivity Level) per J-STD-020

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V _{DS}	40	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current ^(Note 1)	I _D	T _C = 25°C	121
		T _A = 25°C	21
Pulsed Drain Current	I _{DM}	484	A
Single Pulse Avalanche Current ^(Note 2)	I _{AS}	36	A
Single Pulse Avalanche Energy ^(Note 2)	E _{AS}	194	mJ
Total Power Dissipation	P _D	T _C = 25°C	107
		T _C = 125°C	36
Total Power Dissipation	P _D	T _A = 25°C	3.1
		T _A = 125°C	1
Operating Junction and Storage Temperature Range	T _J , T _{STG}	- 55 to +175	°C

THERMAL RESISTANCE			
PARAMETER	SYMBOL	MAXIMUM	UNIT
Thermal Resistance – Junction to Case	R _{θJC}	1.4	°C/W
Thermal Resistance – Junction to Ambient	R _{θJA}	48	°C/W

Thermal Performance Note: R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistances. The case-thermal reference is defined at the solder mounting surface of the drain pins. R_{θJC} is guaranteed by design while R_{θCA} is determined by the user's board design. The R_{θJA} limit presented here is based on mounting on a 1 in² pad of 2 oz copper.

ELECTRICAL CHARACTERISTICS (T _A = 25°C unless otherwise noted)						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Static						
Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	BV _{DSS}	40	--	--	V
Gate Threshold Voltage	V _{GS} = V _{DS} , I _D = 250μA	V _{GS(TH)}	1.8	2.5	3.8	V
Gate-Source Leakage Current	V _{GS} = ±20V, V _{DS} = 0V	I _{GSS}	--	--	±100	nA
Drain-Source Leakage Current	V _{GS} = 0V, V _{DS} = 40V	I _{DSS}	--	--	1	μA
	V _{GS} = 0V, V _{DS} = 40V T _J = 125°C		--	--	100	
	V _{GS} = 0V, V _{DS} = 40V T _J = 175°C		--	--	500	
Drain-Source On-State Resistance (Note 3)	V _{GS} = 10V, I _D = 21A	R _{DS(on)}	--	2.2	3.3	mΩ
	V _{GS} = 10V, I _D = 21A, T _J = 125°C		--	3.8	5.7	
	V _{GS} = 10V, I _D = 21A, T _J = 175°C		--	4.8	7.3	
	V _{GS} = 7V, I _D = 17A		--	2.5	5.1	
Forward Transconductance (Note 3)	V _{DS} = 10V, I _D = 21A	g _{fs}	--	68	--	S
Dynamic (Note 4)						
Total Gate Charge	V _{GS} = 10V, V _{DS} = 20V, I _D = 21A	Q _g	--	87	--	nC
Total Gate Charge	V _{GS} = 7V, V _{DS} = 20V, I _D = 17A	Q _g	--	64	--	
Gate-Source Charge		Q _{gs}	--	20	--	
Gate-Drain Charge		Q _{gd}	--	23	--	
Input Capacitance	V _{GS} = 0V, V _{DS} = 20V, f = 1.0MHz	C _{iss}	--	4917	--	pF
Output Capacitance		C _{oss}	--	484	--	
Reverse Transfer Capacitance		C _{rss}	--	276	--	
Gate Resistance	f = 1.0MHz	R _g	0.5	1.7	3.4	Ω
Switching (Note 4)						
Turn-On Delay Time	V _{GS} = 10V, V _{DS} = 20V, I _D = 21A, R _G = 2Ω	t _{d(on)}	--	10	--	ns
Rise Time		t _r	--	24	--	
Turn-Off Delay Time		t _{d(off)}	--	49	--	
Fall Time		t _f	--	23	--	
Source-Drain Diode						
Diode Forward Voltage (Note 3)	V _{GS} = 0V, I _S = 21A	V _{SD}	--	--	1	V
Reverse Recovery Time	I _S = 21A,	t _{rr}	--	30	--	ns
Reverse Recovery Charge	di/dt = 100A/μs	Q _{rr}	--	20	--	nC

Notes:

1. Silicon limited current only.
2. $L = 0.3\text{mH}$, $V_{GS} = 10\text{V}$, $V_{DD} = 25\text{V}$, $R_G = 50\Omega$, $I_{AS} = 36\text{A}$, Starting $T_J = 25^\circ\text{C}$
3. Pulse test: Pulse Width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
4. Switching time is essentially independent of operating temperature.

ORDERING INFORMATION

ORDERING CODE	PACKAGE	PACKING
TQM033NB04CR RLG	PDFN56U	2,500pcs / 13" Reel
TQM033NB04CR-V RLG	PDFN56U	2,500pcs / 13" Reel

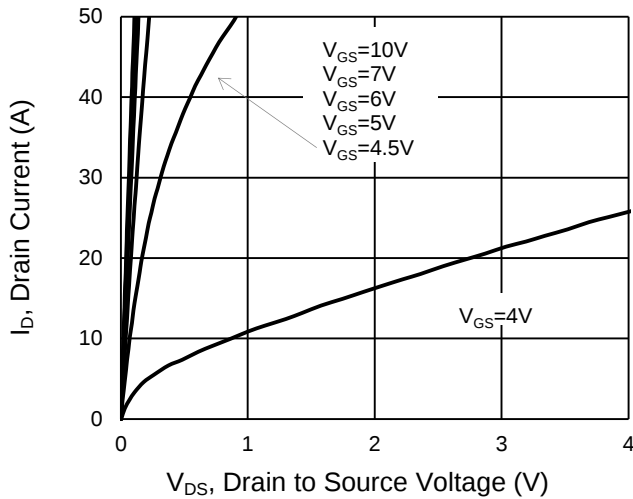
Notes:

V : HOT test

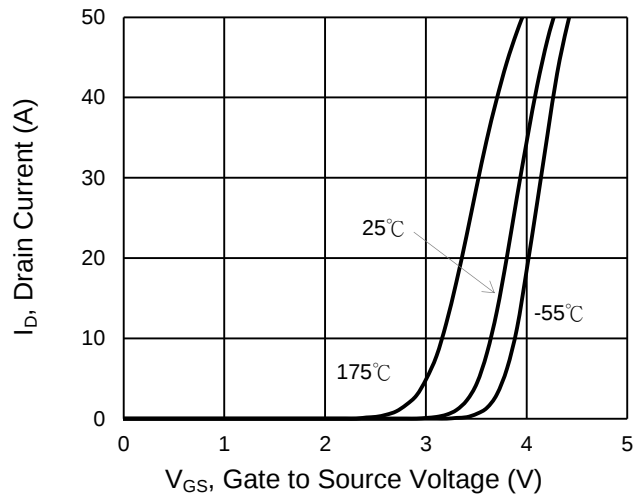
CHARACTERISTICS CURVES

($T_A = 25^\circ\text{C}$ unless otherwise noted)

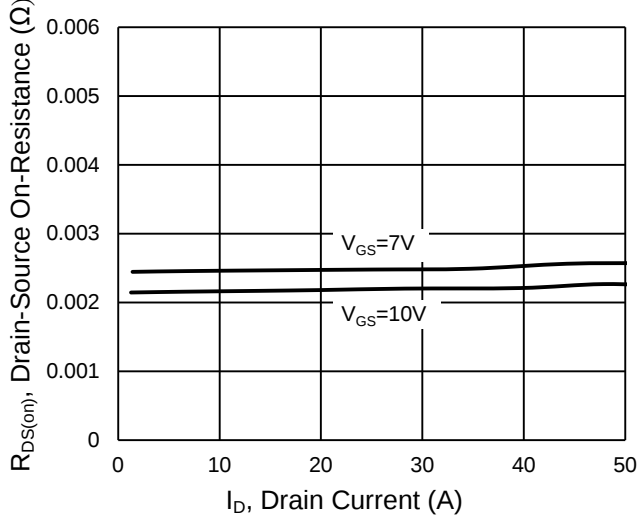
Output Characteristics



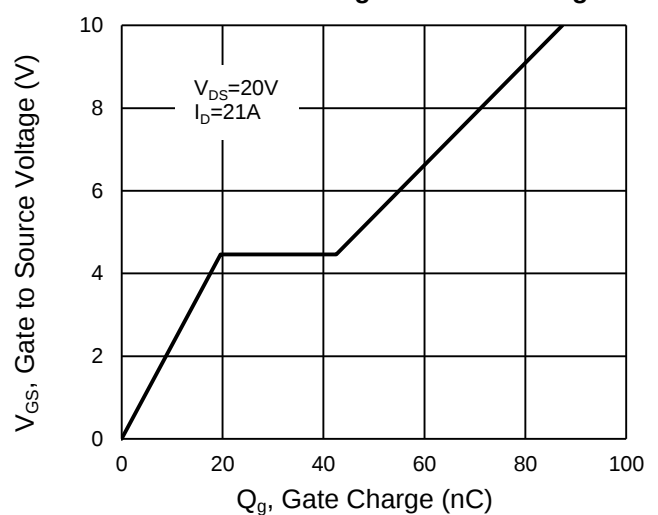
Transfer Characteristics



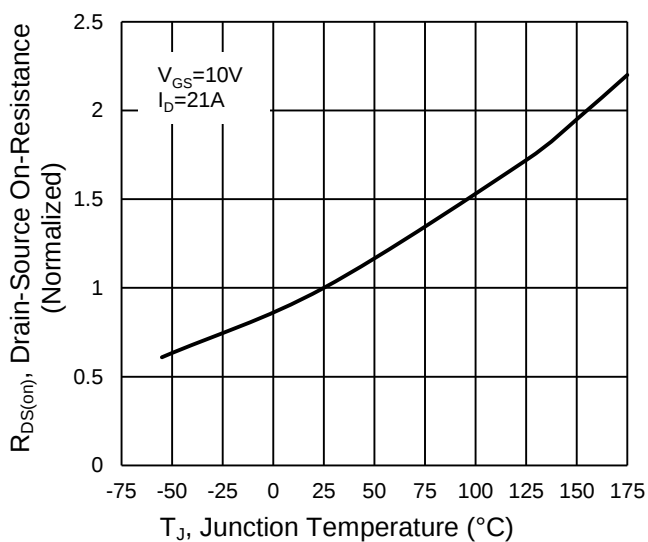
On-Resistance vs. Drain Current



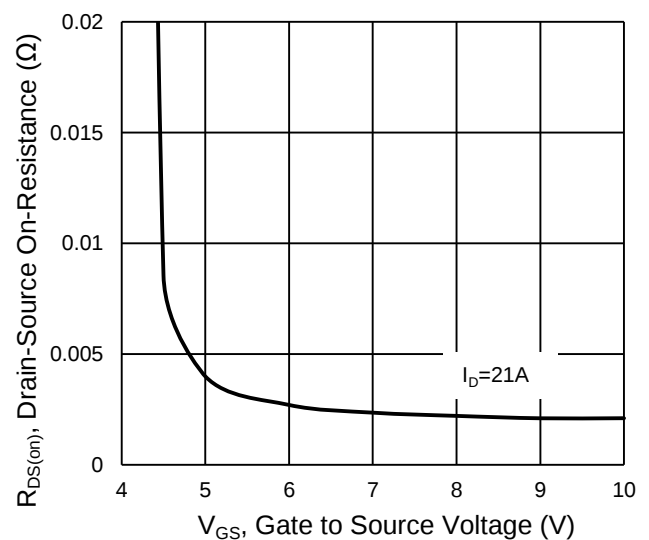
Gate-Source Voltage vs. Gate Charge



On-Resistance vs. Junction Temperature

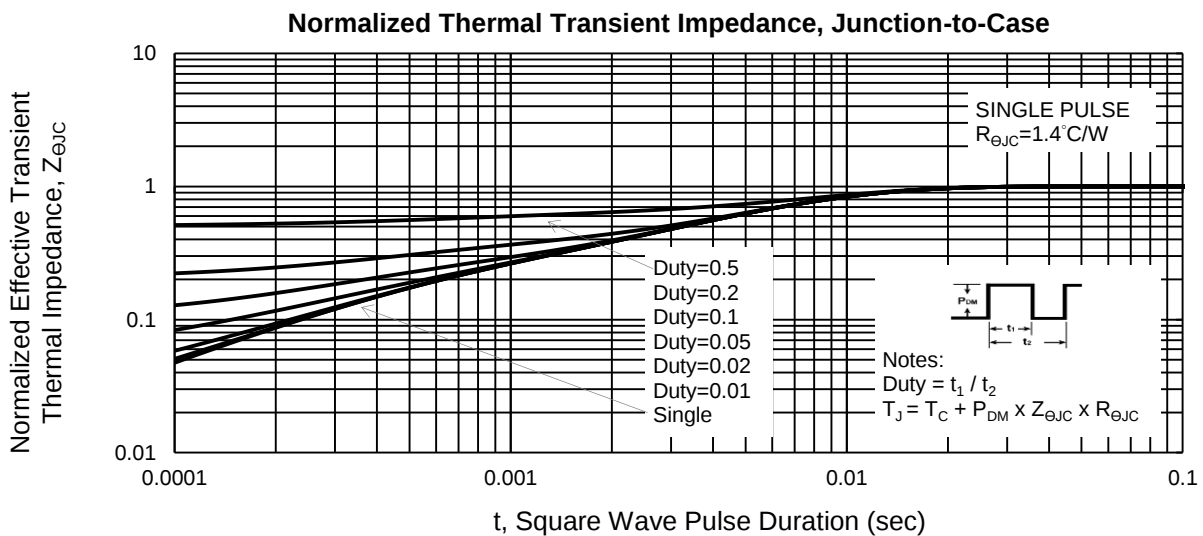
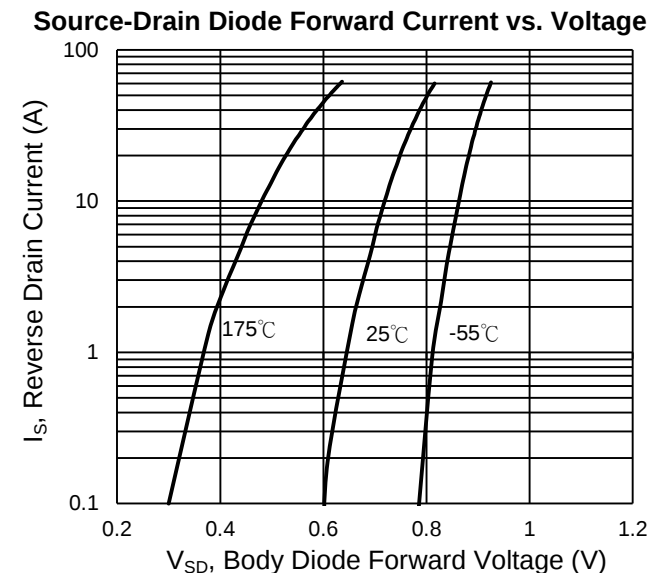
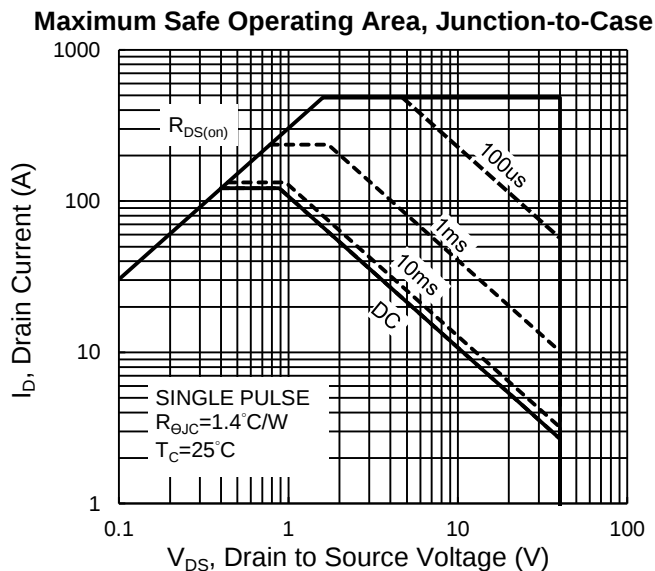
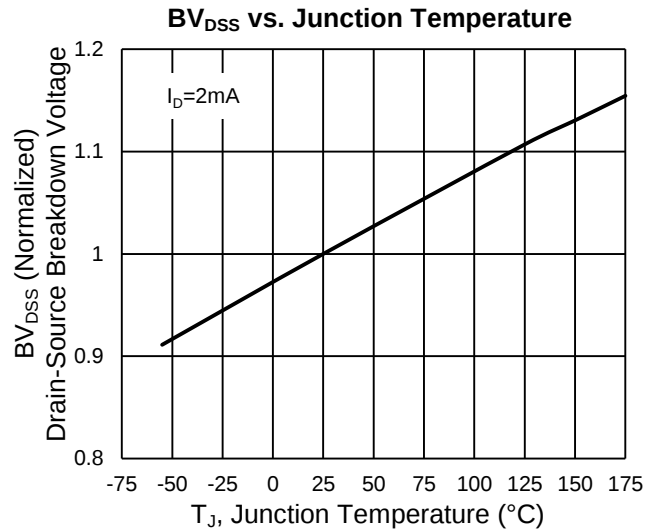
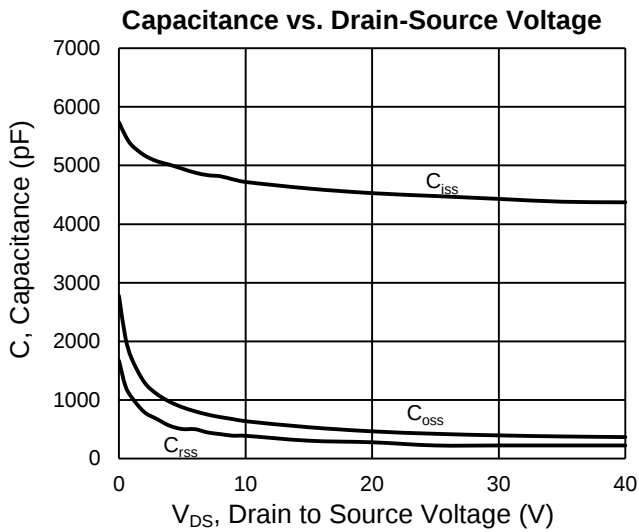


On-Resistance vs. Gate-Source Voltage



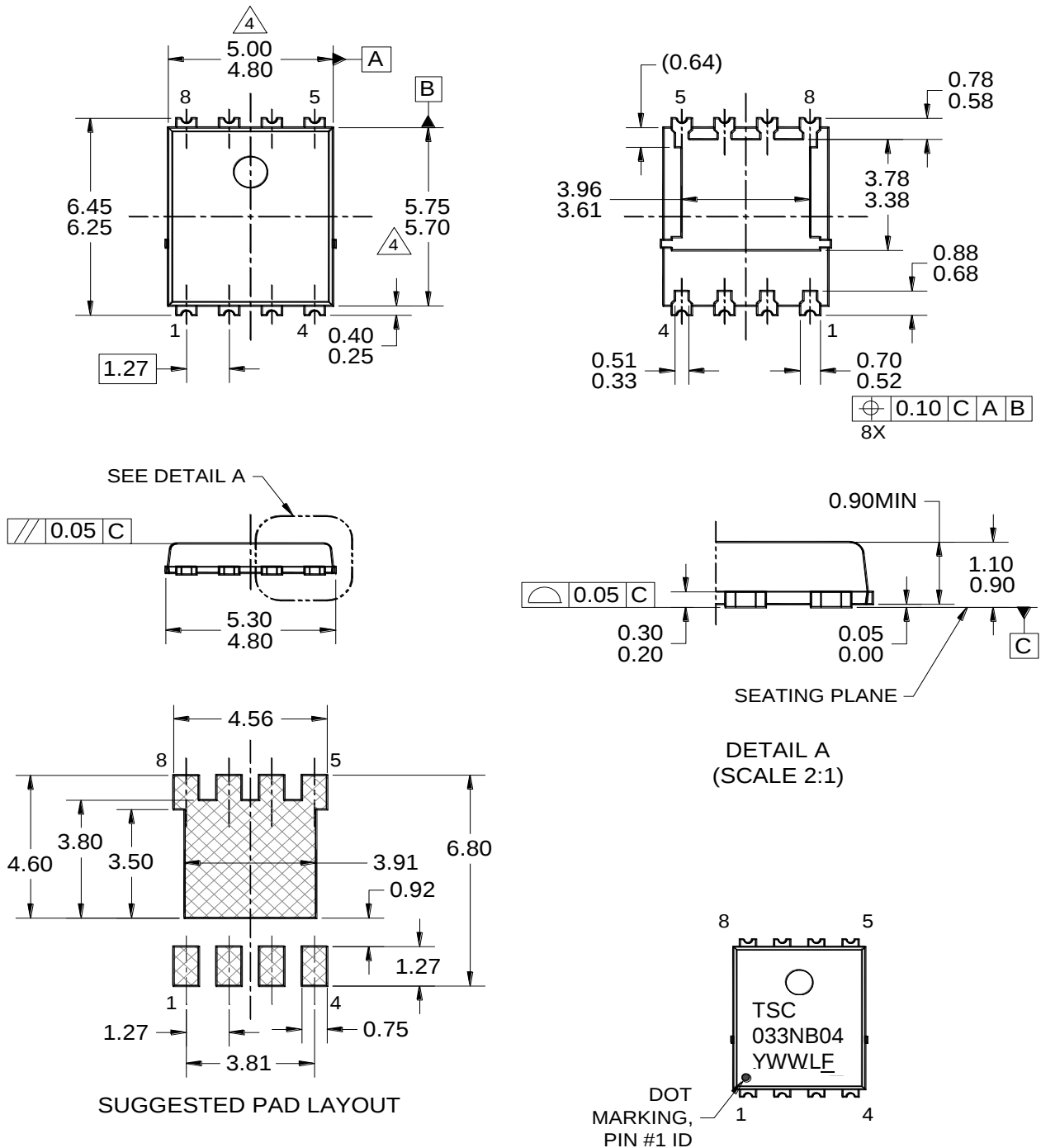
CHARACTERISTICS CURVES

($T_A = 25^\circ\text{C}$ unless otherwise noted)



PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)

PDFN56U



NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
3. PACKAGE OUTLINE REFERENCE: JEITA ED-7500B, EIAJ SC-111BB.
4. MOLDED PLASTIC BODY DIMENSIONS DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
5. DWG NO. REF: HQ2SD07-PDFN56U-023 REV A.

P/N = MARKING CODE
Y = YEAR CODE
WW = WEEK CODE (01~52)
L = LOT CODE (1~9, A~Z)
F = FACTORY CODE
- = AEC-Q101 QUALIFIED

Notice

Specifications of the products displayed herein are subject to change without notice. TSC or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.

Purchasers are solely responsible for the choice, selection, and use of TSC products and TSC assumes no liability for application assistance or the design of Purchasers' products.

Information contained herein is intended to provide a product description only. No license, express or implied, to any intellectual property rights is granted by this document. Except as provided in TSC's terms and conditions of sale for such products, TSC assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of TSC products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify TSC for any damages resulting from such improper use or sale.