

Development Board EPC9091 Quick Start Guide

100 V Half-Bridge with Gate Drive, Using EPC2051

Revision 1.0



DESCRIPTION

The EPC9091 development board is a 100 V maximum device voltage, half bridge with onboard gate drives, featuring the EPC2051 enhancement mode (eGaN®) field effect transistor (FET). The purpose of this development board is to simplify the evaluation process of the EPC2051 eGaN FET by including all the critical components on a single board that can be easily connected into any existing converter.

The EPC9091 development board is 2" x 2" and contains two EPC2051 eGaN FETs in a half bridge configuration using the uP1966A gate driver from uPI Semiconductor Corp. The board contains all critical components and layout for optimal switching performance. There are also various probe points to facilitate simple waveform measurement and efficiency calculation. A block diagram of the circuit is given in Figure 1.

For more information on the EPC2051 please refer to the datasheet available from EPC at www.epc-co.com. The datasheet should be read in conjunction with this quick start guide.

QUICK START PROCEDURE

Development board EPC9091 is easy to set up to evaluate the performance of EPC2051 eGaN FETs. Refer to Figure 2 for proper connections and measurement setup, and follow the procedures below:

1. With power off, connect the input power supply bus to +V_{IN} (J5, J6) and ground / return to GND (J7, J8).
2. With power off, connect the switch node (SW) of the half bridge (J3, J4) and GND (J7, J8) to your circuit as required (half bridge configuration). The EPC9091 features an optional buck converter configuration, as shown in Figure 2, with unpopulated footprints for an output inductor and output capacitors.
3. With power off, connect the gate drive power supply to V_{DD} (J1, Pin-1) and ground return to GND (J1, Pin-2).
4. With power off, connect the input PWM control signal for the high-side switch to PWM (J2, Pin-1) and ground return to GND (J2, Pin-2).
5. Turn on the gate drive supply – make sure the supply is between 7.5 V and 12 V range.
6. Turn on the controller / PWM input source.
7. Turn on the bus voltage to the required value (do not exceed the absolute maximum voltage) and probe switching node to see switching operation.
8. Once operational, adjust the PWM control, bus voltage, and load within the operating range and observe the output switching behavior, efficiency and other parameters.
9. For shutdown, please follow steps in reverse.

Table 1: Performance summary (T_A = 25°C) EPC9091

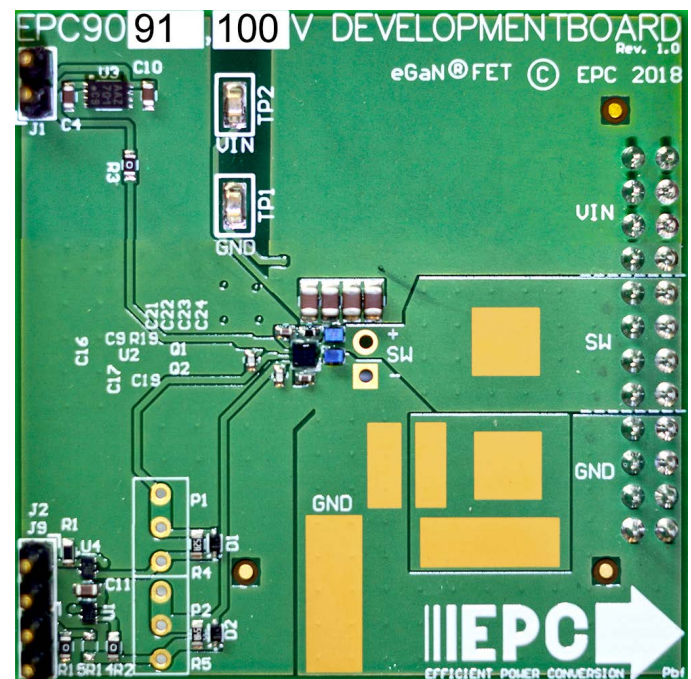
Symbol	Parameter	Conditions	Min	TYP	Max	Units
V _{DD}	Gate Drive Input Supply Range		7.5	10	12	V
V _{IN}	Bus Input Voltage Range ⁽¹⁾				80	V
I _{OUT}	Switch Node Output Current ⁽²⁾				See Note 2	A
V _{PWM}	PWM Logic Input Voltage Threshold	Input 'High'	2.3			V
		Input 'Low'			0.5	
	Minimum 'High' State Input Pulse Width ⁽³⁾	V _{PWM} rise and fall time < 10 ns	20			ns
	Minimum 'Low' State Input Pulse Width ⁽⁴⁾	V _{PWM} rise and fall time < 10 ns	See Note 4			ns

(1) Maximum input voltage depends on inductive loading, maximum switch node ringing must be kept under 100 V for EPC2051.

(2) Maximum current depends on die temperature – actual maximum current will be subject to switching frequency, bus voltage and thermal cooling.

(3) Limited by gate driver rise and fall times.

(4) Limited by time needed to 'refresh' high side bootstrap supply voltage.



EPC9091 development board

NOTE. When measuring the high frequency content switch node, care must be taken to provide an accurate high speed measurement. An optional two pin header (J10) is included for switch node measurement. It is recommended to install measurement point on backside of board to prevent contamination of the top side components.

For information about measurement techniques, please click title and review EPC's Application Note AN023: "[Accurately Measuring High Speed GaN Transistors.](#)"

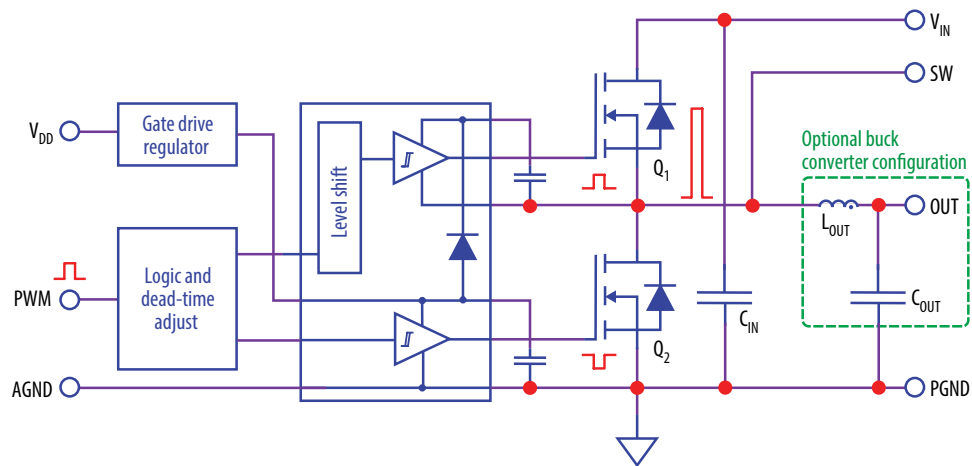


Figure 1: Block diagram of EPC9091 development board.

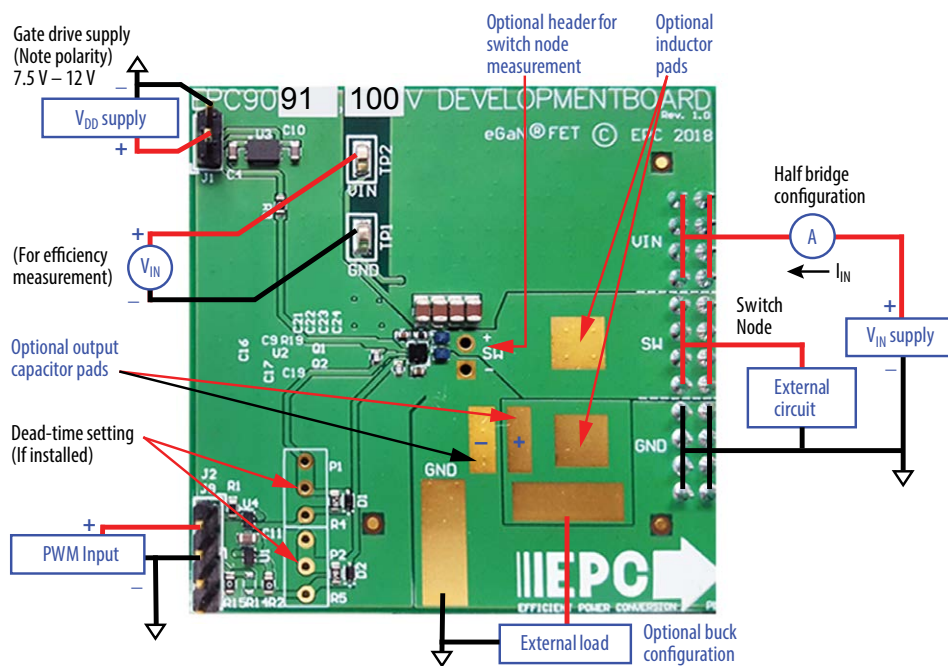
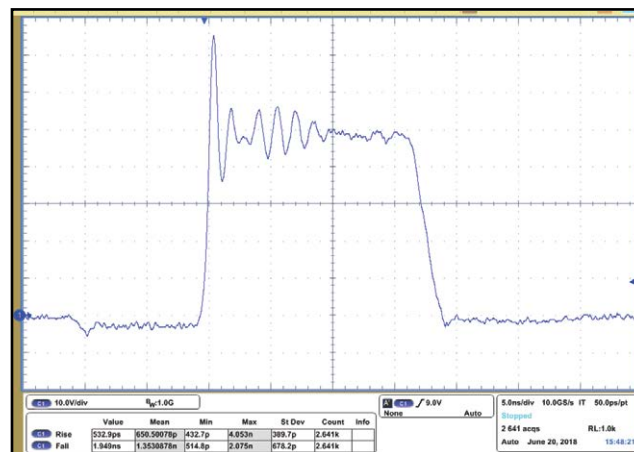


Figure 2: Proper connection and measurement setup



$$V_{IN} = 48 \text{ V}, V_{OUT} = 1 \text{ V}, I_{OUT} = 5 \text{ A}, f_{sw} = 1 \text{ MHz}$$

Figure 3: Typical waveform for $V_{IN} = 48 \text{ V}$ to 1 V_{OUT} , 5 A , 1 MHz buck converter

THERMAL CONSIDERATIONS

The EPC9091 development board showcases the EPC2051 eGaN FET. The EPC9091 is intended for bench evaluation with typical room ambient temperature. The addition of heat-sinking and forced air cooling can significantly increase the current capability of these devices, but care must be taken to not exceed the absolute maximum die temperature of 150° C.

NOTE. The EPC9091 development board does not have any current or thermal protection on board.

For more information regarding the thermal performance of EPC eGaN FETs, please consult:

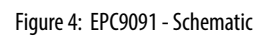
D. Reusch and J. Glaser, *DC-DC Converter Handbook*, a supplement to *GaN Transistors for Efficient Power Conversion*, First Edition, Power Conversion Publications, 2015.

Table 2: Bill of Materials

Item	Qty	Reference	Part Description	Manufacturer	Part Number
1	3	C4, C10, C11	Capacitor, 1 μ F, 10%, 25 V, X5R, 0603	Murata	GRM188R61E105KA12D
2	1	C9	Capacitor, 0.1 μ F, 10%, 25 V, X5R, 0402	TDK	C1005X5R1E104K050BC
3	2	C16, C17	Capacitor, 100 pF, 10%, 50 V, NP0, 0402	Kemet	C0402C101K5GACTU
4	1	C19	Capacitor, 1 μ F, 10%, 25 V, X5R, 0402	TDK	C1005X5R1E105K050BC
5	4	C21, C22, C23, C24	Capacitor, 1 μ F, 20%, 100 V, X7S, 0805	TDK	C2012X7S2A105M125AB
6	2	D1, D2	Schottky Diode, 30 V, SOD523	Diodes Inc.	SDM03U40-7
7	2	Q1, Q2	eGaN FET, 100 V, 21 m Ω	EPC	EPC2051
8	1	U1	IC, NAND, 1CH, 2-INP, 6MICROPAK	Fairchild	NC7SZ00L6X
9	1	U2	IC, Gate driver, Half Bridge, 100 V, DSBGA-12	UPI Semiconductor Corp.	UP1966A
10	1	U3	IC, Linear Regulator, 5 V, 8DFN	Microchip	MCP1703T-5002E/MC
11	1	U4	IC, AND, 1CH, 2-INP, 6-MICROPAK	Fairchild	NC7SZ08L6X
12	1	R1	Resistor, 10 k Ω , 1%, 1/10 W, 0603	Stackpole	RMCF0603FT10K0
13	3	R2, R15, R3	Resistor, 0 Ω jumper, 1/10 W, 0603	Panasonic	ERJ-3GEY0R00V
14	1	R4	Resistor, 340 Ω , 1%, 1/10 W, 0603	Yageo	RC0603FR-07340RL
15	1	R5	Resistor, 357 Ω , 1%, 1/10 W, 0603	Yageo	RC0603FR-07357RL
16	1	R19	Resistor, 0 Ω jumper, 1/16 W, 0402	Stackpole	RMCF0402ZT0R00TR-ND
17	3	J1, J2, J9	2-pin header, through-hole, 100 mil pitch	Tyco	4-103185-0-02
18	6	J3, J4, J5, J6, J7, J8	4-pin header, through-hole, 100 mil pitch	Amphenol FCI	68602-224HLF
19	2	TP1, TP2	Test point, miniature SMT	Keystone Elect	5015

Table 3: Optional components

Item	Qty	Reference	Part Description	Manufacturer	Part Number
1	DNP	P1, P2	Potentiometer, 1 k Ω , through-hole, for dead time adjustment	TT Electronics / BI	64YR1KLF
3	DNP	J11, J12	MMCX connector, SMT, for VGS probe	Molex	734152063
5	DNP	R14	Resistor, 0 Ω jumper, 1/10 W, 0603, for low side drive PWM option	Panasonic	ERJ-3GEY0R00V



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Demonstration Board Notification

The EPC9091 board is intended for product evaluation purposes only and is not intended for commercial use. Replace components on the Evaluation Board only with those parts shown on the parts list (or Bill of Materials) in the Quick Start Guide. Contact an authorized EPC representative with any questions.

This board is intended to be used by certified professionals, in a lab environment, following proper safety procedures. Use at your own risk.

As an evaluation tool, this board is not designed for compliance with the European Union directive on electromagnetic compatibility or any other such directives or regulations. As board builds are at times subject to product availability, it is possible that boards may contain components or assembly materials that are not RoHS compliant. Efficient Power Conversion Corporation (EPC) makes no guarantee that the purchased board is 100% RoHS compliant.

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