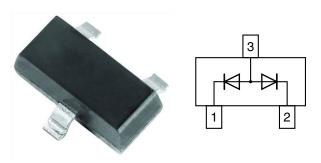


# **Dual Common Anode Small Signal High Voltage Switching Diode**



#### **LINKS TO ADDITIONAL RESOURCES**





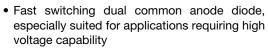






#### **FEATURES**

Silicon epitaxial planar diode





- AEC-Q101 qualified available
- Molding compound meets UL 94 V-0 flammability rating



- Moisture sensitivity level (MSL) 1
- Base P/N-E3 RoHS-compliant, commercial grade
- Base P/N-HE3\_A RoHS-compliant, AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912">www.vishav.com/doc?99912</a>

## **MECHANICAL DATA**

Case: SOT-23

Weight: approx. 9.2 mg
Packaging codes / options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE							
PART	ORDERING CODE	AEC-Q101 QUALIFIED	TYPE MARKING	CIRCUIT CONFIGURATION	TAPED UNITS PER REEL	MINIMUM ORDER QUANTITY	
GSD2004A	GSD2004A-E3-08	no	- DBH	Common anode	3 000	15 000	
	GSD2004A-HE3_A-08	yes			(8 mm tape on 7" reel)	13 000	
	GSD2004A-E3-18	no			10 000	10 000	
	GSD2004A-HE3_A-18	yes			(8 mm tape on 13" reel)	10 000	

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Continuous reverse voltage		$V_{R}$	240	V	
Peak repetitive reverse voltage		$V_{RRM}$	300	V	
Forward current (continuous) (1)		I <sub>F</sub>	350	mA	
Peak repetitive forward current (1)		I <sub>FRM</sub>	625	mA	
Non-repetitive peak forward current (1)	t <sub>p</sub> = 1 μs		4	Α	
	t <sub>p</sub> = 1 s	I <sub>FSM</sub>	1	Α	
Power dissipation	on FR-4 board with recommended soldering footprint	P <sub>tot</sub>	300	mW	
Fower dissipation	Infinite heatsink	⊏tot	500	mW	

#### Note

(1) Infinite heatsink

THERMAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Typical thermal resistance junction to ambient air	according to JEDEC® 51-3 on FR-4 board with recommended soldering footprint	R <sub>thJA</sub>	420	K/W		
Thermal resistance junction to lead	Infinite heatsink	R <sub>thJL</sub>	250	K/W		
Junction temperature		T <sub>j</sub>	150	°C		
Storage temperature range		T <sub>stg</sub>	-65 to +150	°C		
Operating temperature range		T <sub>op</sub>	-55 to +150	°C		



<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reverse breakdown voltage	I <sub>R</sub> = 100 μA	$V_{BR}$	300			V
Laslana aumant	V <sub>R</sub> = 240 V	I <sub>R</sub>			100	nA
Leakage current	$V_R = 240 \text{ V}, T_j = 150 ^{\circ}\text{C}$	I <sub>R</sub>			100	μΑ
Compared valtage	I <sub>F</sub> = 20 mA	V <sub>F</sub>		0.83	0.87	V
Forward voltage	I <sub>F</sub> = 100 mA	V <sub>F</sub>			1	V
Diode capacitance	$V_F = V_R = 0$ , $f = 1$ MHz	C <sub>D</sub>			2	pF
Reverse recovery time	$I_F = I_R = 30$ mA, $i_R = 3$ mA, $R_L = 100$ $\Omega$	t <sub>rr</sub>			50	ns

## **TYPICAL CHARACTERISICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

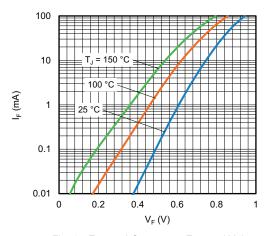


Fig. 1 - Forward Current vs. Forward Voltage

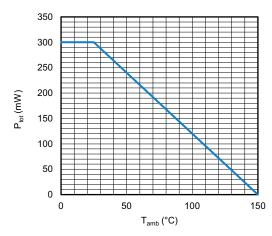


Fig. 2 - Admissible Power Dissipation vs. Ambient Temperature

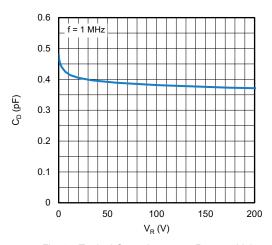


Fig. 3 - Typical Capacitance vs. Reverse Voltage

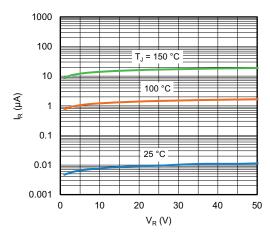
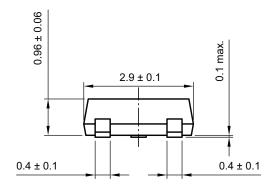
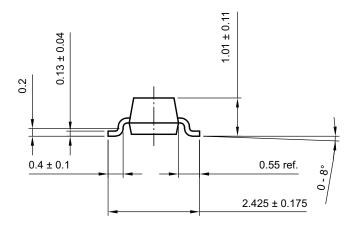


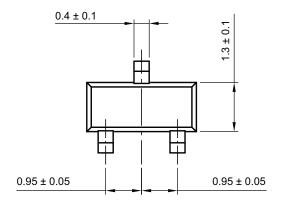
Fig. 4 - Typical Reverse Leakage Current vs. Reverse Voltage



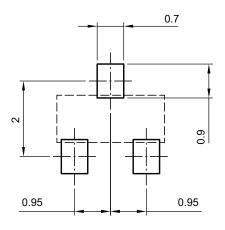
## **PACKAGE DIMENSIONS** in millimeters: **SOT-23**







### footprint recommendation:

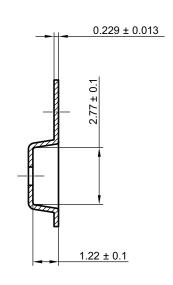


Created - Date: 18-Oct-2021 Rev. 01 - Date: 18-Jan-2022 S8-V-3929.01-009 (4)

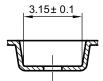
## **CARRIER TAPE SOT-23**

# Ø1.5 +0.1 Ø1.5 +0.1 B B A

## A-A Section

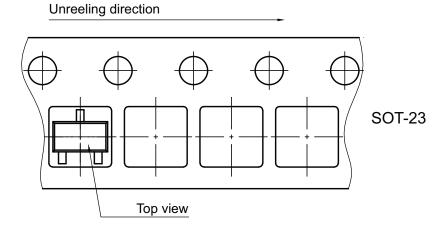


**B-B Section** 



Created Date: 04-Feb-2010 Rev. Date: 07-Feb-2022 S8-V-3929.01-005 (4)

#### **ORIENTATION IN CARRIER TAPE SOT-23**



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