

# ON Semiconductor

## Is Now



To learn more about onsemi™, please visit our website at  
[www.onsemi.com](http://www.onsemi.com)

onsemi and onsemi. and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use onsemi products for any such unintended or unauthorized application, Buyer shall indemnify and hold onsemi and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that onsemi was negligent regarding the design or manufacture of the part. onsemi is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner. Other names and brands may be claimed as the property of others.

# TinyLogic ULP-A OR Gate

## NC7SP32

The NC7SP32 is a single 2-input OR gate in tiny footprint packages. The device is designed to operate for  $V_{CC} = 0.9\text{ V}$  to  $3.6\text{ V}$ .

### Features

- Designed for  $0.9\text{ V}$  to  $3.6\text{ V}$   $V_{CC}$  Operation
- $2.7\text{ ns}$   $t_{PD}$  at  $3.3\text{ V}$  (Typ)
- Inputs/Outputs Over-Voltage Tolerant up to  $3.6\text{ V}$
- $I_{OFF}$  Supports Partial Power Down Protection
- Source/Sink  $2.6\text{ mA}$  at  $3.3\text{ V}$
- Available in SC-88A and MicroPak™ Packages
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

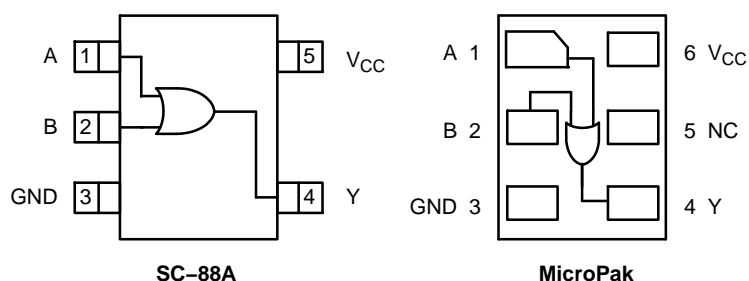


Figure 1. Pinout Diagrams (Top Views)

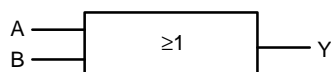


Figure 2. Logic Symbol

### PIN ASSIGNMENT

Pin	SC88A	MicroPak
1	A	A
2	B	B
3	GND	GND
4	Y	Y
5	$V_{CC}$	N.C.
6	—	$V_{CC}$

N.C. = No Connect

### FUNCTION TABLE

Input		Output
A	B	Y
L	L	L
L	H	H
H	L	H
H	H	H



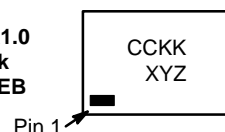
ON Semiconductor®

[www.onsemi.com](http://www.onsemi.com)

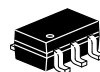
### MARKING DIAGRAMS



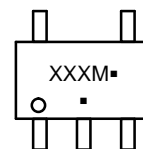
SIP6 1.45X1.0  
MicroPak  
CASE 127EB



CC = Specific Device Code  
KK = 2-Digit Lot Run Traceability Code  
XY = 2-Digit Date Code  
Z = Assembly Plant Code



SC-88A  
CASE 419A-02



XXX = Specific Device Code  
M = Date Code  
▪ = Pb-Free Package

### ORDERING INFORMATION

See detailed ordering, marking and shipping information on page 6 of this data sheet.

# NC7SP32

## MAXIMUM RATINGS

Symbol	Characteristics	Value	Unit
$V_{CC}$	DC Supply Voltage	–0.5 to +4.3	V
$V_{IN}$	DC Input Voltage	–0.5 to +4.3	V
$V_{OUT}$	DC Output Voltage Active–Mode (High or Low State) Tri–State Mode (Note 1) Power–Down Mode ( $V_{CC} = 0$ V)	–0.5 to $V_{CC} + 0.5$ –0.5 to +4.3 –0.5 to +4.3	V
$I_{IK}$	DC Input Diode Current $V_{IN} < GND$	–50	mA
$I_{OK}$	DC Output Diode Current $V_{OUT} < GND$	–50	mA
$I_{OUT}$	DC Output Source/Sink Current	±50	mA
$I_{CC}$ or $I_{GND}$	DC Supply Current per Supply Pin or Ground Pin	±50	mA
$T_{STG}$	Storage Temperature Range	–65 to +150	°C
$T_L$	Lead Temperature, 1 mm from Case for 10 Seconds	260	°C
$T_J$	Junction Temperature Under Bias	+150	°C
$\theta_{JA}$	Thermal Resistance (Note 2) SC–88A MicroPak	377 154	°C/W
$P_D$	Power Dissipation in Still Air SC–88A MicroPak	332 812	mW
MSL	Moisture Sensitivity	Level 1	–
$F_R$	Flammability Rating Oxygen Index: 28 to 34	UL 94 V–0 @ 0.125 in	–
$V_{ESD}$	ESD Withstand Voltage (Note 3) Human Body Model Charged Device Model	2000 1000	V
$I_{Latchup}$	Latchup Performance (Note 4)	±100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Applicable to devices with outputs that may be tri–stated.
2. Measured with minimum pad spacing on an FR4 board, using 10 mm–by–1 inch, 2 ounce copper trace no air flow per JESD51–7.
3. HBM tested to EIA / JESD22–A114–A. CDM tested to JESD22–C101–A. JEDEC recommends that ESD qualification to EIA/JESD22–A115A (Machine Model) be discontinued.
4. Tested to EIA/JESD78 Class II.

## RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
$V_{CC}$	Positive DC Supply Voltage	0.9	3.6	V
$V_{IN}$	DC Input Voltage	0	3.6	V
$V_{OUT}$	DC Output Voltage Active–Mode (High or Low State) Tri–State Mode (Note 1) Power–Down Mode ( $V_{CC} = 0$ V)	0 0 0	$V_{CC}$ 3.6 3.6	V
$T_A$	Operating Temperature Range	–40	+85	°C
$t_r, t_f$	Input Transition Rise and Fall Time $V_{CC} = 3.3$ V ± 0.3 V	0	10	ns/V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

# NC7SP32

## DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Condition	V <sub>CC</sub> (V)	T <sub>A</sub> = 25°C			T <sub>A</sub> = -40°C to +85°C		Unit
				Min	Typ	Max	Min	Max	
V <sub>IH</sub>	High-Level Input Voltage		0.9	–	0.5	–	–	–	V
			1.1 to 1.3	0.65 x V <sub>CC</sub>	–	–	0.65 x V <sub>CC</sub>	–	
			1.4 to 1.6	0.65 x V <sub>CC</sub>	–	–	0.65 x V <sub>CC</sub>	–	
			1.65 to 1.95	0.65 x V <sub>CC</sub>	–	–	0.65 x V <sub>CC</sub>	–	
			2.3 to 2.7	1.6	–	–	1.6	–	
			3.0 to 3.6	2.1	–	–	2.1	–	
V <sub>IL</sub>	Low-Level Input Voltage		0.9	–	0.5	–	–	–	V
			1.1 to 1.3	–	–	0.35 x V <sub>CC</sub>	–	0.35 x V <sub>CC</sub>	
			1.4 to 1.6	–	–	0.35 x V <sub>CC</sub>	–	0.35 x V <sub>CC</sub>	
			1.65 to 1.95	–	–	0.35 x V <sub>CC</sub>	–	0.35 x V <sub>CC</sub>	
			2.3 to 2.7	–	–	0.7	–	0.7	
			3.0 to 3.6	–	–	0.9	–	0.9	
V <sub>OH</sub>	High-Level Output Voltage	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>							V
		I <sub>OH</sub> = -20 µA	0.9	–	V <sub>CC</sub> - 0.1	–	–	–	
			1.1 to 1.3	V <sub>CC</sub> - 0.1	–	–	V <sub>CC</sub> - 0.1	–	
			1.4 to 1.6	V <sub>CC</sub> - 0.1	–	–	V <sub>CC</sub> - 0.1	–	
			1.65 to 1.95	V <sub>CC</sub> - 0.1	–	–	V <sub>CC</sub> - 0.1	–	
			2.3 to 2.7	V <sub>CC</sub> - 0.1	–	–	V <sub>CC</sub> - 0.1	–	
			3.0 to 3.6	V <sub>CC</sub> - 0.1	–	–	V <sub>CC</sub> - 0.1	–	
		I <sub>OH</sub> = -0.5 mA	1.1 to 1.3	0.75 x V <sub>CC</sub>	–	–	0.70 x V <sub>CC</sub>	–	
		I <sub>OH</sub> = -1 mA	1.4 to 1.6	1.07	–	–	0.99	–	
		I <sub>OH</sub> = -1.5 mA	1.65 to 1.95	1.24	–	–	1.22	–	
		I <sub>OH</sub> = -2.1 mA	2.3 to 2.7	1.95	–	–	1.87	–	
		I <sub>OH</sub> = -2.6 mA	3.0 to 3.6	2.61	–	–	2.55	–	
V <sub>OL</sub>	Low-Level Output Voltage	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>							V
		I <sub>OL</sub> = 20 µA	0.9	–	0.1	–	–	–	
			1.1 to 1.3	–	–	0.1	–	0.1	
			1.4 to 1.6	–	–	0.1	–	0.1	
			1.65 to 1.95	–	–	0.1	–	0.1	
			2.3 to 2.7	–	–	0.1	–	0.1	
			3.0 to 3.6	–	–	0.1	–	0.1	
		I <sub>OL</sub> = 0.5 mA	1.1 to 1.3	–	–	0.3 x V <sub>CC</sub>	–	0.3 x V <sub>CC</sub>	
		I <sub>OL</sub> = 1 mA	1.4 to 1.6	–	–	0.31	–	0.37	
		I <sub>OL</sub> = 1.5 mA	1.65 to 1.95	–	–	0.31	–	0.35	
		I <sub>OL</sub> = 2.1 mA	2.3 to 2.7	–	–	0.31	–	0.33	
		I <sub>OL</sub> = 2.6 mA	3.0 to 3.6	–	–	0.31	–	0.33	
I <sub>IN</sub>	Input Leakage Current	V <sub>IN</sub> = 0 V to 3.6 V	0.9 to 3.6	–	–	±0.1	–	±0.5	µA
I <sub>OFF</sub>	Power Off Leakage Current	V <sub>IN</sub> = 0 V to 3.6 V or V <sub>OUT</sub> = 0 V to 3.6 V	0	–	–	0.5	–	0.5	µA
I <sub>CC</sub>	Quiescent Supply Current	V <sub>IN</sub> = V <sub>CC</sub> or GND	0.9 to 3.6	–	–	0.9	–	0.9	µA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

# NC7SP32

## AC ELECTRICAL CHARACTERISTICS

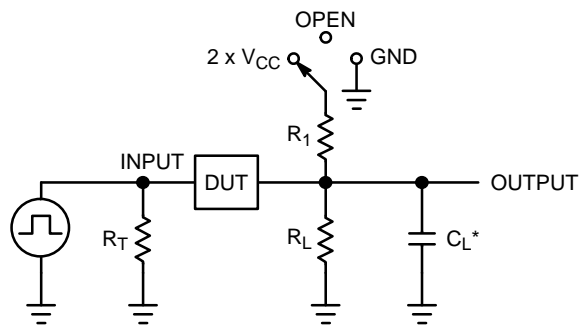
Symbol	Parameter	Condition	V <sub>CC</sub> (V)	T <sub>A</sub> = 25°C			T <sub>A</sub> = -40°C to +85°C		Unit
				Min	Typ	Max	Min	Max	
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay, (A or B) to Y (Figures 3 and 4)	R <sub>L</sub> = 1 MΩ, C <sub>L</sub> = 10 pF	0.9	–	51.8	–	–	–	ns
			1.10 to 1.30	–	13.8	28.6	–	30.9	
			1.40 to 1.60	–	7.2	12.4	–	13.9	
			1.65 to 1.95	–	5.2	9.6	–	12.1	
			2.3 to 2.7	–	3.3	7.0	–	8.0	
			3.0 to 3.6	–	2.7	5.7	–	6.9	
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay, (A or B) to Y (Figures 3 and 4)	R <sub>L</sub> = 1 MΩ, C <sub>L</sub> = 15 pF	0.9	–	52.7	–	–	–	ns
			1.10 to 1.30	–	14.1	29.3	–	33.9	
			1.40 to 1.60	–	7.5	13.3	–	13.9	
			1.65 to 1.95	–	5.4	10.3	–	12.1	
			2.3 to 2.7	–	3.5	7.4	–	8.0	
			3.0 to 3.6	–	2.8	6.1	–	6.9	
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay, (A or B) to Y (Figures 3 and 4)	R <sub>L</sub> = 1 MΩ, C <sub>L</sub> = 30 pF	0.9	–	55.5	–	–	–	ns
			1.10 to 1.30	–	15.2	31.4	–	43.0	
			1.40 to 1.60	–	8.4	16.0	–	18.0	
			1.65 to 1.95	–	6.2	12.0	–	14.0	
			2.3 to 2.7	–	4.0	9.0	–	10.0	
			3.0 to 3.6	–	3.3	7.0	–	8.9	

## CAPACITIVE CHARACTERISTICS

Symbol	Parameter	Test Condition	Typical (T <sub>A</sub> = 25°C)	Unit
C <sub>IN</sub>	Input Capacitance	V <sub>CC</sub> = 0 V	2.0	pF
C <sub>OUT</sub>	Output Capacitance	V <sub>CC</sub> = 0 V	4.0	pF
C <sub>PD</sub>	Power Dissipation Capacitance (Note 5)	f = 10 MHz, V <sub>CC</sub> = 0.9 to 3.6 V, V <sub>IN</sub> = 0 V or V <sub>CC</sub>	6.0	pF

5. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation  $I_{CC(OPR)} = C_{PD} \cdot V_{CC} \cdot f_{in} + I_{CC}$ . C<sub>PD</sub> is used to determine the no-load dynamic power consumption:  $P_D = C_{PD} \cdot V_{CC}^2 \cdot f_{in} + I_{CC} \cdot V_{CC}$ .

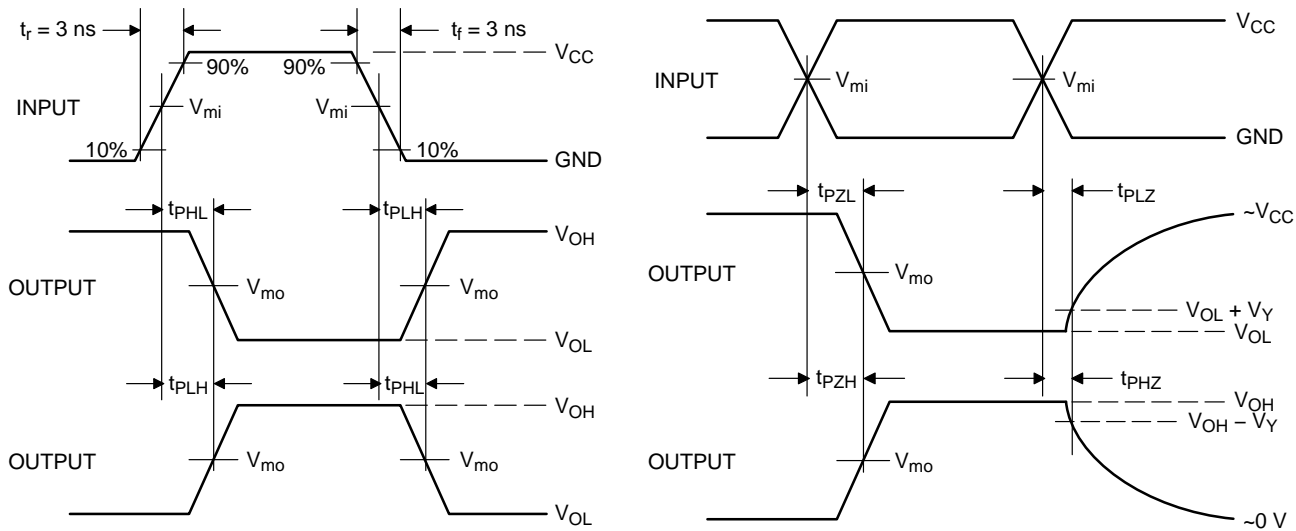
NC7SP32



$C_L$  includes probe and jig capacitance  
 $R_T$  is  $Z_{OUT}$  of pulse generator (typically 50  $\Omega$ )  
 $f = 1$  MHz

Test	Switch Position
$t_{PLH} / t_{PHL}$	Open
$t_{PLZ} / t_{PZL}$	$2 \times V_{CC}$
$t_{PHZ} / t_{PZH}$	GND

Figure 3. Test Circuit



$V_{CC}, V$	$V_{mi}, V$	$V_{mo}, V$	$V_Y, V$
0.9	$V_{CC} / 2$	$V_{CC} / 2$	0.1
1.1 to 1.3	$V_{CC} / 2$	$V_{CC} / 2$	0.1
1.4 to 1.6	$V_{CC} / 2$	$V_{CC} / 2$	0.1
1.65 to 1.95	$V_{CC} / 2$	$V_{CC} / 2$	0.15
2.3 to 2.7	$V_{CC} / 2$	$V_{CC} / 2$	0.15
3.0 to 3.6	1.5	1.5	0.3

Figure 4. Switching Waveforms

## NC7SP32

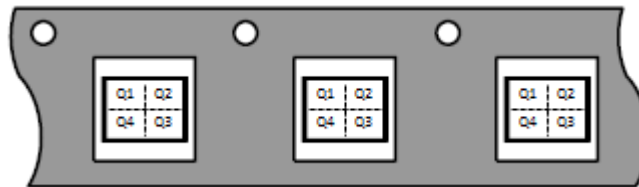
### ORDERING INFORMATION

Device	Package	Marking	Pin 1 Orientation (See below)	Shipping†
NC7SP32P5X	SC-88A	P32	Q4	3000 / Tape & Reel
NC7SP32L6X	MicroPak	K5	Q4	5000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

### Pin 1 Orientation in Tape and Reel

Direction of Feed



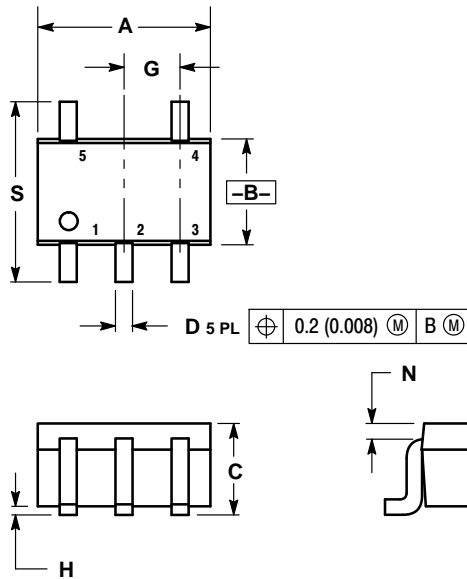
MicroPak is trademark of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries.



# NC7SP32

## PACKAGE DIMENSIONS

SC-88A (SC-70-5/SOT-353)  
CASE 419A-02  
ISSUE L

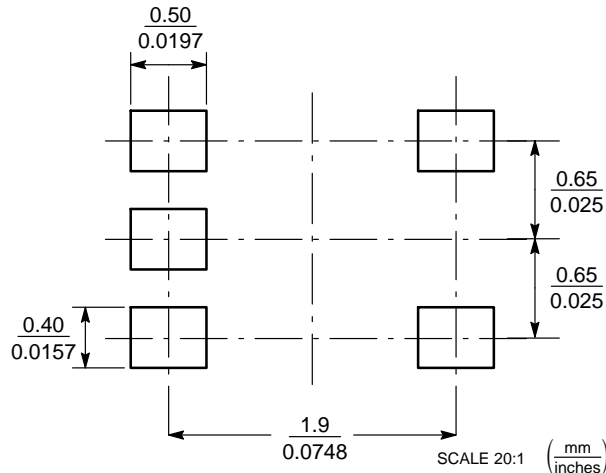



### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. 419A-01 OBSOLETE. NEW STANDARD 419A-02.
4. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.071	0.087	1.80	2.20
B	0.045	0.053	1.15	1.35
C	0.031	0.043	0.80	1.10
D	0.004	0.012	0.10	0.30
G	0.026 BSC		0.65 BSC	
H	---	0.004	---	0.10
J	0.004	0.010	0.10	0.25
K	0.004	0.012	0.10	0.30
N	0.008 REF		0.20 REF	
S	0.079	0.087	2.00	2.20

### SOLDER FOOTPRINT



ON Semiconductor and  are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marketing.pdf](http://www.onsemi.com/site/pdf/Patent-Marketing.pdf). ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

### PUBLICATION ORDERING INFORMATION

#### LITERATURE FULFILLMENT:

Email Requests to: [orderlit@onsemi.com](mailto:orderlit@onsemi.com)

ON Semiconductor Website: [www.onsemi.com](http://www.onsemi.com)

#### TECHNICAL SUPPORT

North American Technical Support:

Voice Mail: 1 800-282-9855 Toll Free USA/Canada

Phone: 011 421 33 790 2910

Europe, Middle East and Africa Technical Support:

Phone: 00421 33 790 2910

For additional information, please contact your local Sales Representative