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CY62165E-3XWI Status: **In Production**



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CY62165E-3XWI	
Automotive Qualified	N
Min. Operating Voltage (V)	1.65
Max. Operating Voltage (V)	5.50
Min. Operating Temp. (°C)	-40
Max. Operating Temp. (°C)	85
Frequency (MHz)	N/A
Temp. Classification	Industrial
Max. Operating VCCQ (V)	5.50
Min. Operating VCCQ (V)	1.65
Speed (ns)	N/A
Density (Kb)	16384
Organization (X x Y)	x8 / x16

Pricing & Inventory Availability					
1-9 unit Price*	10-24 unit Price*	25-99 unit Price*	100-249 unit Price*	250-999 unit Price*	1000 + unit Price*
Contact Sales					

Availability	Quantity	Ships In	Order Now
Out of Stock	0	Please click here to check lead times	Check Distributors**

* Prices are shown in United States dollars and are for budgetary use only for volume of 1ku

** **Check Distributors:** This part is sold through distributors only.

Packaging/Ordering	
No. of Pins	1
Package Cross Section Drawing	Download
Package Carrier Drawing / Orientation	Package Orientation
Standard Pack Quantity	Contact Sales
Minimum Order Quantity (MOQ)	1
Order Increment	1
Estimated Lead Time (days)	21
HTS Code	8542.90.0000
ECCN	3A991
ECCN Suball	(B.2.A.)

Quality and RoHS	
Moisture Sensitivity Level (MSL)	5
Peak Reflow Temp. (°C)	N/A (Cypress Reflow Profile)
RoHS Compliant	N Print RoHS Certificate of Compliance
PB Free	N
Lead/Ball Finish	N/A
Marking	Cypress Marking Format
Device Qualification Reports 🔒 FIT/MTBF, ESD (HBM/CDM) and Latch-up data available in the Device Qualification Report. Please click here .	

Technical Documents
Application Notes (2) AN44517 - Design Recommendation for Battery-Backed SRAMs Using Cypress MoBL® SRAMs <i>Last Update: 24/06/2014</i> AN6081 - Interfacing 90-nm Cypress Asynchronous SRAMs in Legacy Systems <i>Last Update: 19/12/2012</i>
Product Change Notice (PCN) (2) PCN125176 <i>Last Update: 03/12/2012</i> PCN115016 <i>Last Update: 20/01/2011</i>
Product Information Notice (PIN) (3) PIN145296 <i>Last Update: 16/05/2014</i> PIN145277 <i>Last Update: 19/03/2014</i> PCN125157 <i>Last Update: 19/06/2012</i>
White Papers (1) SRAM Board Design Guidelines <i>Last Update: 14/04/2014</i>

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