CAT3604A

4-Channel Regulated Charge Pump White LED Driver

Description

The CAT3604A is a charge pump operating in either 1x (LDO) mode or 1.5x fractional mode regulating current through each of the 4 LED pins. Operation at a fixed high frequency of 1 MHz typical allows the use of very small value ceramic capacitors.

The CAT3604A drives white light-emitting diodes (LEDs) connected in parallel and provides tightly matched regulated current to achieve uniformity of brightness in LCD backlighting applications. An external resistor R_{SET} controls the output current level. LED currents of up to 30 mA are supported over a range of input supply voltages from 3 V to 5.5 V, making the device ideal for battery-powered applications.

LED dimming can be accomplished by several methods including using a DC voltage to set the RSET pin current, applying a PWM signal on the Control signals, or adding a switched resistor in parallel with RSET. The Enable input pin allows the device to be placed in power-down mode with “zero” quiescent current.

The CAT3604A features short circuit and overcurrent limiting protection. The device is available in a 16−pad TQFN package with a max height of 0.8 mm.

Features

- Drives Individually up to 4 LEDs
- Output Current up to 30 mA per LED
- Compatible with Supply Voltage of 3 V to 5.5 V
- Power Efficiency up to 93%
- 2 Modes of Operation 1x and 1.5x
- LED On/Off by Control Lines
- High−frequency Operation at 1 MHz
- Low Value Ceramic Capacitors
- “604” Compatible Pinout
- Soft Start and Current Limiting
- TQFN 16−pad Package, 4 x 4 mm, 0.8 mm Max Height
- These Devices are Pb−Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- Color LCD and Keypad Backlighting
- Cellular Phones
- Handheld Devices
- Digital Cameras
- PDAs
- Portable MP3 Players

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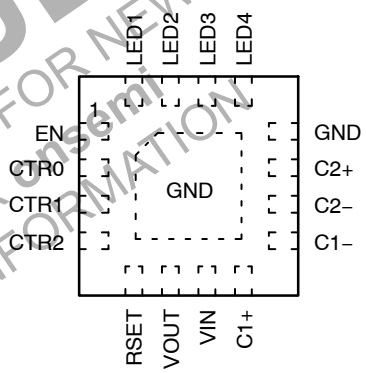
TQFN−16
HV4 SUFFIX
CASE 510AE

PIN CONNECTIONS (Note 1)



(4 x 4 mm) (Top View)

MARKING DIAGRAMS

E364 = CAT3604AHV4−T2

ORDERING INFORMATION

Device	Package	Shipping
CAT3604AHV4−T2	TQFN−16 (Note 2)	2,000/ Tape & Reel

1. The package exposed pad is electrically connected inside the package to GND and to pin 12.
2. Matte−Tin Plated Finish (RoHS−compliant).

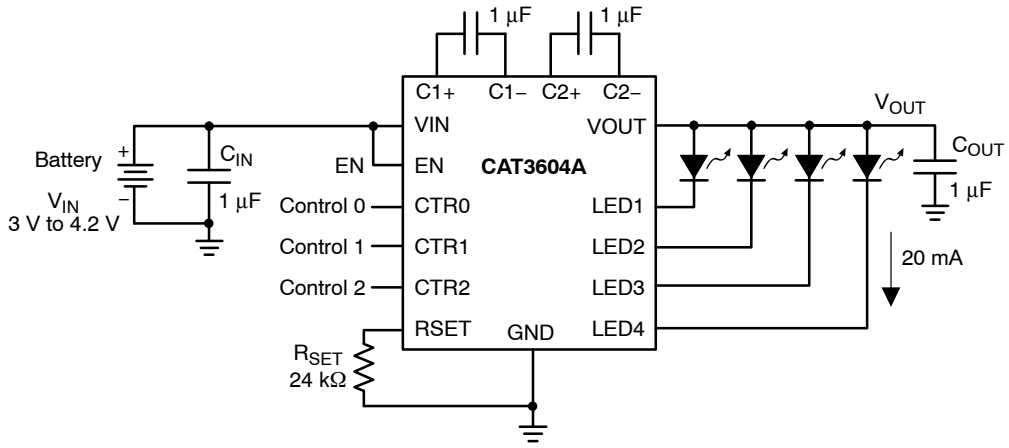


Figure 1. Typical Application Circuit

Table 1. PIN DESCRIPTION

Pin #	Name	Function
1	EN	Enable input, active HIGH
2	CTR0	Digital control input 0
3	CTR1	Digital control input 1
4	CTR2	Digital control input 2
5	RSET	The LED output current is set by the current sourced out of the RSET pin
6	VOUT	Charge pump output connected to the LED anodes
7	VIN	Supply voltage
8	C1+	Bucket capacitor 1 terminal
9	C1	Bucket capacitor 1 terminal
10	C2	Bucket capacitor 2 terminal
11	C2+	Bucket capacitor 2 terminal
12	GND	Ground reference
13	LED4	LED 4 cathode terminal
14	LED3	LED 3 cathode terminal
15	LED2	LED 2 cathode terminal
16	LED1	LED 1 cathode terminal
Pad	GND Pad	Ground reference

Table 2. ABSOLUTE MAXIMUM RATINGS

Parameter	Rating	Unit
VIN, VOUT, LEDx voltage	−0.3 to 7.0	V
EN, CTRx voltage	−0.3 to VIN	V
RSET voltage	−0.3 to VIN	V
RSET current	±1	mA
Ambient Temperature Range	−40 to +85	°C
Storage Temperature Range	−65 to +160	°C
Lead Temperature	300	°C
ESD Rating HBM (Human Body Model)	2,000	V
ESD Rating MM (Machine Model) (Note 3)	200	V

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

3. Machine model is with 200 pF capacitor discharged directly into each pin.

Table 3. RECOMMENDED OPERATING CONDITIONS

Parameter	Range	Unit
VIN	3.0 to 5.5	V
Ambient Temperature Range	−40 to +85	°C
Input/Output/Bucket Capacitors	1 ±20% typical	μF
I_{LED} per LED pin	0 to 30	mA

4. Typical application circuit with external components is shown on page 2.

Table 4. ELECTRICAL OPERATING CHARACTERISTICS

(Limits over recommended operating conditions unless specified otherwise. Typical values at T_A = 25°C, V_{IN} = 3.5 V, I_{RSET} = 5 μA)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
I_Q	Quiescent Current	V_{EN} = 0 V, Shutdown Mode 1x Mode, No Load 1.5x Mode, No Load		0.05 0.3 2.6	1 1 5	μA mA mA
V_{RSET}	RSET Regulated Voltage		1.17	1.2	1.23	V
I_{LED}	Programmed LED Current	I_{RSET} = 5 μA I_{RSET} = 37 μA I_{RSET} = 78 μA		2.4 15.0 30.0		mA
$I_{LED-ACC}$	LED Current Accuracy	0.5 mA ≤ I_{LED} ≤ 3 mA 3 mA ≤ I_{LED} ≤ 30 mA		±15 ±5		%
$I_{LED-DEV}$	LED Channel Matching	(I_{LED} − I_{LEDAVG}) / I_{LEDAVG}		±3		%
R_{OUT}	Output Resistance (Open Loop)	1x Mode 1.5x Mode, I_{OUT} = 100 mA		1.4 6.5	2.5 10	Ω
f_{OSC}	Charge Pump Frequency		0.8	1.0	1.3	MHz
$T_{DROPOUT}$	1x to 1.5x Mode Transition Dropout Delay		0.4	0.6	0.9	ms
I_{EN-CTR}	Input Leakage Current	On Inputs EN, CTR0, 1 & 2		0.001	1	μA
V_{EN-CTR}	High Detect Threshold Low	On Inputs EN, CTR0, 1 & 2	0.4	0.8	1.3	V
I_{SC}	Input Current Limit	VOUT = GND	30	45	60	mA
I_{LIM}	Maximum Input Current	VOUT > 1 V	200	400	600	mA

Block Diagram

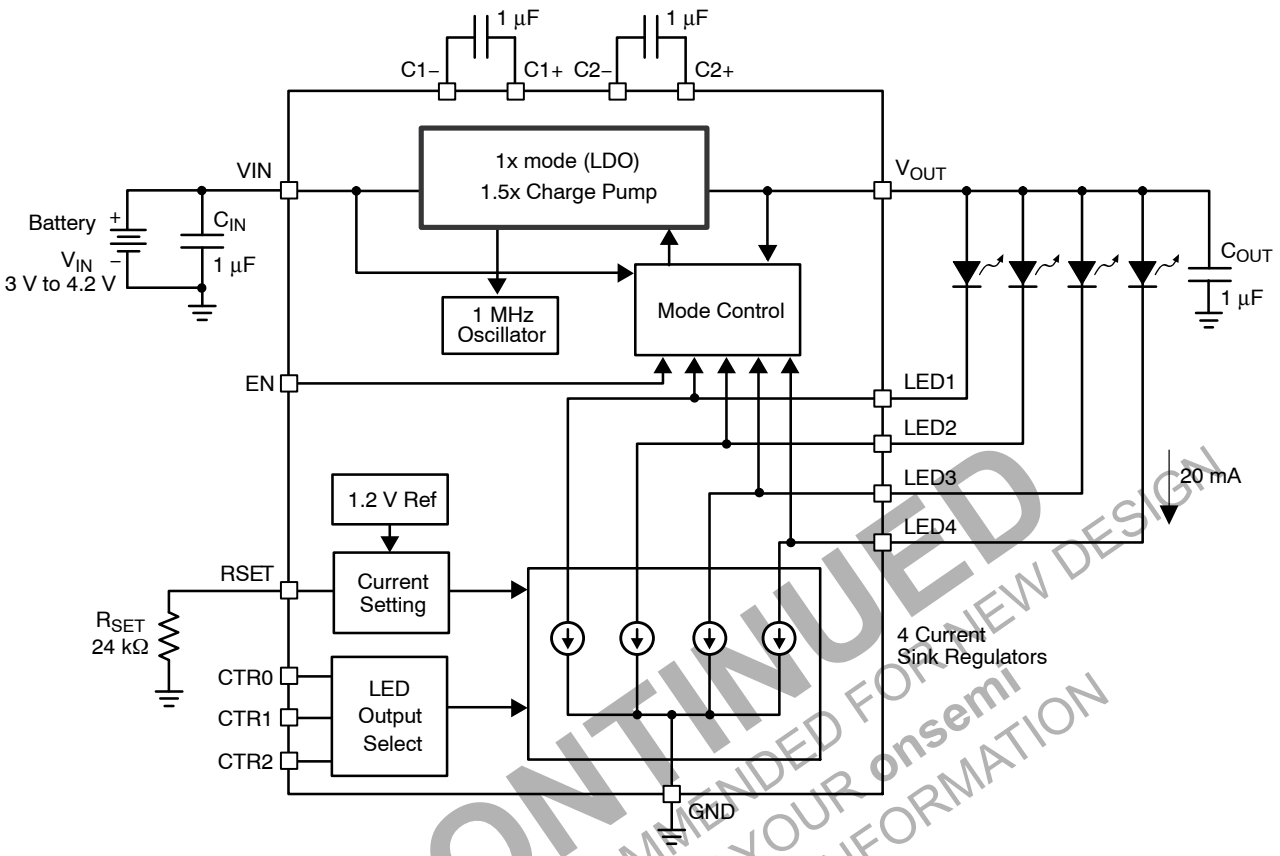


Figure 2. CAT3604A Functional Block Diagram

Basic Operation

At power-up, the CAT3604A starts operation in 1x mode. If it is able to drive the programmed LED current, it continues in 1x mode. If the battery voltage drops to a level where the LED current cannot be met, the driver automatically switches into 1.5x mode. The 1.5x charge pump will boost the output voltage accordingly to achieve the nominal LED current.

The operating mode is reinitialized each and every time the chip is powered up or is taken out of shutdown mode (via EN pin). The use of the control pins (CTR0, CTR1, CTR2) does not reconfigure the mode of operation.

LED Current Setting

The LED current is set by the external resistor R_{SET} connected between the RSET pin and ground. Table 5 lists various LED currents and the associated R_{SET} resistor value for standard 1% precision surface mount resistors.

The digital control lines CTR0, CTR1 and CTR2 allow to turn On or Off a combition of LEDs as shown in Table 6.

Table 5. RSET Resistor Selection

LED Current (mA)	R_{SET} (kΩ)
1	649
2	287
5	102
10	49.9
15	32.4
20	23.7
30	15.4

Table 6. LED Selection

Control Lines			LED Outputs			
CTR2	CTR1	CTR0	LED4	LED3	LED2	LED1
0	0	0	–	–	–	ON
0	0	1	–	–	ON	–
0	1	0	–	ON	–	–
0	1	1	ON	–	–	–
1	0	0	–	–	ON	ON
1	0	1	–	ON	ON	ON
1	1	0	ON	ON	ON	ON
1	1	1	–	–	–	–

NOTE: 1 = logic high (or VIN)
0 = logic low (or GND)
– = LED output OFF

TYPICAL CHARACTERISTICS

(V_{IN} = 3.6 V, EN = V_{IN}, C_{IN} = C_{OUT} = 1 μF, R_{SET} = 24 kΩ, T_{AMB} = 25°C, unless otherwise specified.)

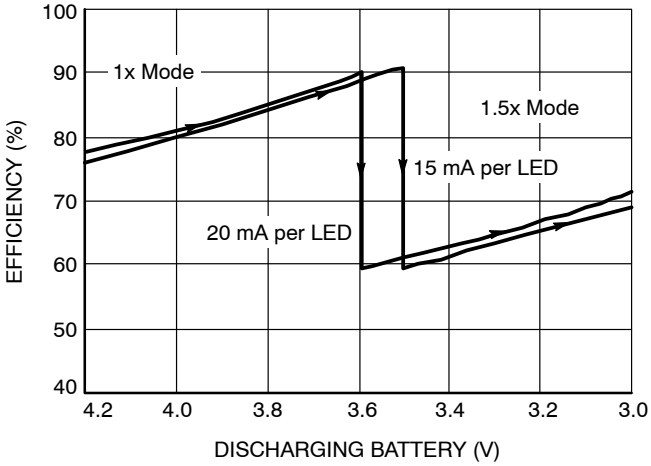


Figure 3. Efficiency vs. Input Voltage (4 LEDs)

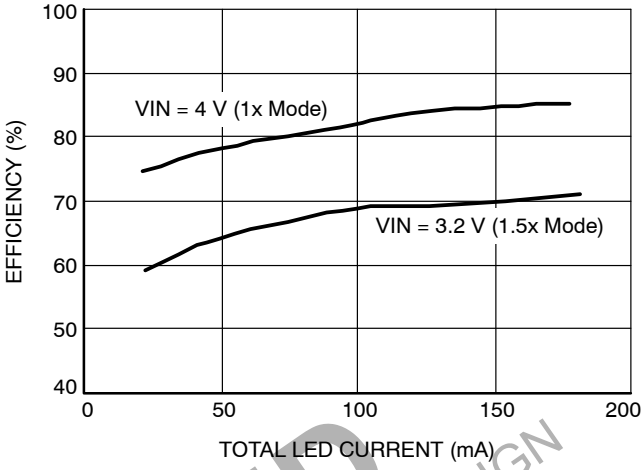


Figure 4. Efficiency vs. Total LED Current (4 LEDs)

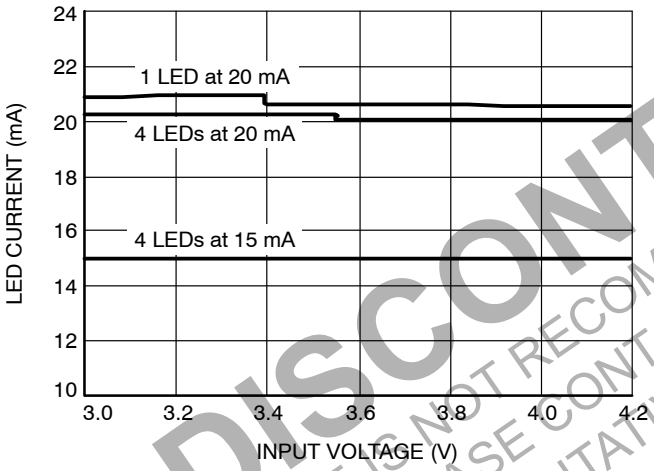


Figure 5. LED Current vs. Input Voltage

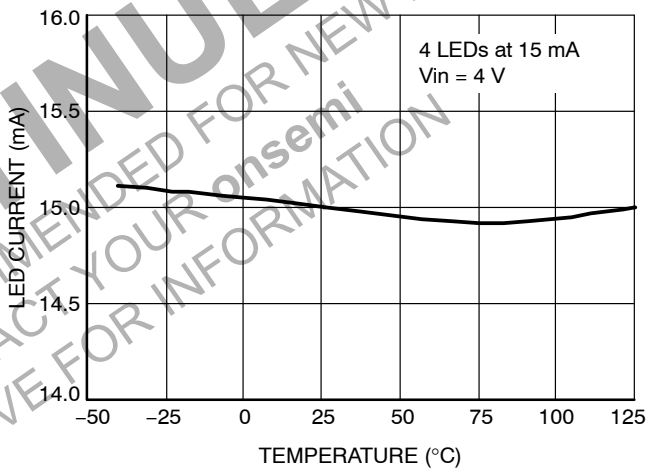


Figure 6. LED Current vs. Temperature

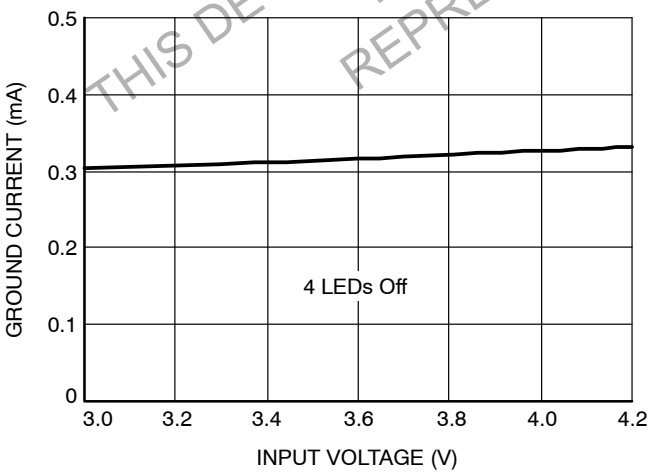


Figure 7. Ground Current vs. Input Voltage (1x Mode)

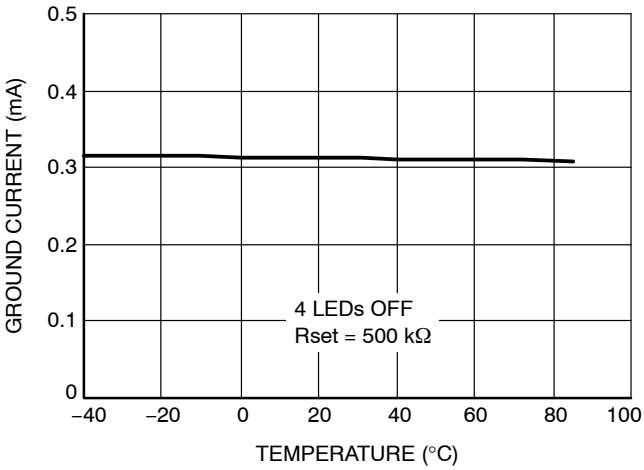


Figure 8. Ground Current vs. Temperature (1x Mode)

TYPICAL CHARACTERISTICS

(V_{IN} = 3.6 V, EN = V_{IN}, C_{IN} = C_{OUT} = 1 μF, R_{SET} = 24 kΩ, T_{AMB} = 25°C, unless otherwise specified.)

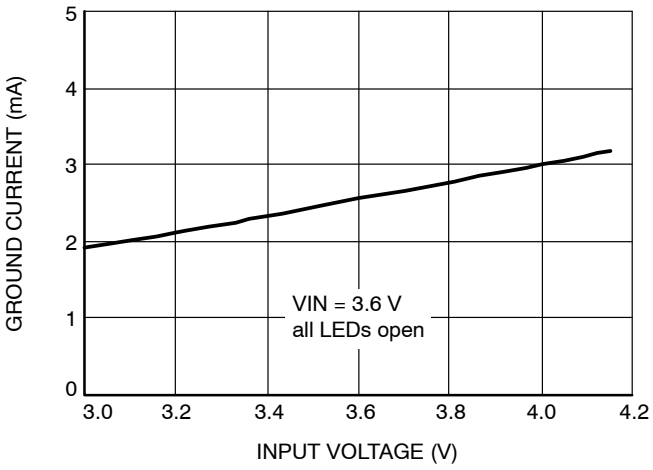


Figure 9. Ground Current vs. Input Voltage (1.5x Mode)

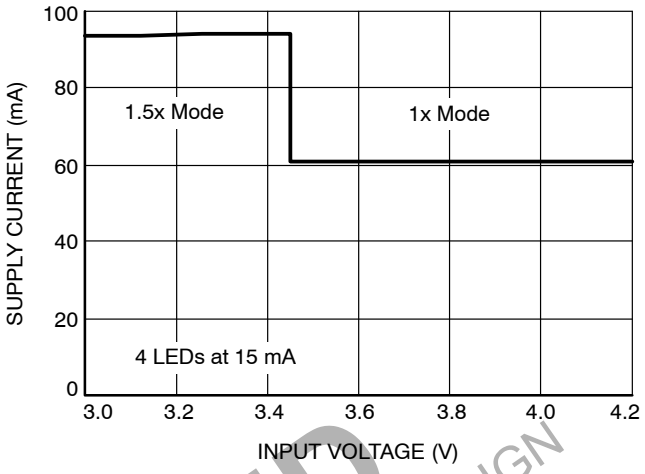


Figure 10. Supply Current vs. Input Voltage

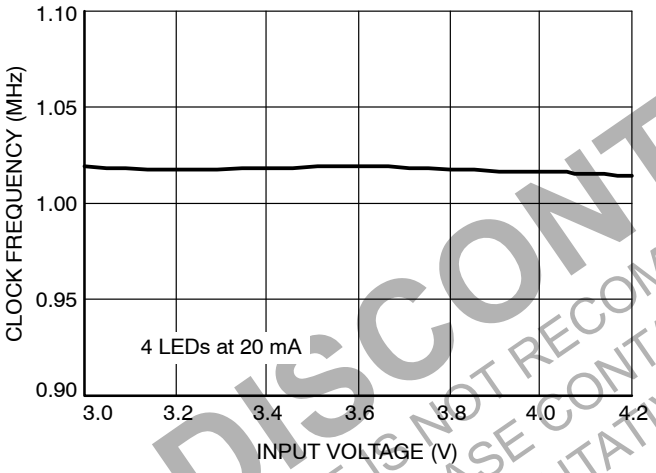


Figure 11. Oscillator Frequency vs. Input Voltage

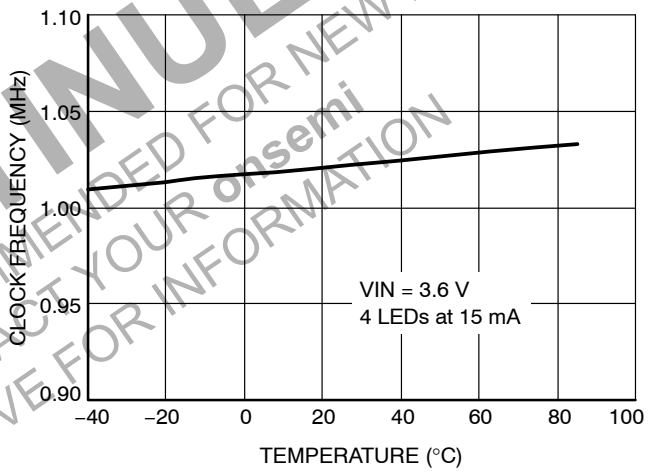


Figure 12. Oscillator Frequency vs. Temperature

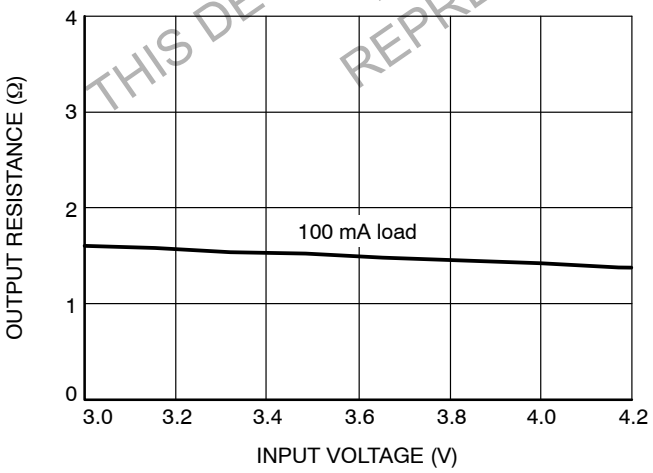


Figure 13. Output Resistance vs. Input Voltage (1x Mode)

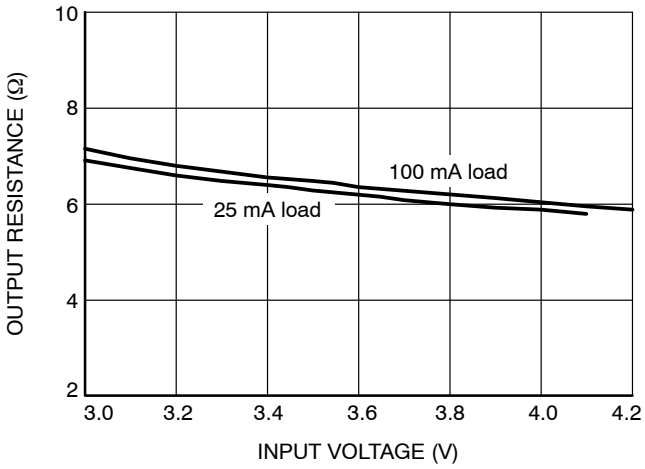


Figure 14. Output Resistance vs. Input Voltage (1.5x Mode)

TYPICAL CHARACTERISTICS

(V_{IN} = 3.6 V, EN = V_{IN}, C_{IN} = C_{OUT} = 1 μF, R_{SET} = 24 kΩ, T_{AMB} = 25°C, unless otherwise specified.)

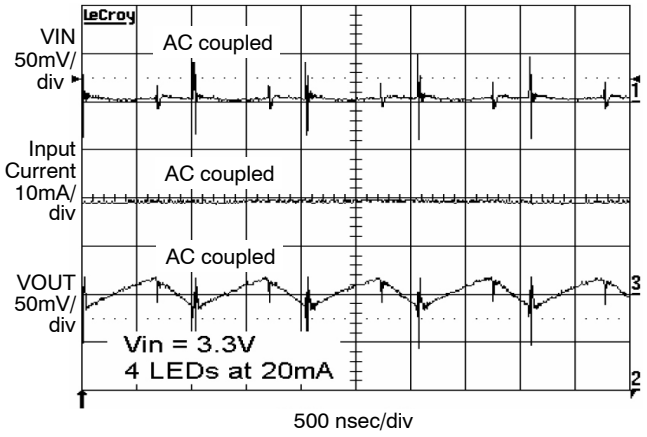


Figure 15. Switching Waveforms in 1.5x Mode

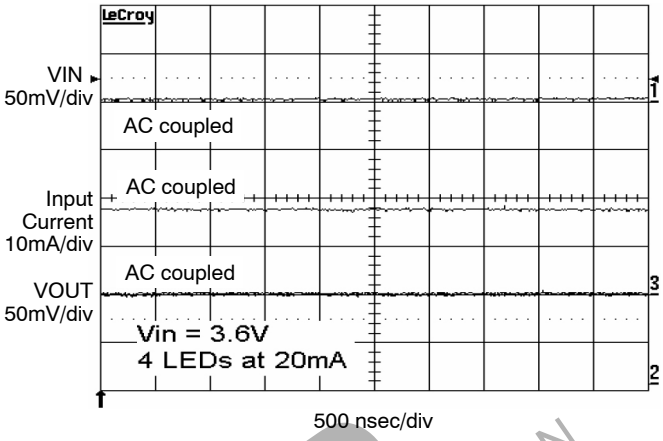


Figure 16. Operating Waveforms in 1x Mode

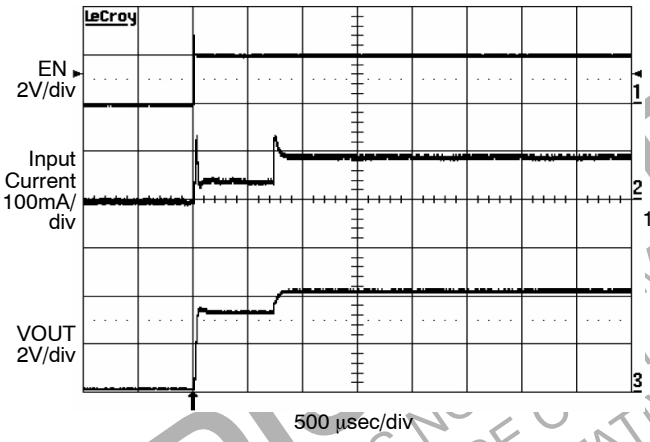


Figure 17. Power Up 4 LEDs at 15 mA, Vin = 3 V (1.5x Mode)

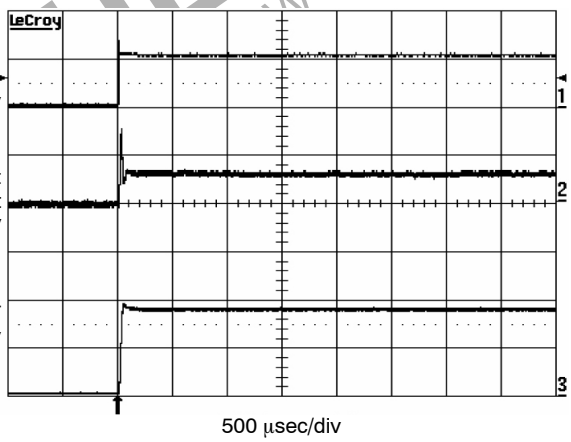


Figure 18. Power Up 4 LEDs at 15 mA, Vin = 3.6 V (1x Mode)

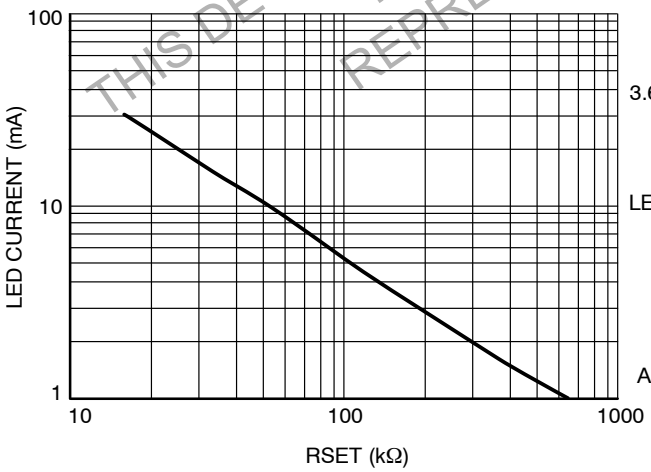


Figure 19. LED Current vs. R_{SET}

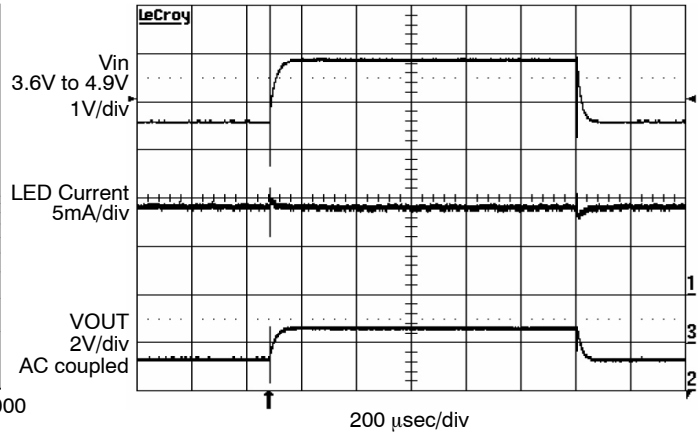


Figure 20. Line Transient Response in 1x Mode

TYPICAL CHARACTERISTICS

(V_{IN} = 3.6 V, EN = V_{IN}, C_{IN} = C_{OUT} = 1 μF, unless otherwise specified.)

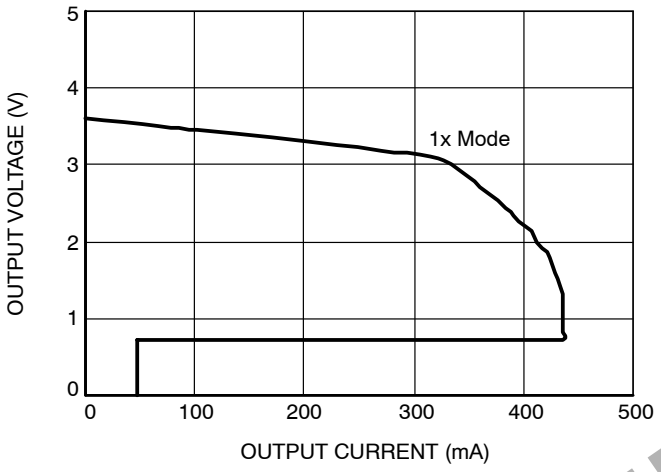


Figure 21. Foldback Current Limiting

Recommended Layout

When the driver is in the 1.5x charge pump mode, the 1 MHz switching frequency operation requires to minimize trace length and impedance to ground on all 4 capacitors. A ground plane should cover the area on the bottom side of the PCB opposite to the IC and the bypass capacitors. Capacitors Cin and Cout require short connection to ground which can be done with multiple vias as shown on Figure 22.

A square copper area matches the QFN16 exposed pad (GND) which is connected by a trace to the pin 12 pad (GND). A large via (metalized hole) centered in the square pad provides a low impedance connection to the ground plane on the opposite side of the PCB and allows the heat dissipated by the driver IC to spread out resulting in excellent thermal performance.

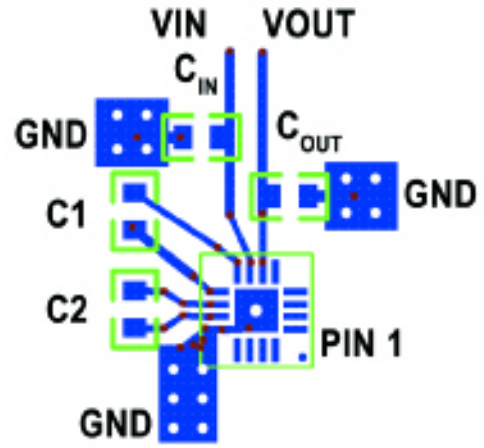


Figure 22. PCB Layout

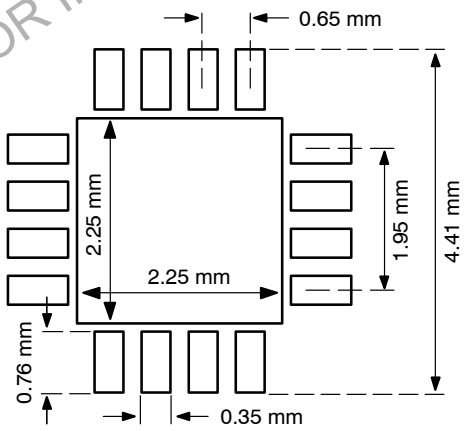


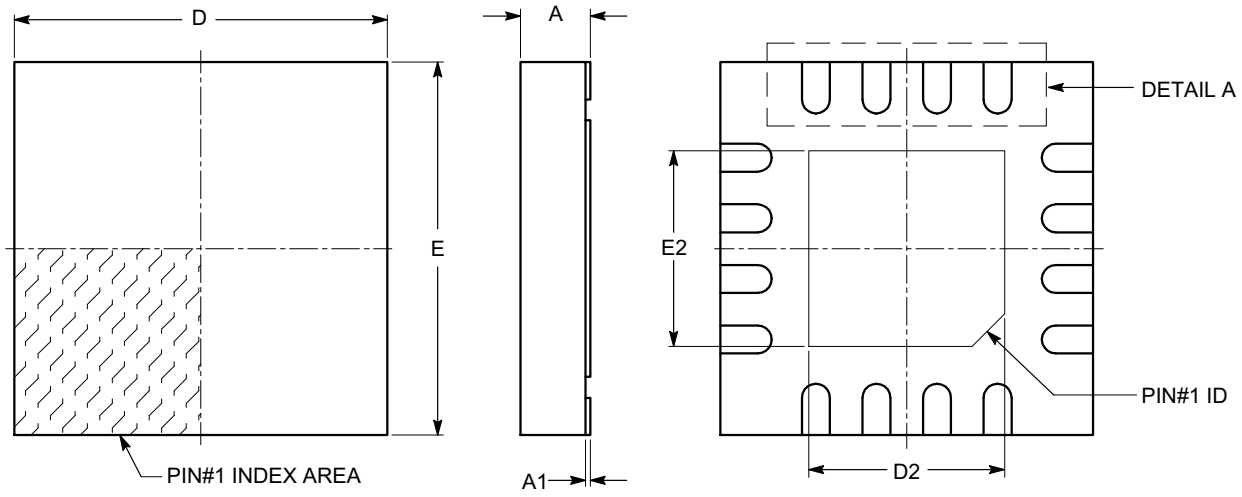
Figure 23. Recommended QFN 16 Package Land Pattern

MECHANICAL CASE OUTLINE
PACKAGE DIMENSIONS

ON Semiconductor®

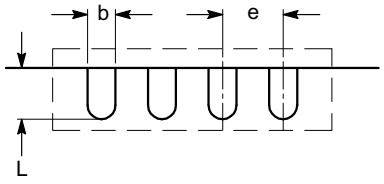
TQFN16, 4x4
CASE 510AE−01
ISSUE A

DATE 18 MAR 2009

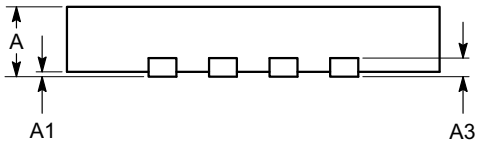


TOP VIEW **SIDE VIEW** **BOTTOM VIEW**

SYMBOL	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
A3		0.20 REF	
b	0.25	0.30	0.35
D	3.90	4.00	4.10
D2	2.00	−−−	2.25
E	3.90	4.00	4.10
E2	2.00	−−−	2.25
e		0.65 BSC	
L	0.45	−−−	0.65



DETAIL A



FRONT VIEW

Notes:
(1) All dimensions are in millimeters.
(2) Complies with JEDEC MO-220.

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DESCRIPTION:	**TQFN16, 4X4**	**PAGE 1 OF 1**

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