

MC34VR5100A2EP :Part Detail

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General Information

Part Number	MC34VR5100A2EP
Description	Power Management IC, i.MX7, pre-prog, 4 buck, 6 LDO, 1 boost, QFN 48, Tray
Product Line	G32PF300
PTI	ANPG
Material Type	Tested Packaged Device
Life Cycle Description (code)	PRODUCT NEWLY INTRO D/RAMP-UP
Status	Active
Application/Qualification Tier	10-YEARS APPLICATION LIFE

Package Information

Package Type and Termination Count	QFN-EP 48
Package Description and Mechanical Drawing	QFN-EP 48 7*7*0.85 P0.5
Device Weight(g)	0.13950
Package Length (nominal)(mm)	7.000
Package Width (nominal)(mm)	7.000
Package Thickness (nominal)(mm)	0.850
Tape & Reel	No

Environmental and Compliance Information

Pb-Free	
RoHS Compliant	
Halogen Free	Yes
Material Composition Declaration (MCD)	Download MCD Report Download MCD Report
RoHS Certificate of Analysis (CoA)	Download RoHS CoA Report
2nd Level Interconnect	e3
Moisture Sensitivity Level (MSL)	3
Floor Life	168 HOURS
Peak Package Body Temperature	260

(PPT)(°C)

Maximum Time at Peak Temperature (s)	40
Number of Reflow Cycles	3
REACH SVHC	NXP REACH Statement
UL94 (plastics flammability test)	V0;Burning stops within 10 seconds on a vertical specimen;no drips allowed

Manufacturing Information

Micron Size(μm)	.13
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Ordering Information

Minimum Package Quantity (MPQ)	260
MPQ Container	TRAY
Exempt from Minimum Delivery Value	Yes
Preferred Order Quantity (POQ)	1300
POQ Container	BOX
Export Control Classification Number (US)	EAR99
Harmonized Tariff (	
CCATS Document	-
ENC Status	-
Other Trade Compliance Documents	-
Budgetary Price excluding tax(US Currency	1000 @ US each

Order

Operating Characteristics

Additional Features - Power Management	Coincell RTC
Ambient Operating Temperature (Min-Max) (°C)	-40 to 105
Battery Charger	Coin cell
Device Function	PMICs for LS1 Processors
Interface and Input Control	I2C
MCU Supported	LS1024A
Num. of Linear/LDO Regulators	6
Number of Regulators-Boost Switching	1
Number of Regulators-Buck Switching	3
Sample Exception Availability	N

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