

ON Semiconductor

Is Now



To learn more about onsemi™, please visit our website at
www.onsemi.com

onsemi and onsemi. and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use onsemi products for any such unintended or unauthorized application, Buyer shall indemnify and hold onsemi and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that onsemi was negligent regarding the design or manufacture of the part. onsemi is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner. Other names and brands may be claimed as the property of others.

LM358S, LM2904S

Single Supply Dual Operational Amplifiers

Utilizing the circuit designs perfected for Quad Operational Amplifiers, these dual operational amplifiers feature low power drain, a common mode input voltage range extending to ground/ V_{EE} , and single supply or split supply operation. The LM358S and LM2904S are half of the LM324S and LM2902S, respectively.

These amplifiers have several distinct advantages over standard operational amplifier types in single supply applications. The common mode input range includes the negative supply, thereby eliminating the necessity for external biasing components in many applications. The output voltage range also includes the negative power supply voltage.

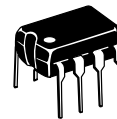
Features

- Short Circuit Protected Outputs
- True Differential Input Stage
- Single Supply Operation: 3.0 V to 32 V
- Low Input Bias Currents
- Internally Compensated
- Common Mode Range Extends to Negative Supply
- Single and Split Supply Operation
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant



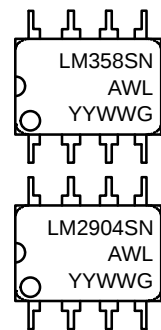
ON Semiconductor®

<http://onsemi.com>



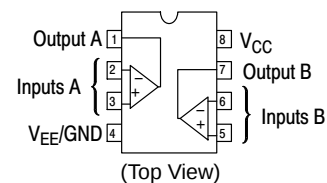
**PDIIP-8
N SUFFIX
CASE 626**

MARKING DIAGRAMS



LMxxxx = Specific Device Code
A, AL = Assembly Location
WL = Wafer Lot
Y, YY = Year
W, WW = Work Week
G or ■ = Pb-Free Package

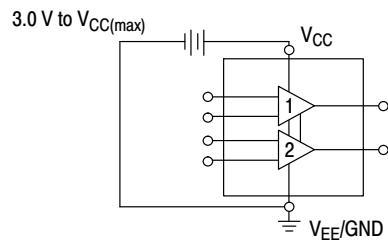
PIN CONNECTIONS



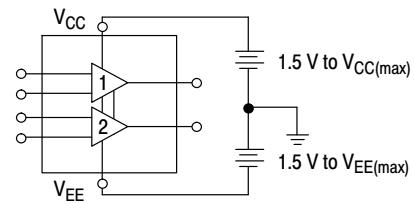
ORDERING INFORMATION

See detailed ordering and shipping information on page 8 of this data sheet.

LM358S, LM2904S



Single Supply



Split Supplies

Figure 1.

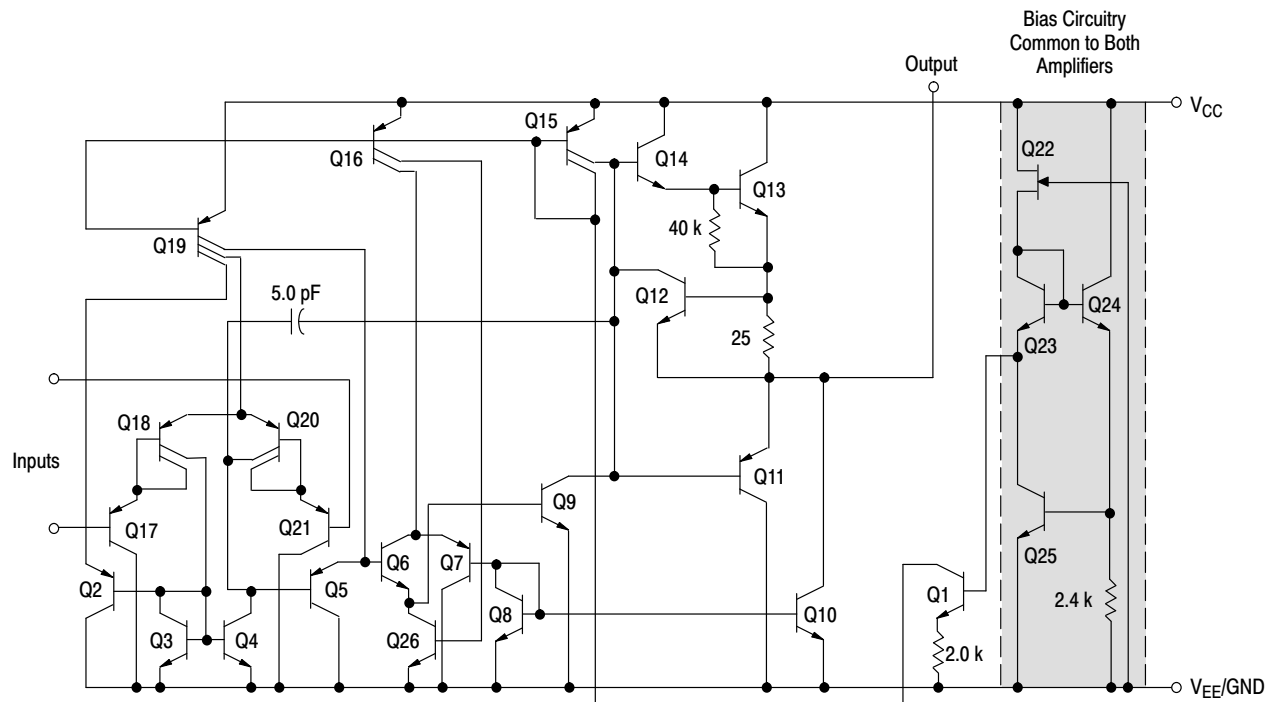


Figure 2. Representative Schematic Diagram
(One-Half of Circuit Shown)

LM358S, LM2904S

MAXIMUM RATINGS (T_A = +25°C, unless otherwise noted.)

Rating	Symbol	Value	Unit
Power Supply Voltages Single Supply Split Supplies	V _{CC} V _{CC} , V _{EE}	32 ±16	Vdc
Input Differential Voltage Range (Note 1)	V _{IDR}	±32	Vdc
Input Common Mode Voltage Range (Note 2)	V _{ICR}	−0.3 to 32	Vdc
Output Short Circuit Duration	t _{SC}	Continuous	
Junction Temperature	T _J	150	°C
Thermal Resistance, Junction-to-Air (Note 3) Case 626	R _{θJA}	161	°C/W
Storage Temperature Range	T _{stg}	−65 to +150	°C
Operating Ambient Temperature Range LM358S LM2904S	T _A	0 to +70 −40 to +105	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Split Power Supplies.
2. For supply voltages less than 32 V the absolute maximum input voltage is equal to the supply voltage.
3. All R_{θJA} measurements made on evaluation board with 1 oz. copper traces of minimum pad size. All device outputs were active.

LM358S, LM2904S

ELECTRICAL CHARACTERISTICS ($V_{CC} = 5.0\text{ V}$, $V_{EE} = \text{GND}$, $T_A = 25^\circ\text{C}$, unless otherwise noted.)

Characteristic	Symbol	LM358S			Unit
		Min	Typ	Max	
Input Offset Voltage $V_{CC} = 5.0\text{ V}$ to 30 V , $V_{IC} = 0\text{ V}$ to $V_{CC} - 1.7\text{ V}$, $V_O \approx 1.4\text{ V}$, $R_S = 0\ \Omega$ $T_A = 25^\circ\text{C}$ $T_A = T_{\text{high}}$ (Note 4) $T_A = T_{\text{low}}$ (Note 4)	V_{IO}	–	2.0	7.0	mV
		–	–	9.0	
		–	–	9.0	
Average Temperature Coefficient of Input Offset Voltage $T_A = T_{\text{high}}$ to T_{low} (Note 4)	$\Delta V_{IO}/\Delta T$	–	7.0	–	$\mu\text{V}/^\circ\text{C}$
Input Offset Current $T_A = T_{\text{high}}$ to T_{low} (Note 4)	I_{IO}	–	5.0	50	nA
		–	–	150	
Input Bias Current $T_A = T_{\text{high}}$ to T_{low} (Note 4)	I_{IB}	–	–45	–250	nA
		–	–50	–500	
Average Temperature Coefficient of Input Offset Current $T_A = T_{\text{high}}$ to T_{low} (Note 4)	$\Delta I_{IO}/\Delta T$	–	10	–	$\text{pA}/^\circ\text{C}$
Input Common Mode Voltage Range (Note 5), $V_{CC} = 30\text{ V}$ $V_{CC} = 30\text{ V}$, $T_A = T_{\text{high}}$ to T_{low}	V_{ICR}	0	–	28.3	V
		0	–	28	
Differential Input Voltage Range	V_{IDR}	–	–	V_{CC}	V
Large Signal Open Loop Voltage Gain $R_L = 2.0\text{ k}\Omega$, $V_{CC} = 15\text{ V}$, For Large V_O Swing, $T_A = T_{\text{high}}$ to T_{low} (Note 4)	A_{VOL}	25	100	–	V/mV
		15	–	–	
Channel Separation $1.0\text{ kHz} \leq f \leq 20\text{ kHz}$, Input Referenced	CS	–	–120	–	dB
Common Mode Rejection $R_S \leq 10\text{ k}\Omega$	CMR	65	70	–	dB
Power Supply Rejection	PSR	65	100	–	dB
Output Voltage–High Limit $V_{CC} = 5.0\text{ V}$, $R_L = 2.0\text{ k}\Omega$, $T_A = 25^\circ\text{C}$ $V_{CC} = 30\text{ V}$, $R_L = 2.0\text{ k}\Omega$, $T_A = T_{\text{high}}$ to T_{low} (Note 4) $V_{CC} = 30\text{ V}$, $R_L = 10\text{ k}\Omega$, $T_A = T_{\text{high}}$ to T_{low} (Note 4)	V_{OH}	3.3	3.5	–	V
		26	–	–	
		27	28	–	
Output Voltage–Low Limit $V_{CC} = 5.0\text{ V}$, $R_L = 10\text{ k}\Omega$, $T_A = T_{\text{high}}$ to T_{low} (Note 4)	V_{OL}	–	5.0	20	mV
Output Source Current $V_{ID} = +1.0\text{ V}$, $V_{CC} = 15\text{ V}$	I_{O+}	20	45	–	mA
Output Sink Current $V_{ID} = -1.0\text{ V}$, $V_{CC} = 15\text{ V}$ $V_{ID} = -1.0\text{ V}$, $V_O = 200\text{ mV}$	I_{O-}	10	30	–	mA
		12	40	–	μA
Output Short Circuit to Ground (Note 6)	I_{SC}	–	45	60	mA
Power Supply Current (Total Device) $T_A = T_{\text{high}}$ to T_{low} (Note 4) $V_{CC} = 30\text{ V}$, $V_O = 0\text{ V}$, $R_L = \infty$ $V_{CC} = 5\text{ V}$, $V_O = 0\text{ V}$, $R_L = \infty$	I_{CC}	–	0.5	3.0	mA
		–	0.3	1.2	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. LM358S: $T_{\text{low}} = 0^\circ\text{C}$, $T_{\text{high}} = +70^\circ\text{C}$

LM2904S: $T_{\text{low}} = -40^\circ\text{C}$, $T_{\text{high}} = +105^\circ\text{C}$

5. The input common mode voltage or either input signal voltage should not be allowed to go negative by more than 0.3 V. The upper end of the common mode voltage range is $V_{CC} - 1.7\text{ V}$.

6. Short circuits from the output to V_{CC} can cause excessive heating and eventual destruction. Destructive dissipation can result from simultaneous shorts on all amplifiers.

LM358S, LM2904S

ELECTRICAL CHARACTERISTICS ($V_{CC} = 5.0\text{ V}$, $V_{EE} = \text{GND}$, $T_A = 25^\circ\text{C}$, unless otherwise noted.)

Characteristic	Symbol	LM2904S			Unit
		Min	Typ	Max	
Input Offset Voltage $V_{CC} = 5.0\text{ V}$ to 30 V , $V_{IC} = 0\text{ V}$ to $V_{CC} - 1.7\text{ V}$, $V_O \approx 1.4\text{ V}$, $R_S = 0\ \Omega$ $T_A = 25^\circ\text{C}$ $T_A = T_{\text{high}}$ (Note 7) $T_A = T_{\text{low}}$ (Note 7)	V_{IO}	–	2.0	7.0	mV
Average Temperature Coefficient of Input Offset Voltage $T_A = T_{\text{high}}$ to T_{low} (Note 7)	$\Delta V_{IO}/\Delta T$	–	7.0	–	$\mu\text{V}/^\circ\text{C}$
Input Offset Current $T_A = T_{\text{high}}$ to T_{low} (Note 7)	I_{IO}	–	5.0	50	nA
Input Bias Current $T_A = T_{\text{high}}$ to T_{low} (Note 7)	I_{IB}	–	–45	–250	nA
Average Temperature Coefficient of Input Offset Current $T_A = T_{\text{high}}$ to T_{low} (Note 7)	$\Delta I_{IO}/\Delta T$	–	10	–	$\text{pA}/^\circ\text{C}$
Input Common Mode Voltage Range (Note 8), $V_{CC} = 30\text{ V}$ $V_{CC} = 30\text{ V}$, $T_A = T_{\text{high}}$ to T_{low}	V_{ICR}	0	–	28.3	V
Differential Input Voltage Range	V_{IDR}	–	–	V_{CC}	V
Large Signal Open Loop Voltage Gain $R_L = 2.0\text{ k}\Omega$, $V_{CC} = 15\text{ V}$, For Large V_O Swing, $T_A = T_{\text{high}}$ to T_{low} (Note 7)	A_{VOL}	25	100	–	V/mV
Channel Separation $1.0\text{ kHz} \leq f \leq 20\text{ kHz}$, Input Referenced	CS	–	–120	–	dB
Common Mode Rejection $R_S \leq 10\text{ k}\Omega$	CMR	50	70	–	dB
Power Supply Rejection	PSR	50	100	–	dB
Output Voltage–High Limit $V_{CC} = 5.0\text{ V}$, $R_L = 2.0\text{ k}\Omega$, $T_A = 25^\circ\text{C}$ $V_{CC} = 30\text{ V}$, $R_L = 2.0\text{ k}\Omega$, $T_A = T_{\text{high}}$ to T_{low} (Note 7) $V_{CC} = 30\text{ V}$, $R_L = 10\text{ k}\Omega$, $T_A = T_{\text{high}}$ to T_{low} (Note 7)	V_{OH}	3.3	3.5	–	V
Output Voltage–Low Limit $V_{CC} = 5.0\text{ V}$, $R_L = 10\text{ k}\Omega$, $T_A = T_{\text{high}}$ to T_{low} (Note 7)	V_{OL}	–	5.0	20	mV
Output Source Current $V_{ID} = +1.0\text{ V}$, $V_{CC} = 15\text{ V}$	I_{O+}	20	45	–	mA
Output Sink Current $V_{ID} = -1.0\text{ V}$, $V_{CC} = 15\text{ V}$ $V_{ID} = -1.0\text{ V}$, $V_O = 200\text{ mV}$	I_{O-}	10	30	–	mA
Output Short Circuit to Ground (Note 9)	I_{SC}	–	45	60	mA
Power Supply Current (Total Device) $T_A = T_{\text{high}}$ to T_{low} (Note 7) $V_{CC} = 30\text{ V}$, $V_O = 0\text{ V}$, $R_L = \infty$ $V_{CC} = 5\text{ V}$, $V_O = 0\text{ V}$, $R_L = \infty$	I_{CC}	–	0.5	3.0	mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

7. LM358S: $T_{\text{low}} = 0^\circ\text{C}$, $T_{\text{high}} = +70^\circ\text{C}$

LM2904S: $T_{\text{low}} = -40^\circ\text{C}$, $T_{\text{high}} = +105^\circ\text{C}$

8. The input common mode voltage or either input signal voltage should not be allowed to go negative by more than 0.3 V . The upper end of the common mode voltage range is $V_{CC} - 1.7\text{ V}$.

9. Short circuits from the output to V_{CC} can cause excessive heating and eventual destruction. Destructive dissipation can result from simultaneous shorts on all amplifiers.

LM358S, LM2904S

CIRCUIT DESCRIPTION

The LM358S and LM2904S are made using two internally compensated, two-stage operational amplifiers. The first stage of each consists of differential input devices Q20 and Q18 with input buffer transistors Q21 and Q17 and the differential to single ended converter Q3 and Q4. The first stage performs not only the first stage gain function but also performs the level shifting and transconductance reduction functions. By reducing the transconductance, a smaller compensation capacitor (only 5.0 pF) can be employed, thus saving chip area. The transconductance reduction is accomplished by splitting the collectors of Q20

and Q18. Another feature of this input stage is that the input common mode range can include the negative supply or ground, in single supply operation, without saturating either the input devices or the differential to single-ended converter. The second stage consists of a standard current source load amplifier stage.

Each amplifier is biased from an internal-voltage regulator which has a low temperature coefficient thus giving each amplifier good temperature characteristics as well as excellent power supply rejection.

LM358S, LM2904S

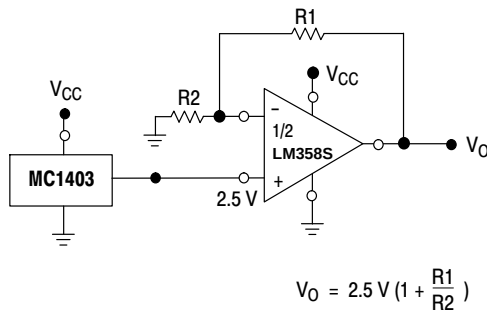


Figure 3. Voltage Reference

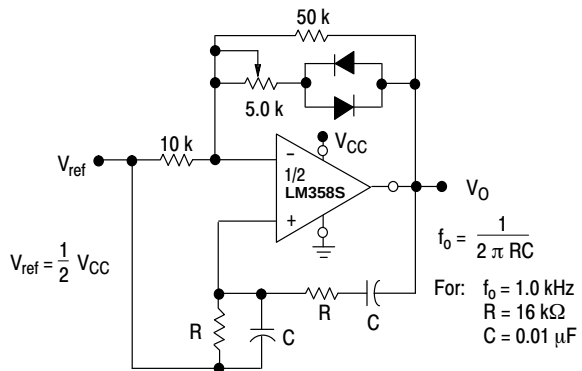


Figure 4. Wien Bridge Oscillator

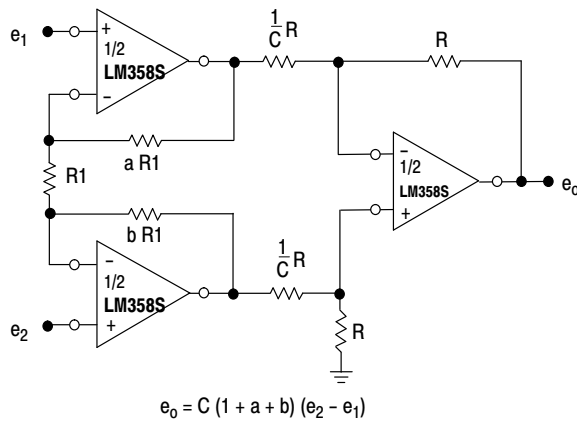


Figure 5. High Impedance Differential Amplifier

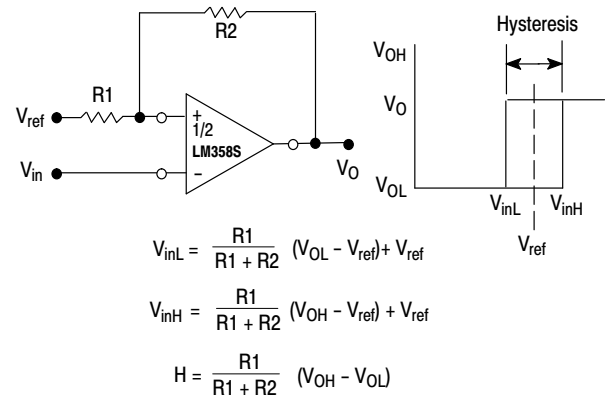


Figure 6. Comparator with Hysteresis

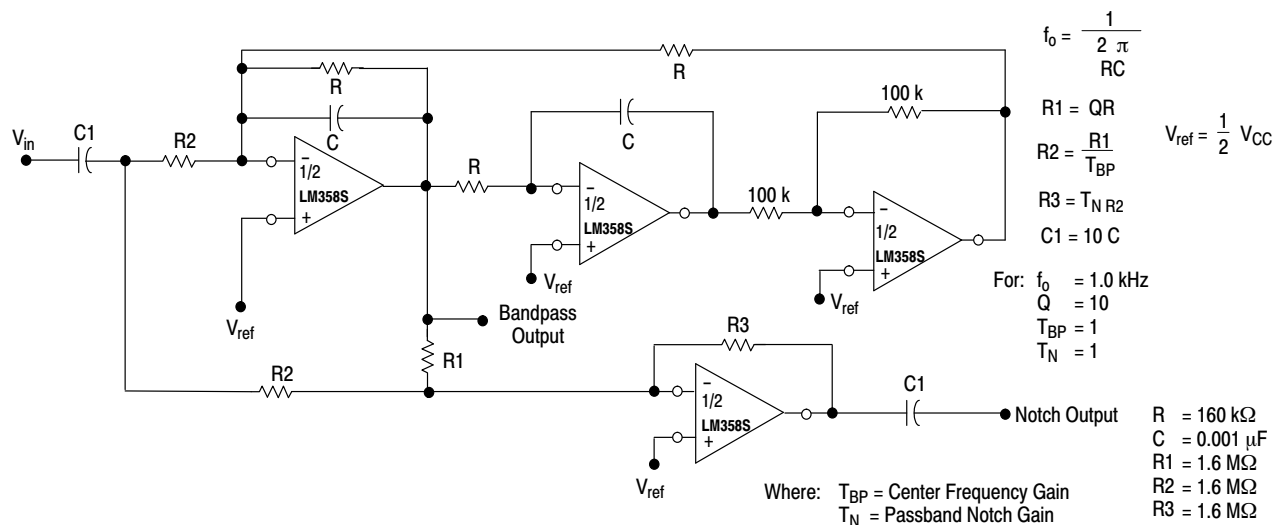
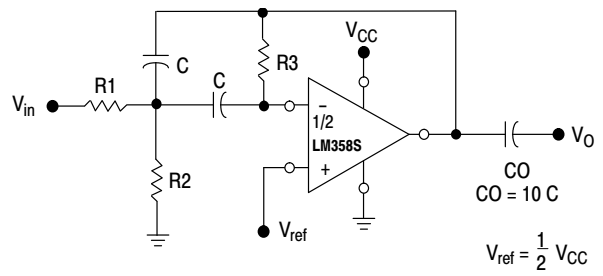


Figure 7. Bi-Quad Filter

LM358S, LM2904S



Given: f_0 = center frequency
 $A(f_0)$ = gain at center frequency

Choose value f_0, C

$$\text{Then: } R3 = \frac{Q}{\pi f_0 C}$$

$$R1 = \frac{R3}{2 A(f_0)}$$

$$R2 = \frac{R1 R3}{4 Q^2 R1 - R3}$$

For less than 10% error from operational amplifier. $\frac{Q_0 f_0}{BW} < 0.1$

Where f_0 and BW are expressed in Hz.

If source impedance varies, filter may be preceded with voltage follower buffer to stabilize filter parameters.

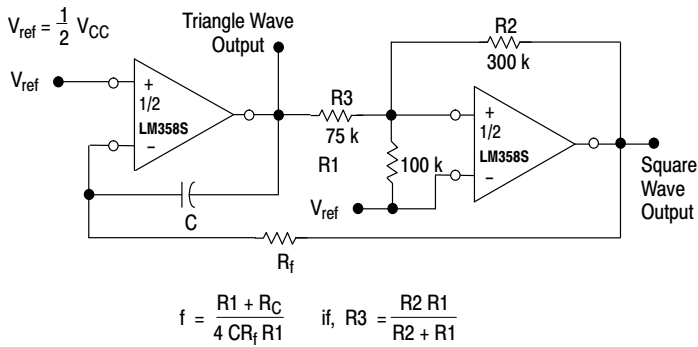


Figure 8. Function Generator

Figure 9. Multiple Feedback Bandpass Filter

ORDERING INFORMATION

Device	Operating Temperature Range	Package	Shipping [†]
LM358SNG	0°C to +70°C	PDIP-8 (Pb-Free)	50 Units / Rail
LM2904SNG	-40°C to +105°C	PDIP-8 (Pb-Free)	50 Units / Rail

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS

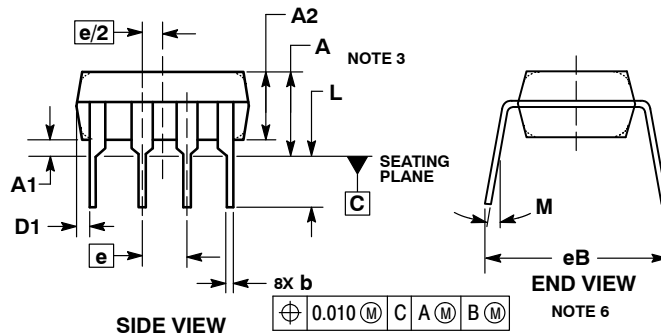
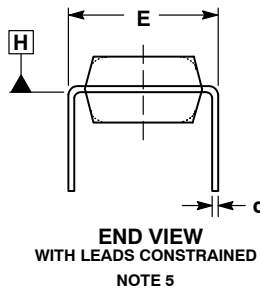
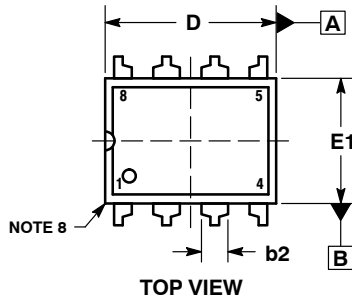
ON Semiconductor®



SCALE 1:1

PDIP-8
CASE 626-05
ISSUE P

DATE 22 APR 2015

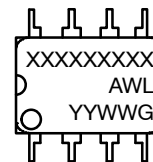


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: INCHES.
3. DIMENSIONS A, A1 AND L ARE MEASURED WITH THE PACKAGE SEATED IN JEDEC SEATING PLANE GAUGE GS-3.
4. DIMENSIONS D, D1 AND E1 DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS ARE NOT TO EXCEED 0.10 INCH.
5. DIMENSION E IS MEASURED AT A POINT 0.015 BELOW DATUM PLANE H WITH THE LEADS CONSTRAINED PERPENDICULAR TO DATUM C.
6. DIMENSION eB IS MEASURED AT THE LEAD TIPS WITH THE LEADS UNCONSTRAINED.
7. DATUM PLANE H IS COINCIDENT WITH THE BOTTOM OF THE LEADS, WHERE THE LEADS EXIT THE BODY.
8. PACKAGE CONTOUR IS OPTIONAL (ROUNDED OR SQUARE CORNERS).

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
A	---	0.210	---	5.33
A1	0.015	---	0.38	---
A2	0.115	0.195	2.92	4.95
b	0.014	0.022	0.35	0.56
b2	0.060 TYP		1.52 TYP	
C	0.008	0.014	0.20	0.36
D	0.355	0.400	9.02	10.16
D1	0.005	---	0.13	---
E	0.300	0.325	7.62	8.26
E1	0.240	0.280	6.10	7.11
e	0.100 BSC		2.54 BSC	
eB	---	0.430	---	10.92
L	0.115	0.150	2.92	3.81
M	---	10°	---	10°

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code
A = Assembly Location
WL = Wafer Lot
YY = Year
WW = Work Week
G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present.

STYLE 1:

- PIN 1: AC IN
2. DC + IN
3. DC - IN
4. AC IN
5. GROUND
6. OUTPUT
7. AUXILIARY
8. V_{CC}

DOCUMENT NUMBER:	98ASB42420B	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
DESCRIPTION:	PDIP-8	PAGE 1 OF 1

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

ON Semiconductor and  are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Email Requests to: orderlit@onsemi.com

ON Semiconductor Website: www.onsemi.com

TECHNICAL SUPPORT

North American Technical Support:

Voice Mail: 1 800-282-9855 Toll Free USA/Canada

Phone: 011 421 33 790 2910

Europe, Middle East and Africa Technical Support:

Phone: 00421 33 790 2910

For additional information, please contact your local Sales Representative

