

## USB2.0 High-Speed (480Mbps) Signal Switch With Vbus Short Protection

### Features

- USB 2.0 compliant (high speed and full speed)
- $R_{ON}$  is  $4.3\Omega$  typical @  $V_{DD} = 3.0V$
- Low bit-to-bit skew
- Low Crosstalk:  $-41dB$  @  $240\text{ MHz}$
- Off Isolation:  $-35dB$  @  $240\text{ MHz}$
- Wide  $-3dB$  bandwidth:  $850\text{ MHz}$
- Near-Zero propagation delay:  $250ps$
- Support for  $1.8V/2.5V/3.3V$  Logic on Control pins
- Channel On Capacitance:  $6.0pF$
- VDD Operating Range:  $1.8V$  to  $4.3V \pm 10\%$
- Data pin I/O ESD:  $>8kV$  HBM protection per JESD22-A114D specification
- I/O pins have over-voltage protection and can tolerate a short to Vbus
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](https://www.diodes.com/quality/product-definitions/) or your local Diodes representative.
- Packaging (Pb-free & Green):
  - 10-pin UQFN (ZM10)

### Applications

- Route signals for USB 2.0
- Cell phone, PDA, Digital camera and Notebook
- LCD Monitor ,TV, Set-top box
- Portable device

### Logic Function Table

/OE	SEL	Function
H	X	I/O's = Hi-Z
L	L	D(+/-) to D(+/-)A
L	H	D(+/-) to D(+/-)B

#### Notes:

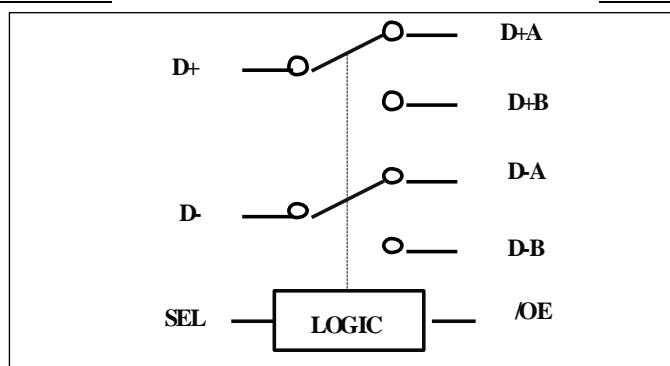
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain  $<900ppm$  bromine,  $<900ppm$  chlorine ( $<1500ppm$  total Br + Cl) and  $<1000ppm$  antimony compounds.

### Description

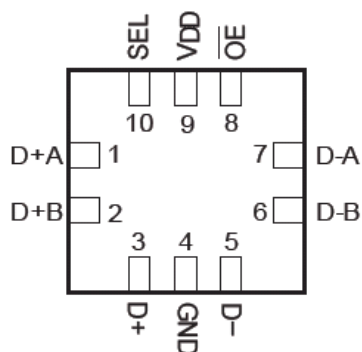
The PI3USB30E is a single differential channel 2:1 multiplexer/demultiplexer USB 2.0 Switch. Industry leading advantages include a propagation delay of less than  $250ps$ , resulting from its low channel resistance and I/O capacitance. The device multiplexes differential outputs from a USB Host device to one of two corresponding outputs. The switch is bidirectional and offers little or no attenuation of the high-speed signals at the outputs. It is designed for low bit-to-bit skew, high channel-to-channel noise isolation and is compatible with various standards, such as High Speed USB 2.0 ( $480\text{ Mb/s}$ ).

The PI3USB30E offers overvoltage protection per the USB2.0 specification. With the chip powered on or off, all I/O pins can withstand a short to Vbus ( $5V \pm 10\%$ ). If  $V_{DD}=0V$ , the I/Os can still have signals present, and the signal will NOT leak through to VDD.

### Block Diagram



## Pin Configuration



## Pin Description

Pin#	Pin Name	Description
1	D+A	Multiplexed Source Inputs
2	D+B	Multiplexed Source Inputs
3	D+	USB Data bus
4	GND	Ground
5	D-	USB Data bus
6	D-B	Multiplexed Source Inputs
7	D-A	Multiplexed Source Inputs
8	/OE	Switch Enable
9	V <sub>DD</sub>	Positive Power Supply
10	SEL	Switch Select

## Maximum Ratings

Storage Temperature .....	-65°C to +150°C
Supply Voltage to Ground Potential .....	-0.5V to +4.6V
Control Input Voltage .....	-0.5V to 4.6V
DC Switch Voltage (D+, D-, D+A, D-A, D+B and D-B) .....	-0.5V to 5.25V
DC Output Current .....	120mA
Power Dissipation .....	0.5W
ESD (HBM) .....	15kV

**Note:**

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability. The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.

## Recommended Operating Conditions

Symbol	Parameter	Min	Typ	Max	Unit
V <sub>DD</sub>	Operating Voltage	1.8	-	4.3	V
V <sub>CNTRL</sub> <sup>(1)</sup>	Control Input Voltage	0	-	4.3	
V <sub>INPUT</sub>	Switch Input Voltage	-0.5	-	4.3	
T <sub>A</sub>	Operating Temperature	-40	-	85	°C

**Notes:**

1. The control input must be held high or low and it must not float.

## DC Electrical Characteristics

(T<sub>A</sub> = -40°C to +85°C, V<sub>DD</sub> = 3.0V - 4.4V)

Parameter	Description	Test Conditions <sup>(1)</sup>	Min	Typ <sup>(2)</sup>	Max	Unit
V <sub>IH</sub>	Input High Voltage	Guaranteed High Level	1.2	-	-	V
V <sub>IL</sub>	Input Low Voltage	Guaranteed Low Level	-	-	0.55	
V <sub>IK</sub>	Clamp Diode Voltage	V <sub>DD</sub> = Max., I <sub>SEL</sub> = -18mA	-	-0.7	-1.2	
I <sub>IH</sub>	Input High Current	V <sub>DD</sub> = Max., V <sub>SEL</sub> = V <sub>DD</sub>	-	-	±100	nA
I <sub>IL</sub>	Input Low Current	V <sub>DD</sub> = Max., V <sub>SEL</sub> = GND	-	-	±100	
R <sub>ON</sub>	Switch On-Resistance <sup>(3)</sup>	V <sub>DD</sub> = Min., 0 ≤ V <sub>INPUT</sub> ≤ 0.4V, I <sub>INPUT</sub> = -40mA	-	4.3	6.5	Ω
R <sub>FLAT(ON)</sub>	On-Resistance Flatness <sup>(3)</sup>	V <sub>DD</sub> = Min., 0 ≤ V <sub>INPUT</sub> ≤ 0.4V, I <sub>INPUT</sub> = -40mA	-	0.25	-	
ΔR <sub>ON</sub>	On-Resistance match from center ports to any other port <sup>(3)</sup>	V <sub>DD</sub> = Min., 0 ≤ V <sub>INPUT</sub> ≤ 0.4V, I <sub>INPUT</sub> = -40mA	-	0.1	1.0	
I <sub>OZ</sub>	Output leakage current when port is off	V <sub>DD</sub> = 4.3V, 0 ≤ V <sub>INPUT</sub> ≤ 0.4V	-	-	±100	nA
I <sub>OFF</sub>	Power-Off Leakage Current	V <sub>INPUT</sub> = 0V~4.3V, V <sub>DD</sub> = 0V	-	-	100	nA

**Notes:**

1. For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.

2. V<sub>DD</sub> = 3.0 - 4.4V, T<sub>A</sub> = 25°C ambient and maximum loading.

3. Measured by the voltage drop between D and Dn pin at indicated current through the Switch On-Resistance is determined by the lower of the voltages on the two (D, Dn) pins.

## Power Supply Characteristics

(T<sub>A</sub> = -40°C to +85°C, V<sub>DD</sub> = 3.0V - 4.4V)

Parameter	Description	Test Conditions <sup>(1)</sup>	Min	Typ	Max	Unit
I <sub>CC</sub>	Quiescent Power Supply current	V <sub>DD</sub> = Max., V <sub>SEL</sub> = V <sub>DD</sub> or GND	-	-	100	nA
I <sub>CCT</sub>	Increase in I <sub>CC</sub> Current per Control Voltage and V <sub>DD</sub>	V <sub>CNTRL</sub> = 1.8V, V <sub>DD</sub> = 4.3V	-	5.8	15	uA

**Notes:**

1. For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.

## Capacitance

 $(T_A = 25^\circ\text{C}, V_{DD} = 3.0\text{V}, f = 1\text{MHz})$ 

Parameters	Description	Test Conditions	Min.	Typ	Max.	Units
$C_{IN}$	Control pin Input Capacitance	$V_{SEL} = 0\text{V}$	-	6.0	-	pF
$C_{OFF}$	Switch Capacitance, Switch OFF		-	1.9	-	
$C_{ON}$	Switch Capacitance, Switch ON		-	6.0	-	

## Dynamic Electrical Characteristics Over the Operating Range

 $(T_A = 25^\circ\text{C}, V_{DD} = 3.0\text{V})$ 

Parameters	Description	Test Conditions <sup>(1)</sup>	Min.	Typ	Max.	Units
XTALK	Crosstalk	$R_L = 50\Omega, f = 240\text{MHz}$	-	-41	-	dB
OIRR	OFF Isolation		-	-35	-	
BW	-3dB Bandwidth	$R_L = 50\Omega$	-	850	-	MHz

## Switching Characteristics

 $(T_A = 25^\circ\text{C}, V_{DD} = 3.0\text{V})$ 

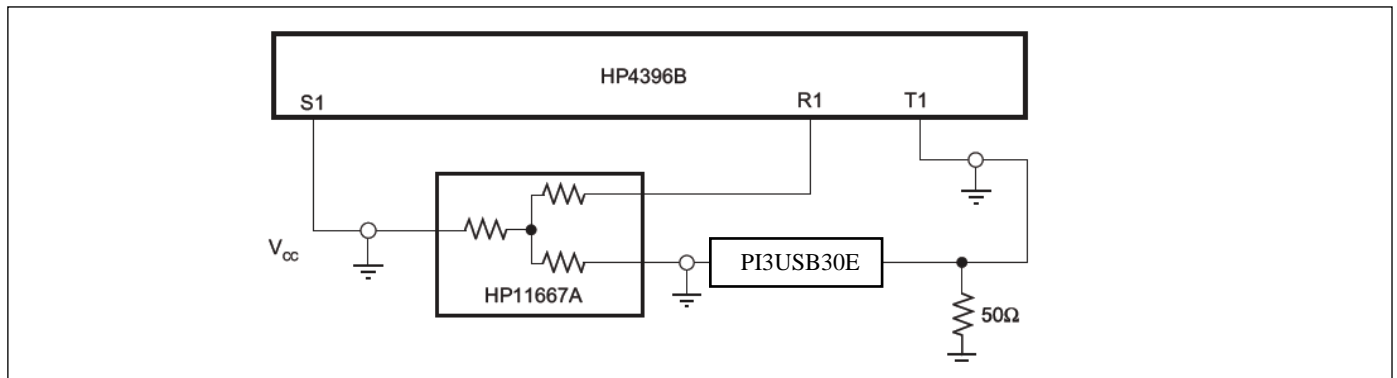
Parameters	Description	Test Conditions <sup>(1)</sup>	Min	Typ	Max	Units
$t_{PD}$	Propagation Delay <sup>(2,3)</sup>	See Test Circuit for Electrical Characteristics	-	0.25	-	ns
$t_{PZH}, t_{PZL}$	Line Enable Time - SEL to D(+/-), D(+/-)n		-	25	-	
$t_{PHZ}, t_{PLZ}$	Line Disable Time - SEL to D(+/-), D(+/-)n		-	4	-	
$t_{BBM}$	Break-Before-Make	-	-	7	-	ns
$t_{SKb-b}$	Output skew, bit-to-bit (opposite transition of the same output ( $t_{PHL} - t_{PLH}$ )) <sup>(2)</sup>	$V_{DD} = 3\text{V}$	-	-	35	ps
		$V_{DD} = 4\text{V}$	-	-	60	

### Notes:

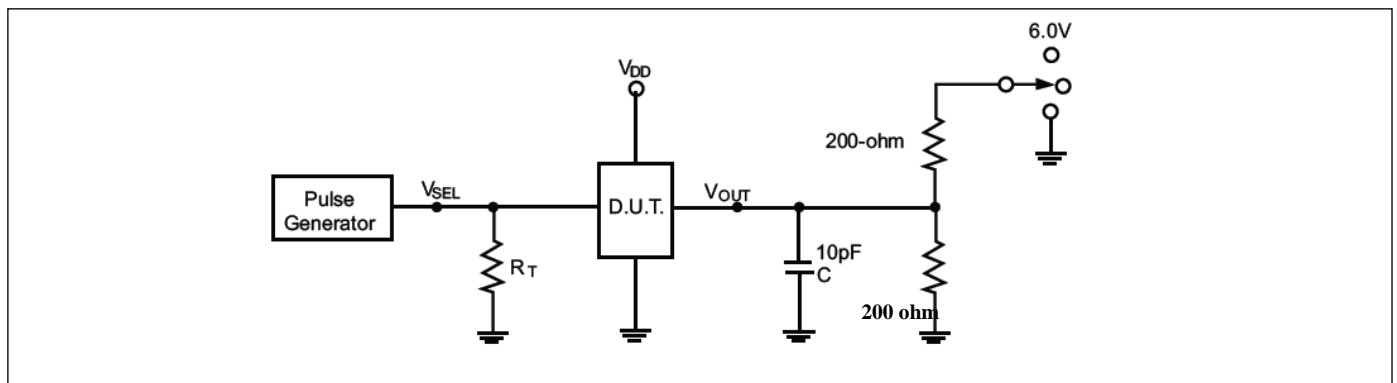
- For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Guaranteed by design.
- The switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 10pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the switch when used in a system is determined by the driving circuit on the driving side of the switch and its interactions with the load on the driven side.

## Test Circuits and Test Diagramming

### Test Circuit for Dynamic Electrical Characteristics



### Test Circuit for Electrical Characteristics



#### Notes:

$C_L$  = Load capacitance: includes jig and probe capacitance.

$R_T$  = Termination resistance: should be equal to  $Z_{OUT}$  of the Pulse Generator

Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control.

Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.

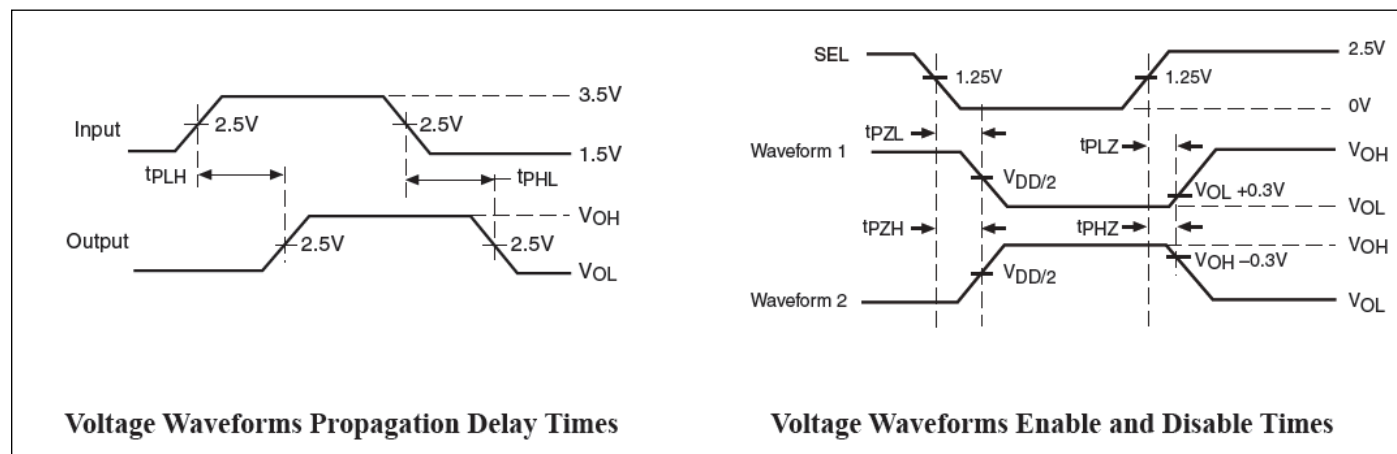
All input impulses are supplied by generators having the following characteristics:  $PRR \leq \text{MHz}$ ,  $Z_O = 50\Omega$ ,  $t_R \leq 2.5\text{ns}$ ,  $t_F \leq 2.5\text{ns}$ .

The outputs are measured one at a time with on transition per measurement.

### Switch Positions

Test	Switch
$t_{PLZ}$ , $t_{PZL}$ (output on I-side)	6.0V
$t_{PHZ}$ , $t_{PZH}$ (output on I-side)	GND
Prop Delay	Open

## Switching Waveforms



## Applications Information

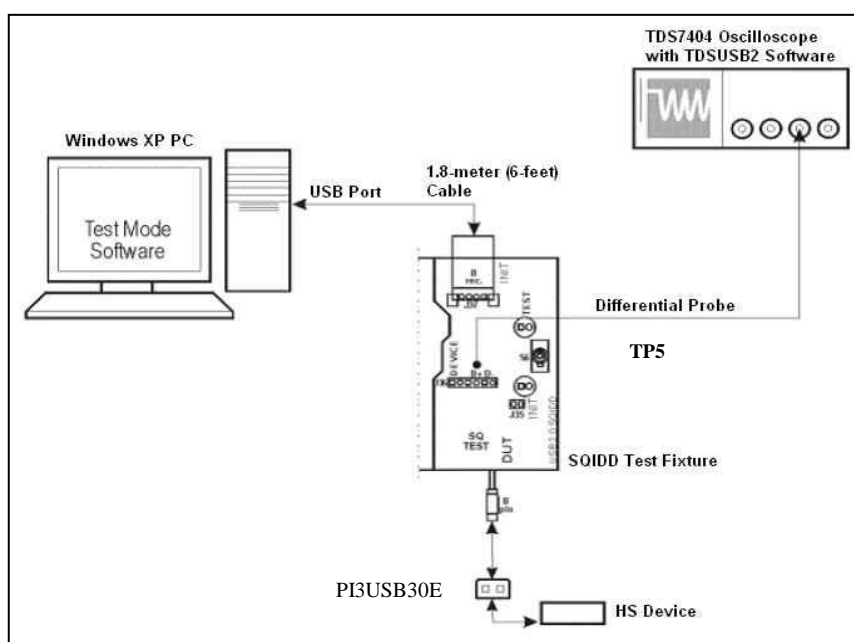
### Logic Inputs

The logic control inputs can be driven up to +4.3V regardless of the supply voltage. For example, given a +3.3V supply, the output enables or select pins may be driven to low to 0V and high to 4.3V.

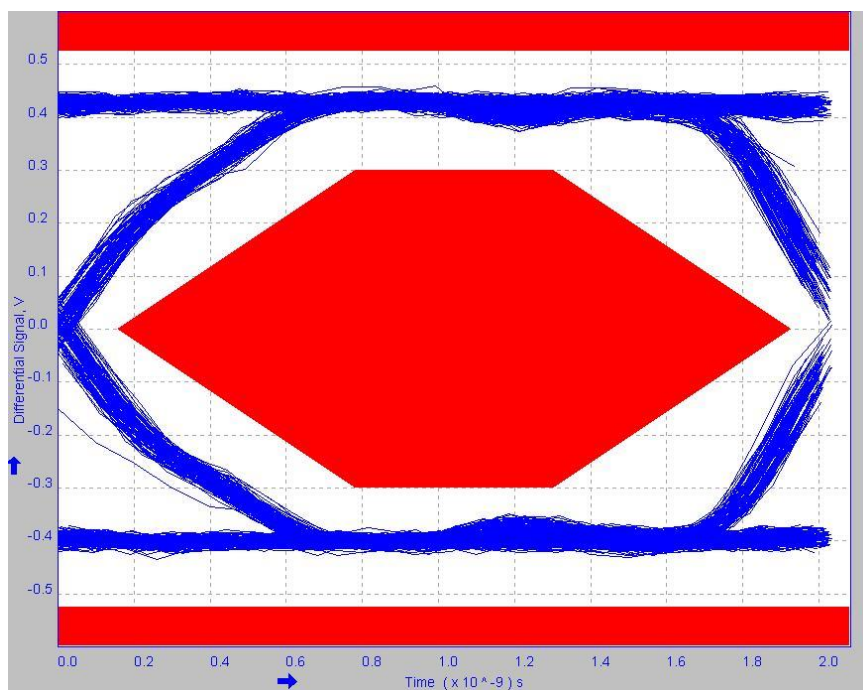
### Power Supply Sequencing

Proper power supply sequencing is recommended for all CMOS devices. Always apply VDD and GND before applying signals to input/output or control pin.

### Eye Diagram Measurement:

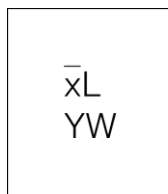


**USB2.0 High-speed (480Mbps) Signal Integrity Test Setup**



High Speed Signal Quality Eye Diagram Test ( $V_{DD} = 3.3V$ )

## Part Marking



xL: PI3USB30EZME

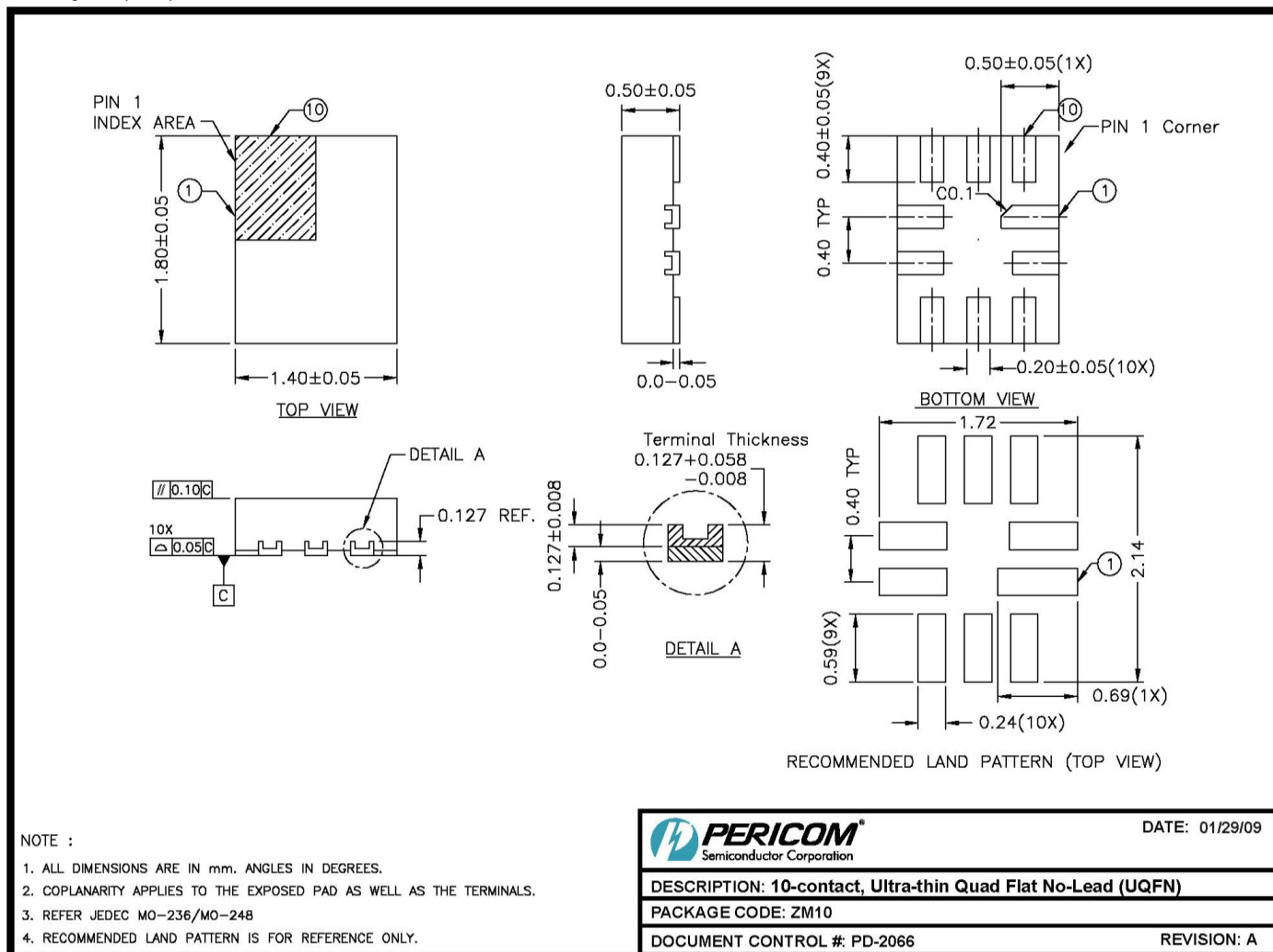
Y: Date Code (Year)

W: Date Code (Workweek)

Bar above the first "x" means pin 1

## Packaging Mechanical

### 10-UQFN (ZM)



09-0072

For latest package info.

 please check: <http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/>

## Ordering Information

Part Number	Package Code	Package Description
PI3USB30EZMEX	ZM	10-contact, Ultra-thin Quad Flat No-Lead (UQFN)

### Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. E = Pb-free and Green
5. X suffix = Tape/Reel



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