



PBSS4320X

20 V, 3 A NPN low V_{CEsat} (BISS) transistor

27 May 2019

Product data sheet

1. General description

NPN low V_{CEsat} transistor in a medium power flat lead SOT89 plastic package.

PNP complement: PBSS5320X

2. Features and benefits

- SOT89 (SC-62) package
- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability: I_C and I_{CM}
- Higher efficiency leading to less heat generation
- Reduced printed-circuit board requirements.
- AEC-Q101 qualified

3. Applications

- Power management
 - DC/DC converters
 - Supply line switching
 - Battery charger
 - LCD backlighting.
- Peripheral drivers
 - Driver in low supply voltage applications (e.g. lamps and LEDs).
 - Inductive load driver (e.g. relays, buzzers and motors).

4. Quick reference data

Table 1. Quick reference data

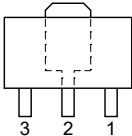
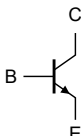
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	20	V
I _C	collector current		[1]	-	-	3	A
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	5	A
h _{FE}	DC current gain	V _{CE} = 2 V; I _C = 0.1 A		220	-	-	
R _{CEsat}	collector-emitter saturation resistance	I _C = 3 A; I _B = 300 mA	[2]	-	85	105	mΩ

[1] Device mounted on a ceramic printed-circuit board 7 cm², single-sided copper, tin-plated.

[2] Pulsed test: t_p ≤ 300 μs; δ ≤ 0.02

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter	 SOT89	 sym123
2	C	collector		
3	B	base		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS4320X	SOT89	plastic surface-mounted package; die pad for good heat transfer; 3 leads	SOT89

7. Marking

Table 4. Marking codes

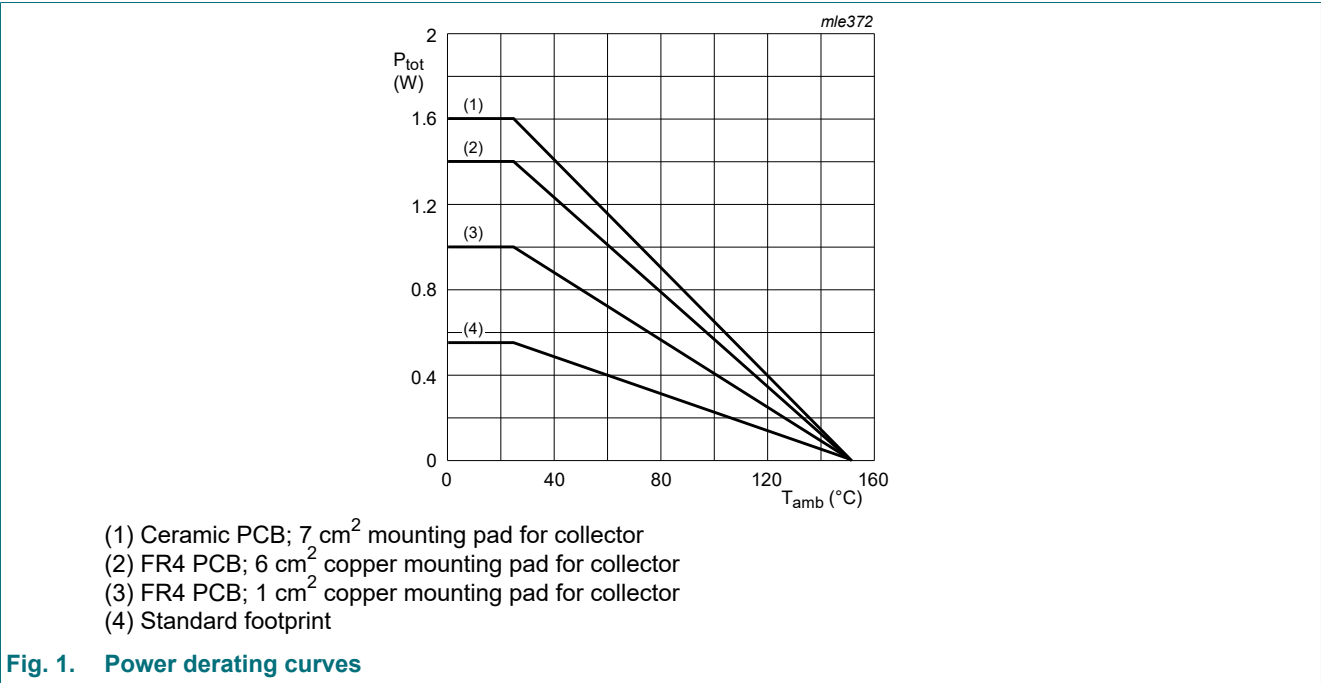
Type number	Marking code
PBSS4320X	S44

8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	20	V
V _{CEO}	collector-emitter voltage	open base		-	20	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current		[1]	-	3	A
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	5	A
I _B	base current			-	0.5	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[2]	-	550	mW
			[3]	-	1	W
			[4]	-	1.4	W
			[5]	-	1.6	W
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on a ceramic printed-circuit board 7 cm², single-sided copper, tin-plated.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².
[4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².
[5] Device mounted on a ceramic PCB 7 cm², single-sided copper, tin-plated.



9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	225	K/W
			[2]	-	-	125	K/W
			[3]	-	-	90	K/W
			[4]	-	-	80	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	16	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².
- [4] Device mounted on a ceramic PCB 7 cm², single-sided copper, tin-plated.

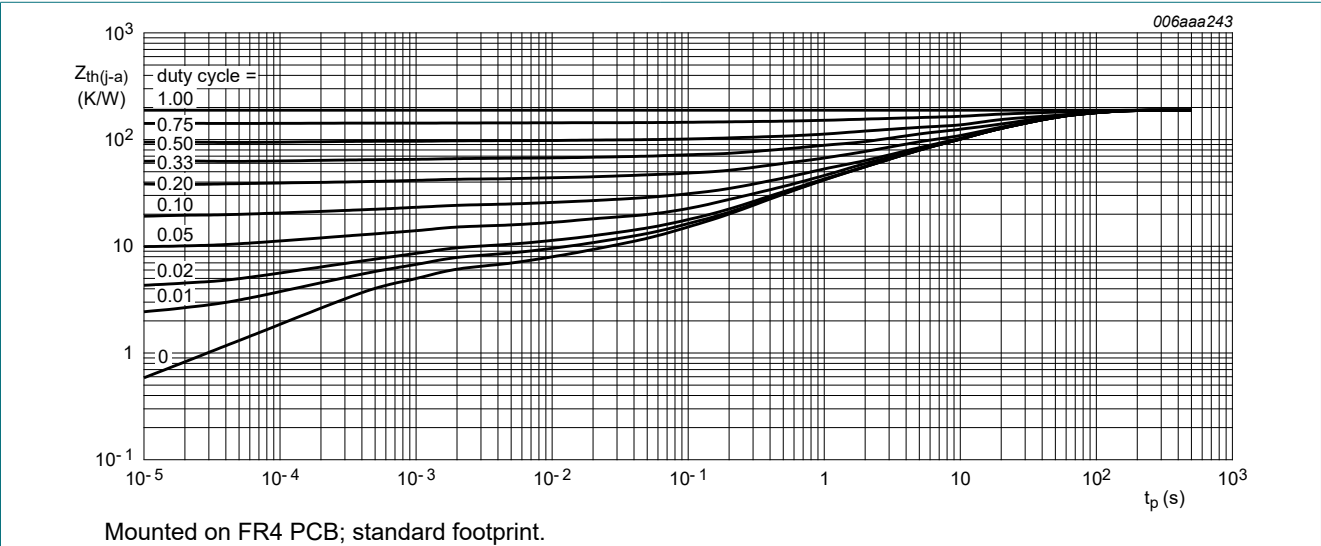


Fig. 2. Transient thermal impedance as a function of pulse duration; typical values

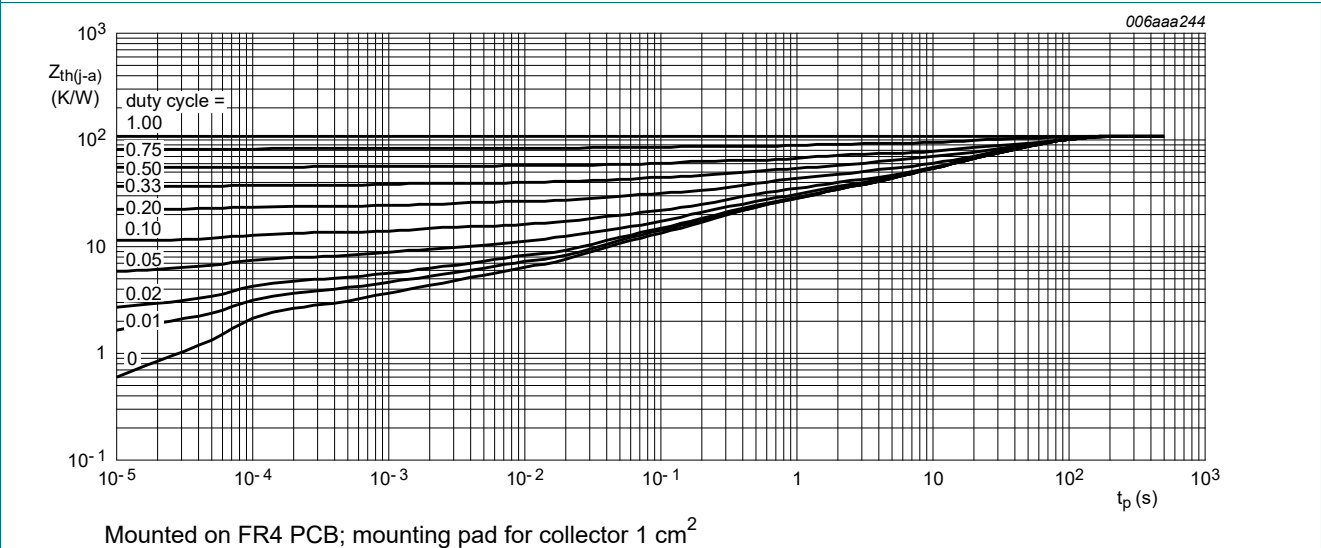
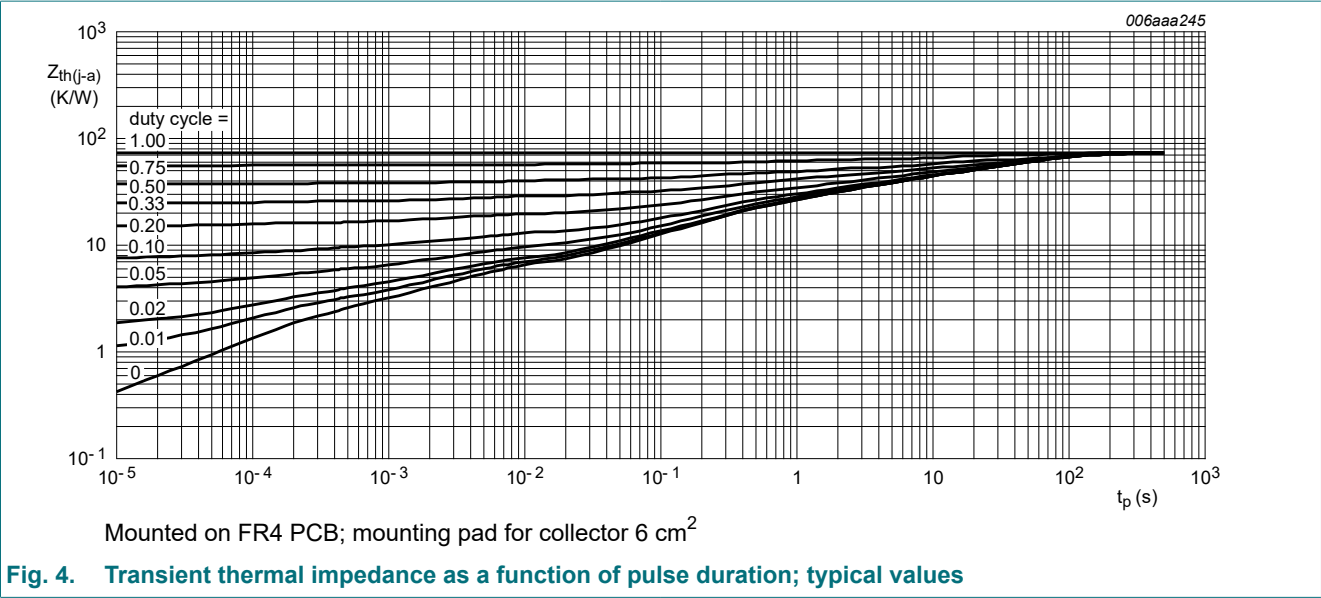


Fig. 3. Transient thermal impedance as a function of pulse duration; typical values



10. Characteristics

Table 7. Characteristics
T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = 20 V; I _E = 0 A	-	-	100	nA
		V _{CB} = 20 V; I _E = 0 A; T _j = 150 °C	-	-	50	µA
I _{CES}	collector-emitter cut-off current	V _{CE} = 20 V; V _{BE} = 0 V	-	-	100	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 2 V; I _C = 0.1 A	220	-	-	
		V _{CE} = 2 V; I _C = 0.5 A	220	-	-	
		V _{CE} = 2 V; I _C = 1 A	[1] 220	-	-	
		V _{CE} = 2 V; I _C = 2 A	[1] 200	-	-	
		V _{CE} = 2 V; I _C = 3 A	[1] 150	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = 0.5 A; I _B = 50 mA	-	-	70	mV
		I _C = 1 A; I _B = 50 mA	-	-	120	mV
		I _C = 2 A; I _B = 100 mA	-	-	240	mV
		I _C = 3 A; I _B = 300 mA	[1] -	-	310	mV
R _{CEsat}	collector-emitter saturation resistance		[1] -	85	105	mΩ
V _{BEsat}	base-emitter saturation voltage	I _C = 2 A; I _B = 100 mA	-	-	1.1	V
		I _C = 3 A; I _B = 300 mA	[1] -	-	1.2	V
V _{BEon}	base-emitter turn-on voltage	V _{CE} = 2 V; I _C = 1 A	1.1	-	-	V
f _T	transition frequency	V _{CE} = 5 V; I _C = 100 mA; f = 100 MHz	100	-	-	MHz
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz	-	-	35	pF

[1] Pulsed test: t_p ≤ 300 µs; δ ≤ 0.02

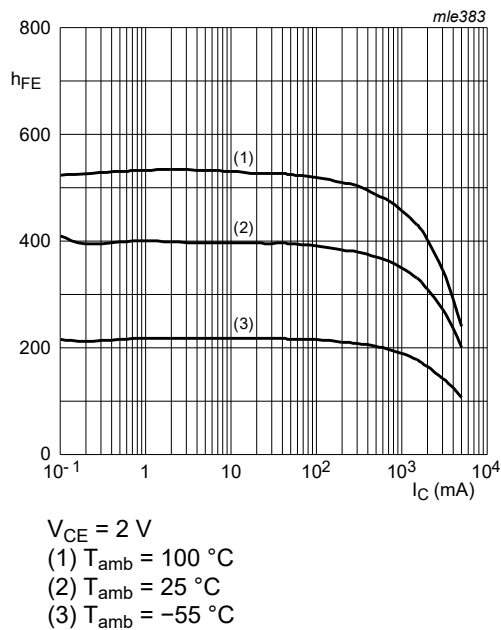


Fig. 5. DC current gain as a function of collector current; typical values

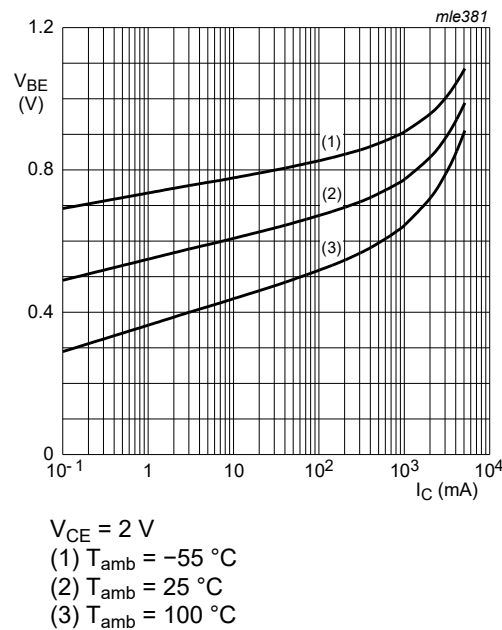


Fig. 6. Base-emitter voltage as a function of collector current; typical values

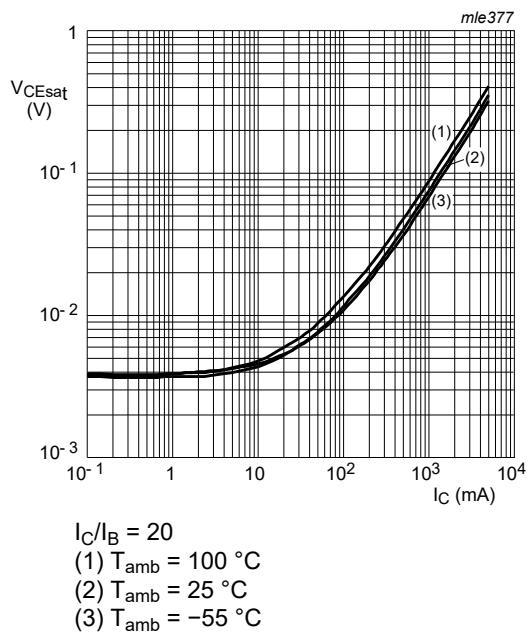


Fig. 7. Collector-emitter saturation voltage as a function of collector current; typical values

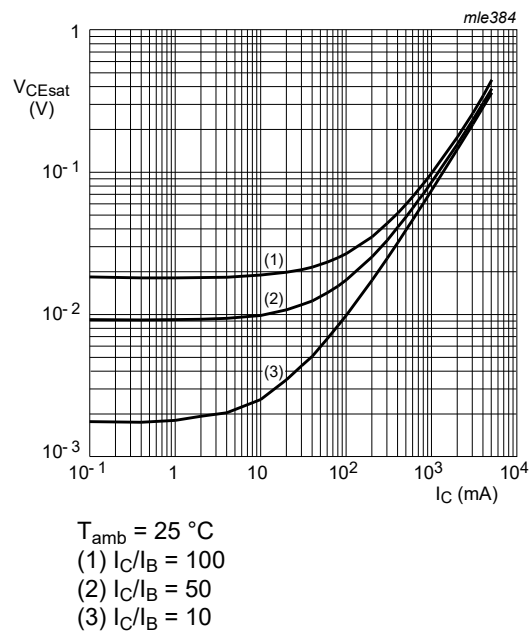


Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

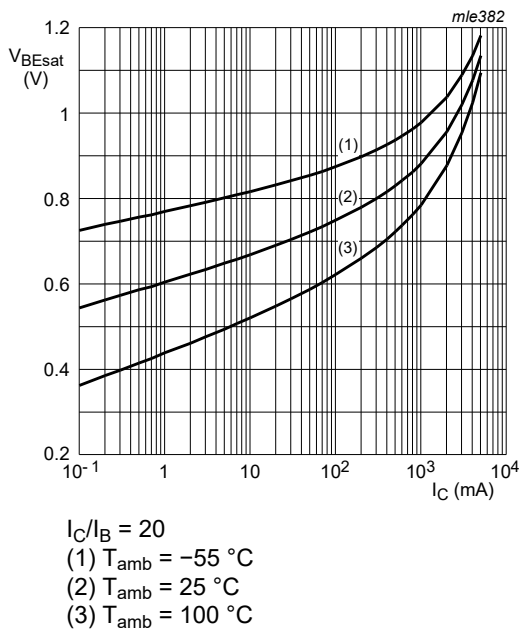


Fig. 9. Base-emitter saturation voltage as a function of collector current; typical values

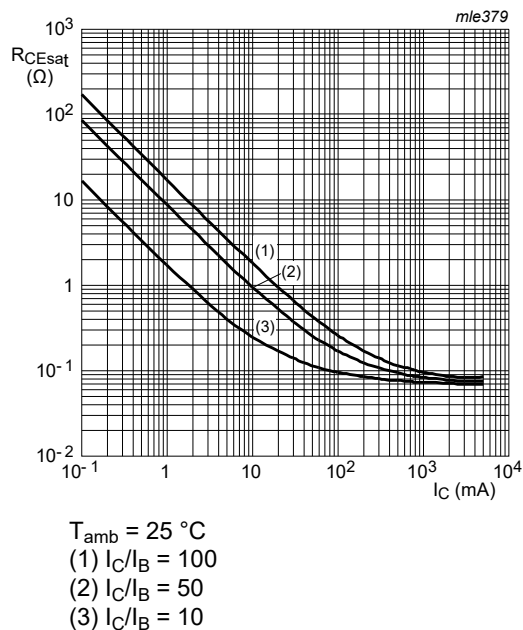


Fig. 10. Equivalent on-resistance as a function of collector current; typical values

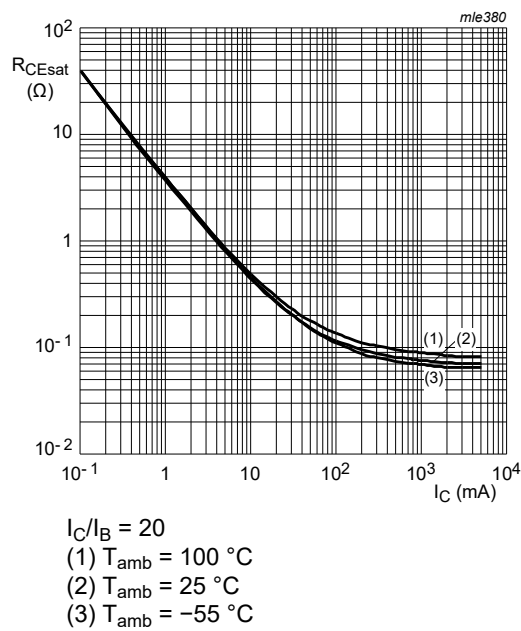


Fig. 11. Equivalent on-resistance as a function of collector current; typical values

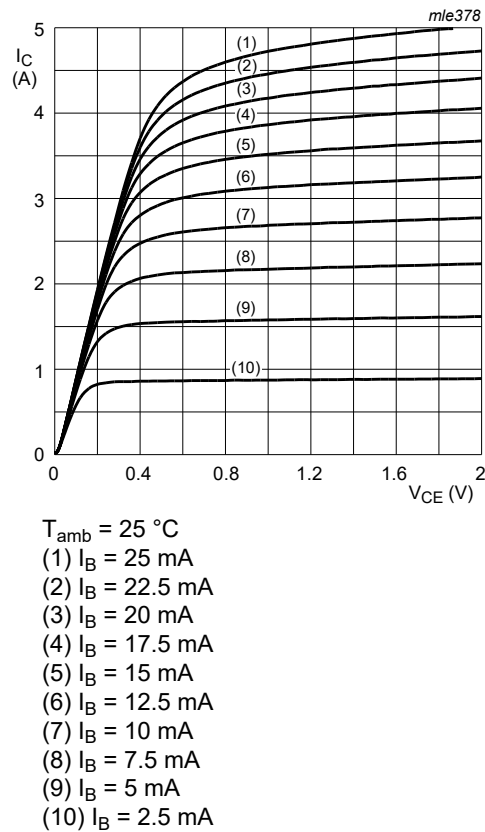


Fig. 12. Collector current as a function of collector-emitter voltage; typical values

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

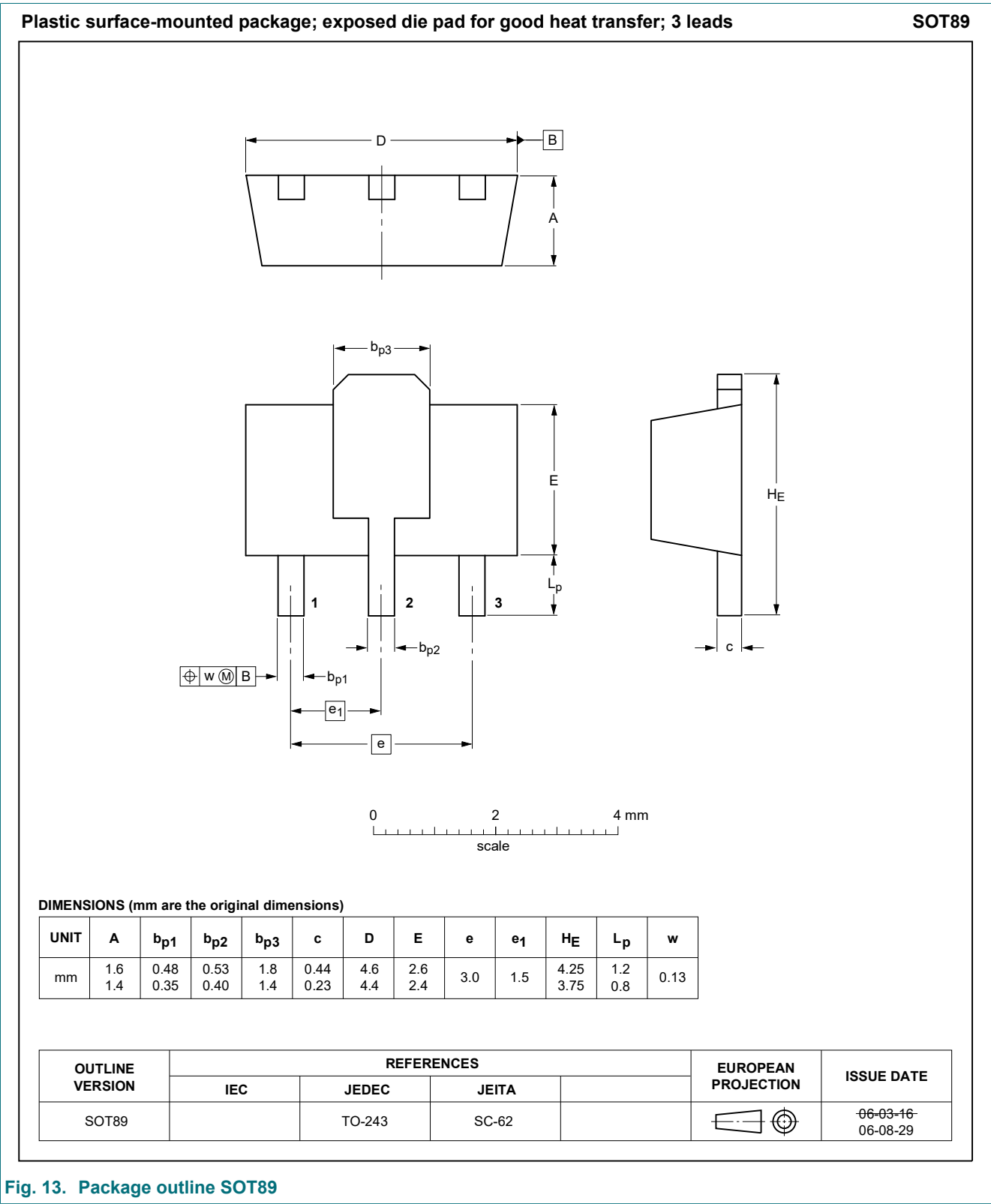


Fig. 13. Package outline SOT89

13. Soldering

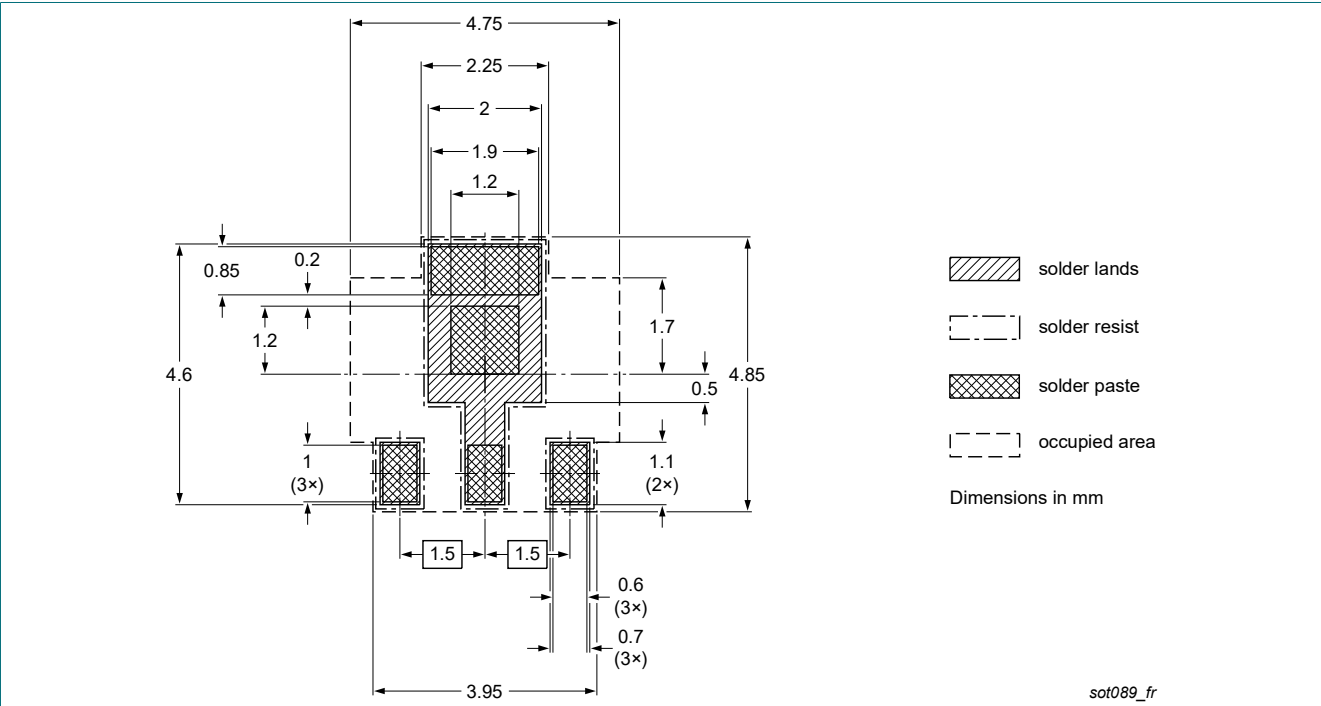


Fig. 14. Reflow soldering footprint for SOT89

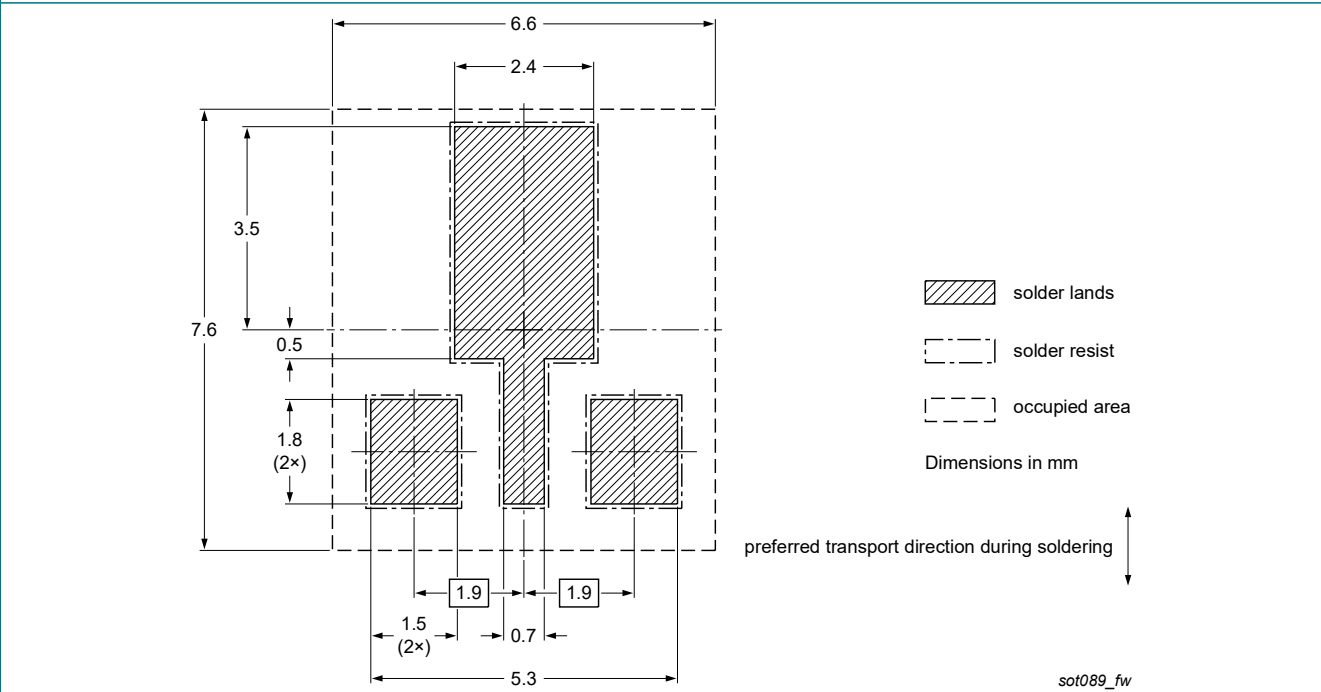


Fig. 15. Wave soldering footprint for SOT89

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4320X v.1	20190527	Product data sheet	-	PBSS4320X v.2
Modifications:	<ul style="list-style-type: none">Characteristics: V_{BEsat} corrected from typical to maximum.The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.Legal texts have been adapted to the new company name where appropriate.			
PBSS4320X v.2	20041103	Product data sheet	-	PBSS4320X v.1
PBSS4320X v.1	20031215	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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