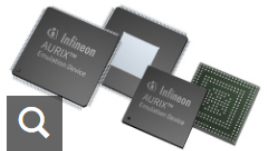


SAK-TC277TF-64F200N DC

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ISO26262-compliant

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Debug, Trave, Measure and Calibrate

SAK-TC277TF-64F200N DC belongs to the **TC2x Emulation Devices**. It has the same feature set as the **TC27x** standard family with **additional debug and tracing capabilities**. SAK-TC277TF-64F200N DC offers support for calibration, rapid prototyping, instrumentation and debug/emulation.

Summary of Features

TC277TF has the same features as the **TC27x standard** devices:

- 3 TriCore running at 200 MHz
- 4 MB flash/ ECC protection
- 472 KB SRAM/ ECC protection
- 10xSENT, 3xPSIS, 1xPSI5S sensor interfaces
- State of the art connectivity : 1xEthernet 100 Mbit, 1xFlexRay, 4xCAN FD, 4xASCLIN, 4xQSPI, 1xI²C, I²S emulation
- PG-LFBGA-292
- Single voltage supply 3.3 V or 5V
- Temperature : 40°C to 125°C

Emulation key Features

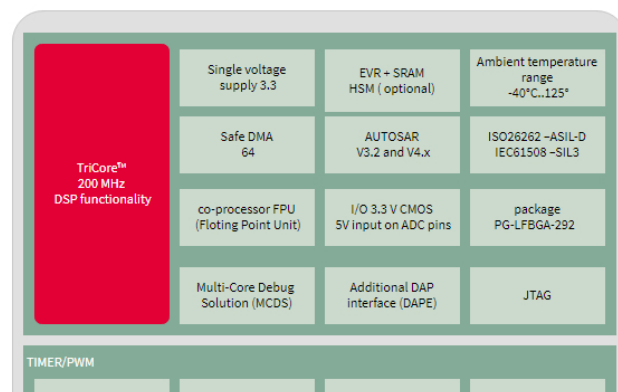
- 2 MB Extension Memory (EMEM) for calibration and tracing
- Multi-Core Debug Solution (MCDS)
 - Parallel and time aligned trace of cores and busses
 - Powerful trigger conditions
 - On-chip logic analyzer functions
- Additional DAP interface (DAPE) for connecting a second independent tool
- Injector for faults and stress (IFS)
- CPUs have full access to all emulation resources, enabling all kind of interfaces as tool connection

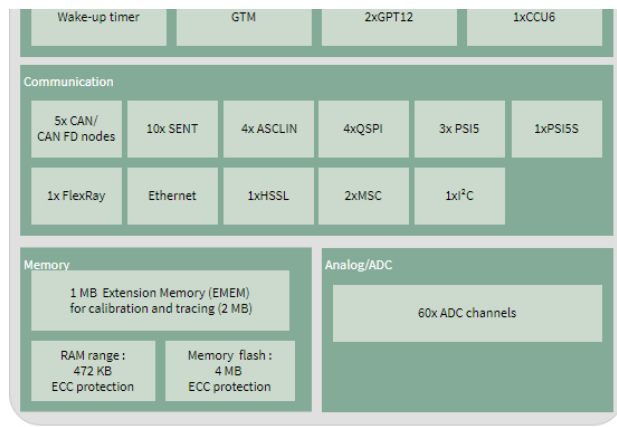
Benefits

- Higher software/system quality and faster time to market with best in class:
 - Multi-core debugging and trace
 - Trace based measurement
 - Rapid prototyping
 - Calibration
- Scalable tooling options from very cost effective to highest performance
- Consistent architecture over the whole family for all packages
- Different tool combinations due to two DAP interfaces
- Very cost effective trace

Diagrams

Interactive Diagram [pass over the diagram with the cursor to learn more about the features]





Parametrics

Parametrics	SAK-TC277TF-64F200N DC
# of cores / lockstep cores	3/2
ASIL/SIL support	ASIL-D/ SIL-3
CAN Nodes	5
Clock Frequency	200.0 MHz
DMA Channels	64
DSP Functionality	yes
External Bus Interface	no
FlexRay™	yes
Floating Point Unit	yes
Hardware Accelerator	no
I/O Operation Voltages min max	3.3 V 5.0 V
LIN	4.0
On-chip Clock Generation	yes
On-chip Voltage Regulator	yes
Oscillator Watchdog	yes
Program Memory	4.0 MByte
Real Time Clock	yes
SPI	4.0
SRAM (incl. Cache)	472.0 kByte
Temperature	-40°C to 125°C
Type of Memory	Flash
Watchdog Timer	yes
eMMC	no

Documents

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Sales Product Name	SAK-TC277TF-64F200N DC
OPN	TC277TF64F200NDCKXUMA1
Product Status	active and preferred
Package name	PG-LFBGA-292
Order online	Buy online
Completely lead free	yes
Halogen free	yes
RoHS compliant	yes
Packing Size	1000
Packing Type	TAPE & REEL
Moisture Level	3
Moisture Packing	DRY

Boards

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Support

Search the FAQs! Enter your search terms...



Top 6 FAQs. Use the search bar above to show more!

Which debuggers can be used with Dave4?

XC800 8-Bit Microcontroller Family: Which JTAG port should I use?

Dave Download Problem

It may happen that the email server of

XMC link does not provide the 20-pin Core-Sight Debug interface with the extended trace signals. Dave4 supports only JLink or XMCLink. But you can also use Segger JLink Trace Pro along with t...

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There is just one physical JTAG module. The default port is P0.x; you should use this one if you can. For P5 you can only debug after software (SW) has properly reconfigured the ports from P0 to P5. So...

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Memtool - support and licences

We are not able to provide official support for MemTool, because it is free of charge and not related to a specific product. For this reason we cannot license the distribution of MemTool....

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Meaning of Grade A behind the Step Code at Microcontrollers

The Grade A marking shows that the microcontroller can access internal flash with one wait state only. By comparison, the industry standard is two wait states for flash access.

Technical Support

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