

MOSFET – Dual, N-Channel, Small Signal

20 V, 540 mA NTZD3154N

Features

- Low R_{DS(on)} Improving System Efficiency
- Low Threshold Voltage
- Small Footprint 1.6 x 1.6 mm
- ESD Protected Gate
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- Load/Power Switches
- Power Supply Converter Circuits
- Battery Management
- Cell Phones, Digital Cameras, PDAs, Pagers, etc.

MAXIMUM RATINGS (T_{.I} = 25°C unless otherwise noted.)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V_{DSS}	20	V
Gate-to-Source Voltage			V _{GS}	±7.0	V
Continuous Drain Current	Steady	$T_A = 25^{\circ}C$	lo.	540	mA
(Note 1)	State	$T_A = 85^{\circ}C$	I _D	390	
Power Dissipation (Note 1)	Stea	dy State	P _D	250	mW
Continuous Drain Current (Note 1)	t ≤ 5 s	$T_A = 25^{\circ}C$	I _D	570	mA
(Note 1)	1 3 3 3	T _A = 85°C	טי	410	
Power Dissipation (Note 1)	t:	≤ 5 s	P _D	280	mW
Pulsed Drain Current $t_p = 10 \mu s$			I _{DM}	1.5	Α
Operating Junction and Storage Temperature			T _J , T _{STG}	-55 to 150	°C
Source Current (Body Diode)			IS	350	mA
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			TL	260	°C

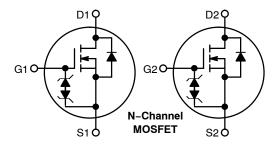
THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Ambient - Steady State (Note 1)	$R_{ heta JA}$	500	°C/W
$Junction-to-Ambient - t \le 5 s \text{ (Note 1)}$		447	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Surface mounted on FR4 board using 1 in sq pad size (Cu. area = 1.127 in sq [1 oz] including traces).

V _{(BR)DSS}	R _{DS(on)} Typ	I _D Max (Note 1)	
	400 mΩ @ 4.5 V		
20	500 mΩ @ 2.5 V	540 mA	
	700 mΩ @ 1.8 V		



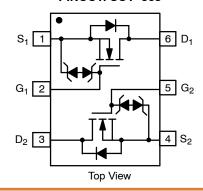


TV = Specific Device Code

M = Date Code= Pb-Free Package

(Note: Microdot may be in either location)

PINOUT: SOT-563



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

NOTE: Some of the devices on this data sheet have been **DISCONTINUED**. Please refer to the table on page 4.

NTZD3154N

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise noted.)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 250	μΑ	20	-	-	V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	-		-	14	-	mV/°C
Zero Gate Voltage Drain Current		V _{GS} = 0 V	T _J = 25°C	-	-	1.0	μΑ
	I _{DSS}	V _{DS} = 16 V	T _J = 125°C	-	-	5.0	
Gate-to-Source Leakage Current	I _{GSS}	V_{DS} = 0 V, V_{GS} = ± 4	1.5 V	_	-	±5.0	μΑ
ON CHARACTERISTICS (Note 3)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_D = 250$	μΑ	0.45	-	1.0	V
Negative Threshold Temperature Coefficient	V _{GS(TH)} /T _J	-		-	2.0	-	mV/°C
Drain-to-Source On Resistance		$V_{GS} = 4.5 \text{ V}, I_D = 540$) mA	-	0.4	0.55	Ω
	R _{DS(on)}	V _{GS} = 2.5 V, I _D = 500) mA	-	0.5	0.7	
		V _{GS} = 1.8 V, I _D = 350 mA		-	0.7	0.9	
Forward Transconductance	9FS	V _{DS} = 10 V, I _D = 540 mA		-	1.0	-	S
CHARGES AND CAPACITANCES	•						
Input Capacitance	C _{ISS}			-	80	150	pF
Output Capacitance	C _{OSS}	V _{GS} = 0 V, f = 1.0 MHz, V	_{DS} = 16 V	-	13	25	
Reverse Transfer Capacitance	C _{RSS}			-	10	20	
Total Gate Charge	Q _{G(TOT)}	V _{GS} = 4.5 V, V _{DS} = 10 V; I _D = 540 mA		-	1.5	2.5	nC
Threshold Gate Charge	Q _{G(TH)}			-	0.1	-	
Gate-to-Source Charge	Q _{GS}			-	0.2	-	1
Gate-to-Drain Charge	Q_{GD}			-	0.35	-	
SWITCHING CHARACTERISTICS, V _{GS} = V	Note 4)			-	-		
Turn-On Delay Time	t _{d(ON)}	V_{GS} = 4.5 V, V_{DD} = 10 V, I_{D} = 540 mA, R_{G} = 10 Ω		-	6.0	-	ns
Rise Time	t _r			-	4.0	-	
Turn-Off Delay Time	t _{d(OFF)}			_	16	_	
Fall Time	t _f			-	8.0	-	
DRAIN-SOURCE DIODE CHARACTERISTIC	s			<u> </u>	<u> </u>	-	-
Forward Diode Voltage	.,	$V_{GS} = 0 \text{ V},$ $T_{J} = 25^{\circ}\text{C}$		-	0.7	1.2	V
	V _{SD}	I _S = 350 mA	T _J = 125°C	-	0.6	-	
Reverse Recovery Time	t _{RR}	$V_{GS} = 0 \text{ V}, d_{ SD}/d_t = 100 \text{ A/}\mu\text{s}, I_S = 350 \text{ mA}$		-	6.5	_	ns
	•	de / IOB/ t // / C					

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Surface–mounted on FR4 board using 1 in. sq. pad size (Cu. area = 1.127 in sq [1 oz] including traces).

3. Pulse Test: pulse width ≤ 300 μs, duty cycle ≤ 2%.

4. Switching characteristics are independent of operating junction temperatures.

NTZD3154N

TYPICAL PERFORMANCE CURVES (T_J = 25°C unless otherwise noted)

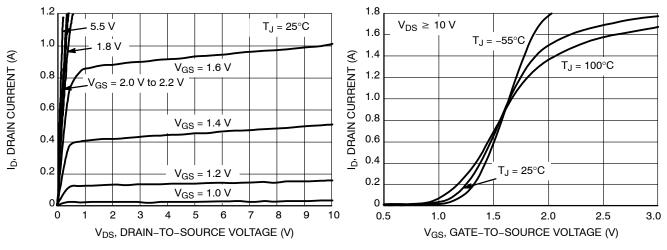


Figure 1. On-Region Characteristics

Figure 2. Transfer Characteristics

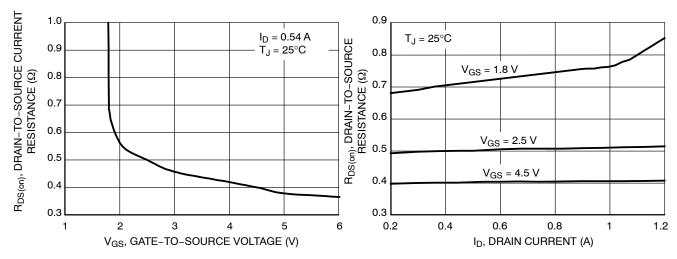


Figure 3. On-Resistance versus Gate-to-Source Voltage

Figure 4. On-Resistance versus Drain Current and Gate Voltage

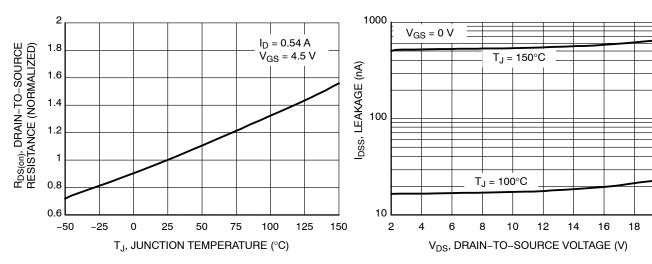


Figure 5. On–Resistance Variation with Temperature

Figure 6. Drain-to-Source Leakage Current versus Voltage

20

NTZD3154N

TYPICAL PERFORMANCE CURVES ($T_J = 25^{\circ}$ C unless otherwise noted)

 V_{DS}

 Q_{GS}

0.2

0

0.6

0.5

0.4

0.3

0.2

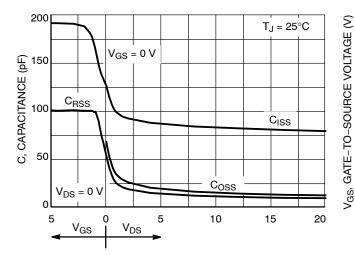
 Q_{GD}

0.4

 $V_{GS} = 0 V$

 $T_J = 25^{\circ}C$

0.6



Qg, TOTAL GATE CHARGE (nC) Figure 8. Gate-to-Source and Drain-to-Source Voltage versus Total Charge

8.0

 Q_T

V_{DS}, DRAIN-TO-SOURCE VOLTAGE (V)

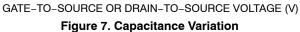
 V_{GS}

 $I_D = 0.54 A$ $T_J = 25^{\circ}C$

1.4

1.6

1.2



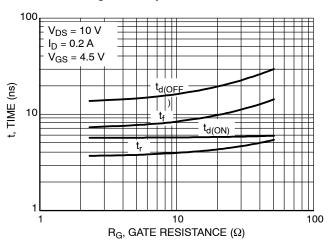


Figure 10. Diode Forward Voltage versus Current

0.6

V_{SD}, SOURCE-TO-DRAIN VOLTAGE (V)

0.7

SOURCE CURRENT (A) 0.2 <u>ŵ</u> 0.1

0.5

Figure 9. Resistive Switching Time Variation versus Gate Resistance

ORDERING INFORMATION

Device	Package	Shipping
NTZD3154NT1G		
NTZD3154NT1H	SOT-563 (Pb-Free)	4000 / Tarra 9 Davi
NTZD3154NT2G		4000 / Tape & Reel
NTZD3154NT2H		
NTZD3154NT5H		8000 / Tape & Reel

DISCONTINUED (Note 5)

(Pb-Free)

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{5.} DISCONTINUED: These devices are not recommended for new design. Please contact your onsemi representative for information. The most current information on these devices may be available on www.onsemi.com.



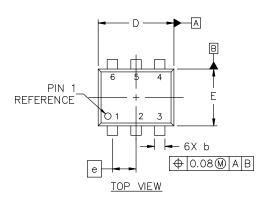


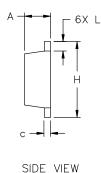
SOT-563-6 1.60x1.20x0.55, 0.50P CASE 463A **ISSUE J**

DATE 15 FEB 2024

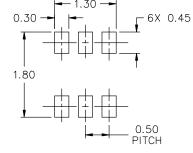
NOTES:

- DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
- ALL DIMENSION ARE IN MILLIMETERS.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.





DIM	MILLIMETERS			
ויונע	MIN.	N□M.	MAX.	
Α	0.50	0.55	0.60	
b	0.17	0.22	0.27	
\cup	0.08	0.13	0.18	
D	1.50	1.60	1.70	
Ε	1.10	1.20	1.30	
е	0.50 BSC			
I	1.50	1.60	1.70	
L	0.10	0.20	0.30	
D E e	1.50 1.10 1.50	1.60 1.20 0.50 BSC	1.70 1.30	



STYLE 1:	STYLE 2:	STYLE 3:
PIN 1. EMITTER 1	PIN 1. EMITTER 1	PIN 1. CATHODE 1
2. BASE 1	2. EMITTER 2	2. CATHODE 1
3. COLLECTOR 2	3. BASE 2	3. ANODE/ANODE 2
4. EMITTER 2	4. COLLECTOR 2	4. CATHODE 2
5. BASE 2	5. BASE 1	5. CATHODE 2
6. COLLECTOR 1	6. COLLECTOR 1	6. AN□DE/AN□DE 1

STYLE 6: PIN 1. CATHODE 2. ANODE

3. CATHODE

4. CATHODE 5. CATHODE

CATHODE

RECOMMENDED	MOLINITING	FOOTPRINT*
KECOMIMENDED	MOONTING	LOO INKINI.

FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

STYLE 7: PIN 1. CATHODE 2. ANODE 3. CATHODE 4. CATHODE	STYLE 8: PIN 1. DRAIN 2. DRAIN 3. GATE 4. SDURCE 5. DRAIN	STYLE 9: PIN 1. SDURCE 1 2. GATE 1 3. DRAIN 2 4. SDURCE 2
4. CATHUDE	4. SHURCE	4. SHURCE 2
5. AN□DE	5. DRAIN	5. GATE 2
6. CATHUDE	6. DRAIN	6. DRAIN 1

2. BASE 2 3. COLLECTOR 1

EMITTER 1

5. BASE 1

STYLE 11: PIN 1. EMITTER 2

STYLE 5

PIN 1. CATHODE

2. CATHODE

3. ANDDE 4. ANDDE 5. CATHODE

GENERIC MARKING DIAGRAM*



XX = Specific Device Code

M = Month Code = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking

	SOT-563-6 1.60x1.20x0.55, 0.50P		PAGE 1 OF 1	
DOCUMENT NUMBER:	98AON11126D Electronic versions are uncontrolled except when accessed directly from Printed versions are uncontrolled except when stamped "CONTROLLED			
6. ANDDE 1	6. CULLECTUR 2			

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. **onsemi** does not convey any license under its patent rights nor the rights of others.

STYLE 4: PIN 1. COLLECTOR

STYLE 10: PIN 1. CATHODE 1 2. N/C 3. CATHODE 2

5. N/C

4. ANDDE 2

2. COLLECTOR

3. BASE
4. EMITTER
5. COLLECTOR

COLLECTOR

onsemi, ONSEMI, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems. or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

 $\textbf{Technical Library:} \ \underline{www.onsemi.com/design/resources/technical-documentation}$

onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at

www.onsemi.com/support/sales