

Description

The Si21692 integrates two separate high-performance DVB-T2, DVB-T, DVB-C, DVB-S, and DVB-S2 digital demodulators into a single compact package for terrestrial, cable, and satellite TV standards. Leveraging Silicon Labs' proven digital demodulation architecture, each embedded demodulator achieves excellent reception performance for each media while significantly minimizing front-end design complexity and cost. Connecting the Si21692 to both a dual terrestrial/cable TV tuner, and a dual satellite tuner, results in a high-performance and cost optimized TV front-end solution. Silicon Labs' internally-developed DVB-T2 (including T2-Lite) demodulators support all modes specified by the DVB-T2 standard (V1.3.1). Main features of the DVB-T2 mode are, SISO and MISO support, FEF management, fully autonomous signal acquisition including automatic L1 signaling parsing support for all pilot patterns, and DVB-T2/T auto-detection. The DVB-T and DVB-C demodulators are enhanced versions of proven and broadly used Si2167/68/69 Silicon Labs devices.

The satellite demodulation functionality allows demodulating widely deployed DVB-S, DIRECTV™ (DSS) legacy standards, and new generation DVB-S2 (AMC compliant) satellite broadcasts. A zero-IF interface allows for a seamless connection to market proven satellite silicon tuners.

The Si21692 offers an on-chip blind scanning algorithm for DVB-S/S2 and DVB-C standards (as well as blind lock). It also integrates two DiSEqC™ 2.0 LNB interfaces for satellite dish control and, for each satellite demodulator, an equalizer to compensate for echoes in long cable feeds from the LNB to the satellite tuner RF input.

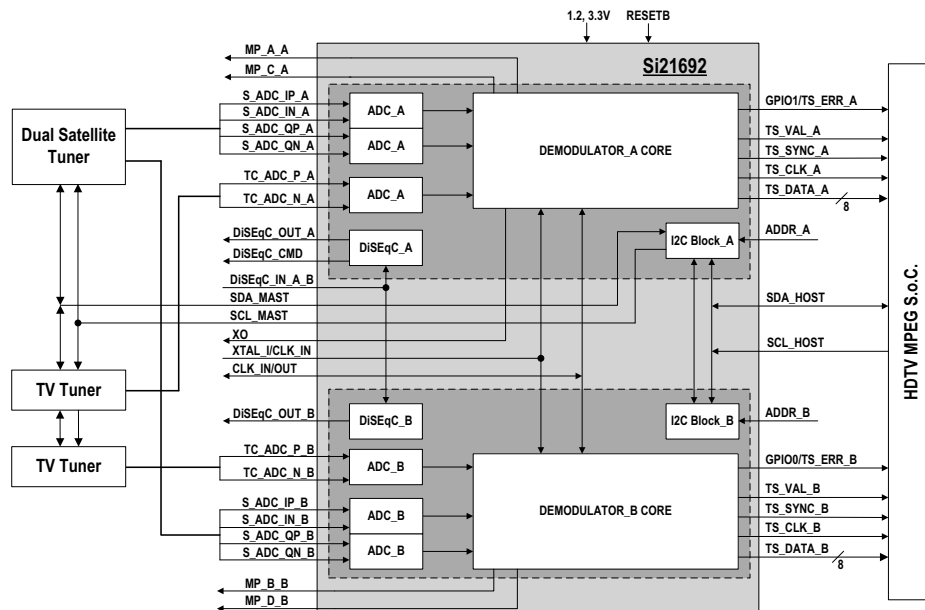
The Si21692 embeds two independent programmable transport stream interfaces which provide a flexible range of output modes and are fully compatible with all MPEG decoders or conditional access modules to support any customer application.

Features

- DVB-T2 and T2-Lite (ETSI EN 302 755-V1.3.1)
 - Bandwidth: 1.7, 5, 6, 7 or 8 MHz (and extended BW)
 - Supports up to 255 PLPs and outputs the data PLP plus the common PLP (on a single TS)
 - Scrambling of L1 post-signaling supported
 - NorDig Unified 2.4, D-Book 7 V2 compliant
- DVB-T (ETSI EN 300 744)
 - NorDig Unified 2.4, D-Book 7 V2 compliant
- DVB-C (ETSI EN 300 429) / ITU J.83 Annex A/B/C
 - 1 to 7.2 MSymbol/s, C-Book compliant
- DVB-S2 (ETSI EN 302 307 and TR102-376)
 - 1 to 45 MSymbol/s
- DVB-S (ETSI EN 300 421)
 - 1 to 45 MSymbol/s
- Dual DiSEqC™ 2.x interface, Unicable support
- Enhanced immunity to co-channel interferers
- I²C serial bus interfaces (master and host)
- Dual independent differential IF input for T/C tuners and differential ZIF I/Q inputs for satellite tuners
- GPIOs and multi-purpose ports (two per demodulator)
- Firmware control for upgradeability
- Separate flexible TS interfaces with serial or parallel outputs
- Fast lock times for all standards, including DVB-T2
- Only two power supplies: 1.2 and 3.3 V
- 8x8 mm, QFN-68 pin package, Pb-free/RoHS compliant
- Pin-to-pin and API compatible with all dual demodulator family: Si216x2

Applications

- Multi-receiver iDTV: on-board or in a NIM
- Advanced multimedia PVR STBs
- PC-TV accessories
- PVR, DVD, and Blu-Ray disc recorders

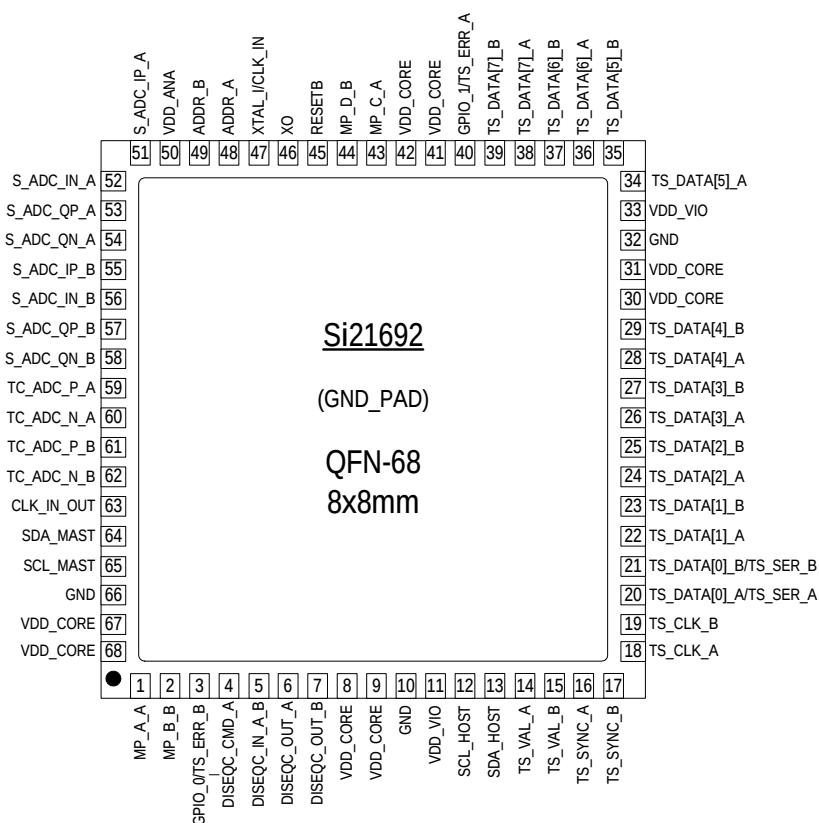


Selected Electrical Specifications

(T_A = -10 to 70 °C).

Parameter	Test Condition	Min	Typ	Max	Unit
General					
Input clock reference		4	—	30	MHz
Supported XTAL frequency		16	—	30	MHz
Total power consumption for each demodulator	DVB-T2 ¹	—	420	—	mW
	DVB-T ² /DVB-C ³	—	190/180	—	mW
	DVB-S ⁴ /DVB-S2 ⁵	—	240/480	—	mW
Thermal resistance (θ _{JA})	4 layer PCB	—	42	—	°C/W
Power Supplies					
V _{DD_VCORE}		1.14	1.20	1.30	V
V _{DD_VANA}		3.00	3.30	3.60	V
V _{DD_VIO}		3.00	3.30	3.60	V
Notes: 1. Test conditions: 8 MHz, 256 QAM, 32K FFT, CR=3/5, GI=1/128, PP7, C/N at picture failure. 2. Test conditions: 8 MHz, IF mode, 8K FFT, 64 QAM, parallel TS output. 3. Test conditions: 6.9 MBaud, IF mode, 256 QAM, parallel TS output. 4. Test conditions: 30 MBaud, CR=7/8, parallel TS (at QEF: BER = 2 · 10⁻⁴). 5. Test conditions: 32 MBaud, 3/5 Code Rate, 8PSK, pilots On, parallel TS, C/N at picture failure (PER = 10⁻⁴).					

Pin Assignments



Selection Guide

Part #	Description
Si21692-B40-GM/R	Dual Digital TV Demodulator for DVB-T2/T/C/S/S2, 8x8 mm QFN-68