HCC1000 (Through Hole) HCC1001 (SMT)

Features:

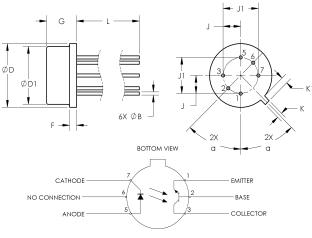
- TID Capable to 100Krad (SI)/cm2 ELDRS (0.1rad/s)
- Neutron capable to 1E12 neutrons (14MeV)
- Processed to MIL-STD-19500 TXV level
- 1 KV electrical Isolation
- Base Contact provided for conventional transistor biasing

Description:

These devices are similar to Optek's 4N series of opto isolators with exception of the chips. It is processed per MIL-PRF-19500 TXV level and can be modified per customer SCDs. Each device consists of a IRLED & NPN transistor mounted in either hermetic TO-78 metal can, 6 pin SMD or custom packaging.

Applications:

Circuit Electrical Isolation in Space Applications such as Satellites, Launchers, Space Vehicles & Planetary Rovers.



(COLLECTOR MAY OR MAY NOT BE CONNECTED INTERNALLY TO CASE)

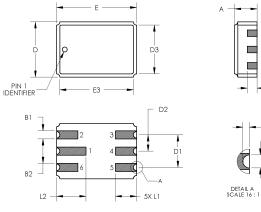
HCC1000-6 leads metal can (TO-78) Package

	DIMENSIONS				
	INC	HES	MILLIMETERS		
LTR	MIN	MAX	MIN	MAX	
ØD	0.335	0.370	8.51	9.40	
ØD1	0.305	0.335	7.75	8.51	
ØВ	0.016	0.019	0.41	0.48	
α	45° T.P.		45° T.P.		
F		0.040		1.02	
G	0.155	0.185	3.94	4.70	
J	.100 T.P.		2.54 T.P.		
J1	.200 T.P.		5.08 T.P.		
К	0.028	0.034	0.71	0.86	
K1	0.029	0.045	0.74	1.14	
L	0.500	0.600	12.70	15.24	

General Note

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1—Anode 2—N/C 3—Collector 4—Base 5—Emitter 6—Cathode

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HCC1001—6 pin SMT LCC Package

	DIMENSIONS				
	INCHES		MILLIMETERS		
LTR	MIN	MAX	MIN	MAX	
Α	0.066	0.080	1.68	2.03	
A1	0.026	0.034	0.66	0.86	
B1	0.022	0.028	0.56	0.71	
B2	.072 REF		1.83 REF		
B3	0.006	0.022	0.15	0.56	
D	0.165	0.175	4.19	4.44	
D1	0.095	0.105	2.41	2.67	
D2	0.045	0.055	1.14	1.39	
D3		0.175		4.44	
E	0.240	0.250	6.10	6.35	
E3		0.250		6.35	
L1	0.060	0.070	1.65	1.78	
L2	0.082	0.098	2.08	2.49	
L3	0.003		0.08		

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HCC1000 (Through Hole) HCC1001 (SMT)



Electrical Specifications

Absolute Maximum Ratings (T_A = 25 °C unless otherwise noted)

Storage Temperature Range	-55 °C to +150 °C
Operating Temperature Range	-55 °C to +150 °C
Input-to-Output Isolation Voltage	± 1.00 kVDC ⁽¹⁾
Lead Soldering Temperature (TO-78 Metal Can) [1/16 inch (1.6 mm) from case for 5 seconds with soldering iron]	260 °C ⁽²⁾
Soldering Temperature (SMD) Vapor Phase Reflow for 30 seconds	215 °C

Input Diode (LED)

Forward DC Current (65 °C or below)	40 mA
Reverse Voltage	2 V
Power Dissipation	60 mW ⁽³⁾

Output Phototransistor:

Continuous Collector Current	50 mA
Collector-Emitter Voltage	40 V
Collector-Base Voltage	45 V
Emitter-Base Voltage	7.0 V
Power Dissipation	300 mW ⁽⁴⁾

Notes:

1. Measured with input leads shorted together and output leads shorted together.

2. RMA flux is recommended.

3. Derate linearly 1.0 mW/°C above 65 °C.

4. Derate linearly 3.0 mW/°C above 25 °C.

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Performance

Electrical Characteristics (T_A = 25 °C unless otherwise noted)

SYMBOL	PARAMETER	MIN	ТҮР	МАХ	UNITS	TEST CONDITIONS
nput Dioc	le			•		
V _F	Forward Voltage	0.80 1.00 0.70	- -	1.70 1.9 1.50	V	I _F = 10.0 mA I _F = 10.0 mA, T _A = -55 °C I _F = 10.0 mA, T _A = 125 °C
I _R	Reverse Current	-	-	100	μΑ	V _R = 2.0 V
Output Ph	ototransistor					
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	40	-	-	V	$I_{\rm C}$ = 1.0 mA, $I_{\rm B}$ = 0, $I_{\rm F}$ = 0
V _{(BR)CBO}	Collector-Base Breakdown Voltage	45	-	-	V	$I_{c} = 100 \ \mu\text{A}, I_{B} = 0, I_{F} = 0$
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	7	-	-	V	$I_E = 100 \ \mu A, I_C = 0, I_F = 0$
${\sf I}_{C(OFF)}^1$	Collector-Emitter Dark Current	-	-	100	nA	V _{CE} = 20 V, I _B = 0, I _F = 0
I _{C(OFF)} ²	Collector-Emitter Dark Current	-	-	100	μΑ	V _{CE} = 20 V, I _B = 0, I _F = 0, T _A = 100 °C
I _{CB(OFF)}	Collector-Base Dark Current	-	-	10	nA	V _{CB} = 20 V, I _E = 0, I _F = 0
Coupled						
I _{C(ON)}	On-State Collector Current	1 15 10 15	- - -	- - -	mA	$\begin{split} I_F &= 1.0 \text{ mA}, $
		2.8 2.0	- -	-		$I_F = 2.0 \text{ mA}, V_{CE} = 5.0 \text{ V}, I_B = 0, T_A = -55 \text{ °C}$ $I_F = 2.0 \text{ mA}, V_{CE} = 5.0 \text{ V}, I_B = 0, T_A = 100 \text{ °C}$
I _{CB(ON)}	On-State Collector Base	30	-	-	μΑ	$V_{CB} = 5 \text{ V}, I_{E} = 0, I_{F} = 10 \text{ mA}$
V _{CE(SAT)}	Collector-Emitter Saturation Voltage	-	-	0.30	v	I _F = 20.0 mA, I _C = 10.0 mA, I _B = 0
H_{FE}	DC Current Gain	100	-	-	V	V_{CE} = 5.0 V , I_{C} = 10.0 mA, I_{F} = 0 mA
R _{IO}	Resistance (Input-to-Output)	10 ¹¹	-	-	Ω	$V_{I-O} = \pm 1000 \text{ VDC}^{(1)}$
CIO	Capacitance (Input-to-Output)	-	-	5	pF	V _{I-O} = 0 V, f = 1.0 MHz ⁽¹⁾
T _{R,} T _F	Rise and Fall Time	-	-	20	μs	V_{CC} = 10.0 V , I_F = 10.0 mA, R_L = 100 Ω

Notes:

1. Measured with input leads shorted together and output leads shorted together.

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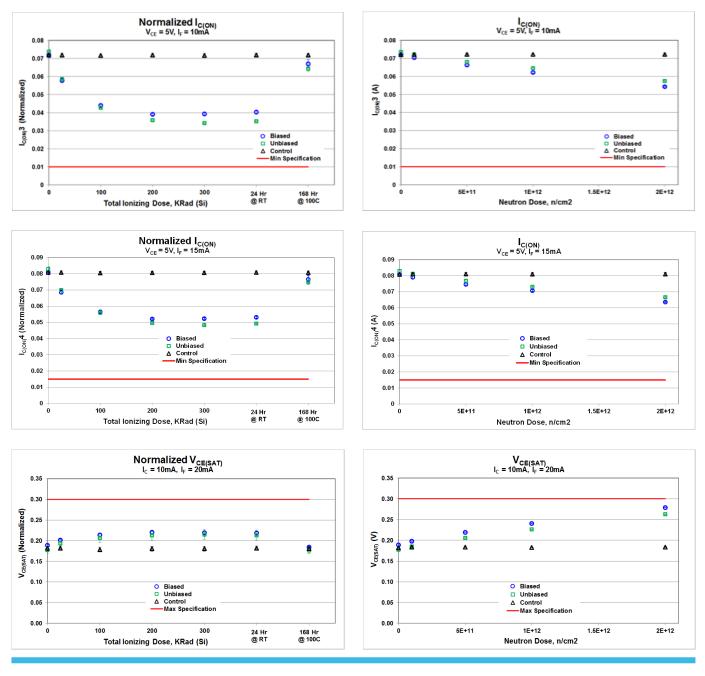
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Radiation Test Standards:

- Total Ionizing Dose: MIL-STD-883 Method 1019.7 and ASTM F1892-06 (0.1rad (si)/s) dose rate
- Neutron: MIL-STD-883 Method1017.2 and ASTM Designation: E 772—94
- Full Radiation report available



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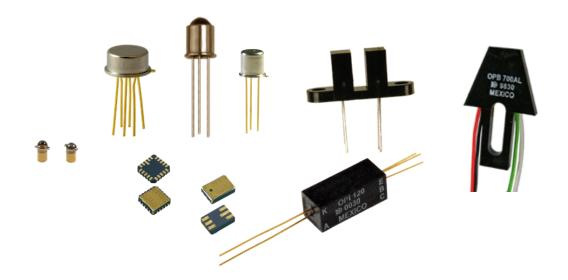
HCC1000 (Through Hole) HCC1001 (SMT)



Packaging

Package styles available:

Radiation testing was in a TO-78 can; however, functional & radiation samples can be supplied in discrete pairs such as, "pills" or TO-46 / TO-18 metal cans, 4 & 6 pin Hermetic Ceramic LLC, high voltage assemblies like the OPI120 and OPI150 hermetic high voltage isolators and more.



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