





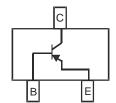
## LOW $V_{\text{CE(SAT)}}$ PNP SURFACE MOUNT TRANSISTOR

#### **Features**

- Epitaxial Planar Die Construction
- Complementary NPN Type Available (DNLS160)
- Surface Mount Package Suited for Automated Assembly
- Lead Free/RoHS Compliant (Note 1)
- "Green Device" (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

# 7

SO1-23



Schematic and Pin Configuration

#### **Mechanical Data**

- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.008 grams (approximate)

### **Maximum Ratings** @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	
Collector-Base Voltage	V <sub>CBO</sub>	-80	V	
Collector-Emitter Voltage	V <sub>CEO</sub>	-60	V	
Emitter-Base Voltage	$V_{EBO}$	-5	V	
Collector Current - Continuous	Ic	-1	Α	
Peak Pulse Collector Current	I <sub>CM</sub>	-2	Α	
Base Current (DC)	I <sub>B</sub>	-300	mA	

#### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @ T <sub>A</sub> = 25°C	P <sub>D</sub>	300	mW
Thermal Resistance, Junction to Ambient (Note 3) @ T <sub>A</sub> = 25°C	$R_{ heta JA}$	417	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

Notes:

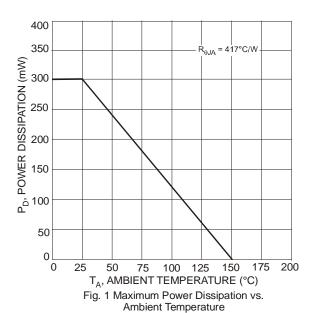
- No purposefully added lead.
- 2. Diode's Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.
- 3. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on page 4 or in Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

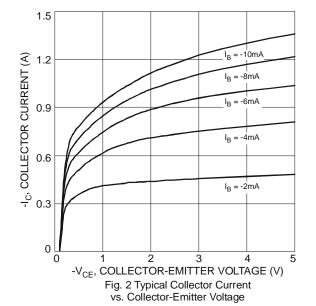


#### **Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

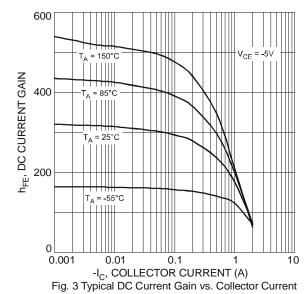
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 4)								
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	-80		_	<b>V</b>	$I_C = -100 \mu A, I_E = 0$		
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	-60		_	V	$I_C = -10 \text{mA}, I_B = 0$		
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	-5	_	_	V	$I_E = -100 \mu A, I_C = 0$		
Collector Cutoff Current	lone			-100	nA	$V_{CB} = -60V, I_{E} = 0$		
Concetor Caton Current	I <sub>CBO</sub>			-50	μΑ	$V_{CB} = -60V$ , $I_E = 0$ , $T_A = 150$ °C		
Collector Cutoff Current	I <sub>CES</sub>	_	_	-100	nA	$V_{CE} = -60V, V_{BE} = 0$		
Emitter Cutoff Current	I <sub>EBO</sub>	_		-100	nA	$V_{EB} = -5V, I_C = 0$		
ON CHARACTERISTICS (Note 4)								
		200	325	_		$V_{CE} = -5V$ , $I_C = -1mA$		
DC Current Gain	h <sub>FE</sub>	150	250 —	_	V	$V_{CE} = -5V, I_{C} = -500mA$		
		100	180			$V_{CE} = -5V, I_{C} = -1A$		
		_	-90	-160		$I_C = -100 \text{mA}, I_B = -1 \text{mA}$		
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	_	-90	-175	mV	$I_C = -500 \text{mA}, I_B = -50 \text{mA}$		
		_	-160	-330		$I_C = -1A$ , $I_B = -100mA$		
Collector-Emitter Saturation Resistance	R <sub>CE(SAT)</sub>		160	330	mΩ	$I_C = -1A$ , $I_B = -100mA$		
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>	_	-0.95	-1.1	V	$I_C = -1A$ , $I_B = -50mA$		
Base-Emitter Turn On Voltage	V <sub>BE(ON)</sub>	_	-0.82	-0.9	V	V <sub>CE</sub> = -5V, I <sub>C</sub> = -1A		
SMALL SIGNAL CHARACTERISTICS								
Output Capacitance	C <sub>obo</sub>	_	10	15	рF	V <sub>CB</sub> = -10V, f = 1.0MHz		
Current Gain-Bandwidth Product	$f_{T}$	150	220	_	MHz	$V_{CE} = -10V$ , $I_{C} = -50mA$ , $f = 100MHz$		

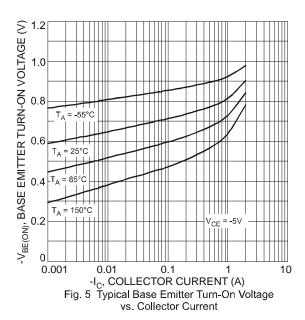
Notes: 4. Measured under pulsed conditions. Pulse width =  $300\mu$ s. Duty cycle  $\leq 2\%$ .

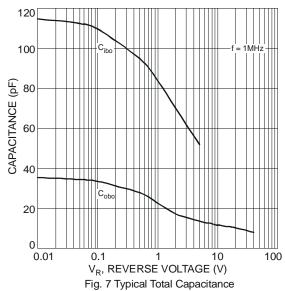












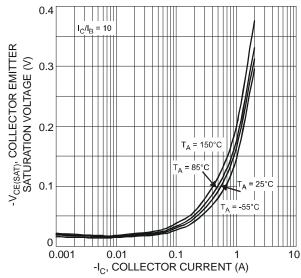


Fig. 4 Typical Collector Emitter Saturation Voltage vs. Collector Current

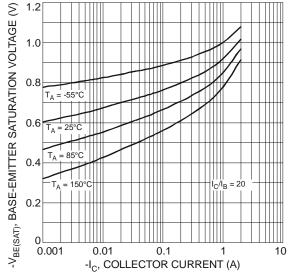
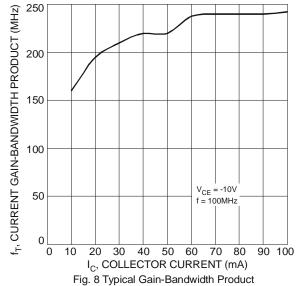


Fig. 6 Typical Base-Emitter Saturation Voltage vs. Collector Current



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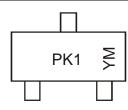


#### Ordering Information (Note 5)

Device	Packaging	Shipping
DPLS160-7	SOT-23	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

## **Marking Information**



PK1 = Product Type Marking Code YM = Date Code Marking

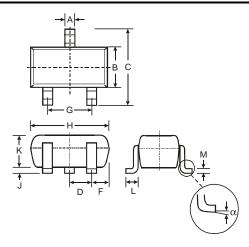
Y = Year ex: U = 2007

M = Month ex: 9 = September

#### Date Code Key

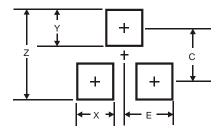
Year	2008	;	2009	2010	1	2011	2012		2013	2014	Ļ	2015
Code	V		W	Х		Υ	Z		Α	В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

# **Package Outline Dimensions**



SOT-23					
Dim	Min	Max			
Α	0.37	0.51			
В	1.20	1.40			
С	2.30	2.50			
D	0.89	1.03			
F	0.45	0.60			
G	1.78	2.05			
Н	2.80	3.00			
J	0.013 0.10				
K	0.903	1.10			
L	0.45	0.61			
М	0.085	0.180			
α	0°	8°			
All Dir	All Dimensions in mm				

## **Suggested Pad Layout**



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35

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