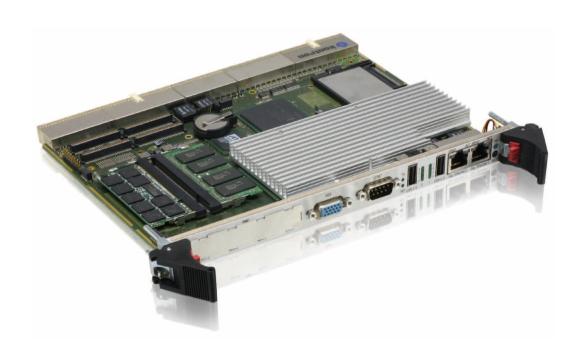


# » CP6012 «



### 6U Intel® Core™ Duo Processor PICMG 2.16 Blade

### » Highest Performance / Watt

Intel® T2500 Duo processor 2.0 GHz

### » Highest Memory Density

Up to 4 GByte dual-channel, DDR2, 400 MHz memory

### » Highest Versatility

Comprehensive I/O capabilities: GigEthernet, PMC/XMC, USB, VGA, SATA, CompactFlash ...

If it's embedded, it 's Kontron.

## The Power of Intel® Core™ Duo

### Nearly double your processing power...

Explore the power and the potential of two cores in one processor with Kontron's CP6012 based on the Intel® Core™ Duo processor.

The CP6012, a 6U CompactPCI CPU board with Intel® Core™ Duo processor meets the highest performance demands. Combined with the E7520 and 6300ESB chipset, it handles server-like data throughput and provides next generation bandwidth capabilities.

#### » Greater Performance / Watt

Compared to previous processor designs the dual-core technology allows approximately twice the performance at similar power consumption.

The PICMG 2.16-compliant Kontron CP6012 offers up to 4 GB dual-channel 400 MHz DDR2 registered ECC SDRAM (via two 200-pin SODIMM sockets), providing up to 6.4 GB/sec data throughput. The CP6012 is designed for bandwidthintensive applications and thanks to hotswap support and IPMI (PICMG 2.9 - compliant Intelligent Platform Management Interface) the CPU board meets the highest demands for the management of high-availability applications. Many of these are data and tele-communications applications, but also include highly sensitive, securityrelated solutions as well as image processing systems in medical technology and other vertical industries.

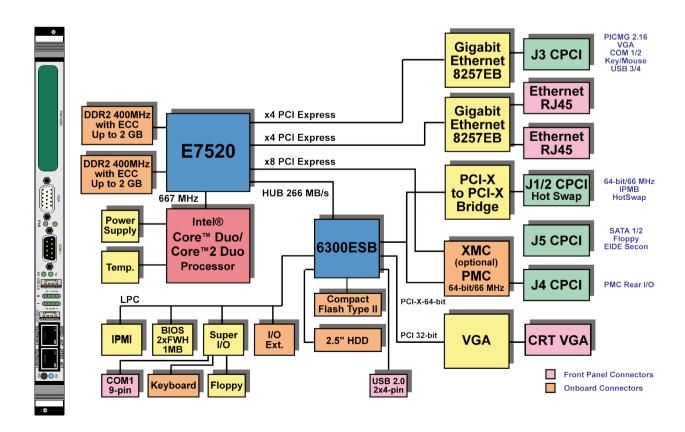
#### » Unique Versatility

The highly integrated CP6012 features a XMC site according to XMC.3 supporting x8 PCI Express (alternatively a 64/66 PCI PMC site), an onboard 2.5-inch SATA hard disk and CompactFlash - all usable at the same time in a single slot.

The Intel 6300ESB I/O Controller Hub provides advanced I/O technology including USB 2.0 and Serial ATA150. Four Gigabit Ethernet ports (2x ports at the front and 2x for full PICMG 2.16 support) provide comprehensive connectivity capabilities, enabling innovative applications today by offering enough headroom for the emerging next generation requirements. Highly versatile, the CP6012 can be used in a system or peripheral slot. A rich set of LEDs at the front panel for debug and diagnose, as well as full rear I/O connectivity completes the CP6012.

#### » Longterm Availability

Delivering a stable product based on Intel's embedded product line, the CP6012 ensures long term availability. This eliminates the risk of unplanned design changes and unexpected expensive application modification. While minimizing deployment risks, the CP6012 provides a broad range of software support to ease the process of product integration and maximize the competitive advantage of meeting the time-to-market window.



Up to 4 GB DDR2 4 max. 6.4 GB/sec d: 2 redundant 1 MB f 8 kB for storing CM Four 10/100/1000 controllers (two co Four USB 2.0 interf VGA Video Controller Woo 16C550 compa Keyboard on rear a Mouse interface on Floppy disk control pin D-Sub (RS232) b-pin D-Sub (RS232) b-pin D-Sub (RS232) compact LAN activity (yell without	ata throughput Firmware Hubs (FWH) for BIO: AND data when operating with MB/s Gigabit Ethernet ports Supper ports are routed to the faces with up to 480 Mbit/see FAIT ES1000 2D-engine, PCI Itible UARIS (COM1/2) Ind onboard connector Iter on rear  Connector  C front panel C for BIOS POST code or general d  D for BIOS POST code or general d  C front panel C	based on two Intel® 82571EB dual front and two copper ports are rouc, two front, two rear (32-bit / 33 MHz, external memory) and the control LED for hot swap, 2x follows	l Gigabit Ethernet PCI Express bus uted to PICMG 2.16 rear pins) y 64MB
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2,0	1 2 2 1 1	-	1 4 2
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	1 1	-	
	1	-	1
	0	1	1
	2	-	4
	1	<u>-</u>	1
	2	1	2
		1	1
	via J4	4/1	1
	1	-	1
<ul> <li>» PICMG 2.0 Rev. 3.0 compatible, 64-bit / 66 MHz</li> <li>» 5V or 3.3V signalling, REQ/GNT for 7 slots</li> <li>» Operating in system slot as system master and in peripheral slot in PCI passive mode (no communication to CPCI but a support of the property of the pro</li></ul>			
» One 64-bit / 66MHz PMC slot, Pn1-Pn4, rear I/O Pn3 to J4, 3.3 V PCIvoltage » Alternatively one XMC slot via P15, supporting XMC.3 x8 PCIExpress			
<ul> <li>Watchdog, software configurable, 125 msec to 256 sec, generates IRQ, NMI or hardware reset</li> <li>Hardware monitor for thermal control, fan speed, and all onboard voltages</li> <li>RTC (integrated in 6300ESB) and CMOS RAM with backup, battery replaceable</li> </ul>			
» J3: PICMG 2.16, VGA, COM 1/2, keyboard, mouse, USB 3/4 » J4: PMC rear I/O » J5: SATA 1/2, IDE (secondary)			
IPMI 1.5-compliant for IPMI based management and CompactPCI System Management PICMG 2.9 R1.0			
CompactPCI Core Sp CompactPCI Hot Sw CompactPCI System CompactPCI Packet Sesigned to meet or Safety: UL 1950,	pecification PICMG 2.0 Rev. 3.0 ap Specification PICMG 2.1 R2. Management PICMG 2.9 R1.0 Switching Backplane PICMG 2. exceed: UL 94, CSA 22.2 No 950, EN	.0 16 R1.0 60950, IEC 950	
233 x 160 x 20.5 mm, 6U, 4HP			
333	RTC (integrated in J3: PICMG 2.16, VI J4: PMC rear I/O J5: SATA 1/2, IDE PMI 1.5-compliant CompactPCI Core Sp CompactPCI Hot Sw CompactPCI System CompactPCI System CompactPCI Packe Signed to meet or Safety: UL 1950, EMI/EMC: EN 551  33 x 160 x 20.5 mm	RTC (integrated in 6300ESB) and CMOS RAM with J3: PICMG 2.16, VGA, COM 1/2, keyboard, mous J4: PMC rear I/O J5: SATA 1/2, IDE (secondary)  PMI 1.5-compliant for IPMI based management at CompactPCI Core Specification PICMG 2.0 Rev. 3.0 CompactPCI Hot Swap Specification PICMG 2.1 R2 CompactPCI System Management PICMG 2.9 R1.0 CompactPCI Packet Switching Backplane PICMG 2.9 signed to meet or exceed:  » Safety: UL 1950, UL 94, CSA 22.2 No 950, EN SEMI/EMC: EN 55022 / EN 55024, EN 50081-1	RTC (integrated in 6300ESB) and CMOS RAM with backup, battery replaceable  J3: PICMG 2.16, VGA, COM 1/2, keyboard, mouse, USB 3/4  J4: PMC rear I/0  J5: SATA 1/2, IDE (secondary)  PMI 1.5-compliant for IPMI based management and CompactPCI System Management  CompactPCI Core Specification PICMG 2.0 Rev. 3.0  CompactPCI Hot Swap Specification PICMG 2.1 R2.0  CompactPCI System Management PICMG 2.9 R1.0  CompactPCI System Management PICMG 2.9 R1.0  CompactPCI Packet Switching Backplane PICMG 2.16 R1.0  esigned to meet or exceed:  » Safety: UL 1950, UL 94, CSA 22.2 No 950, EN 60950, IEC 950  » EMI/EMC: EN 55022 / EN 55024, EN 50081-1 / EN 6100-6-2

Technical Informati	on	
Software Support	<ul> <li>AMI BIOS with POST codes, setup console redirection to serial port (VT100 mode) with CMOS setup access, BIOS parameters saved in EEPROM, diskless, keyboardless, videoless operation</li> <li>LAN boot support</li> <li>Board identification number accessible via EEPROM</li> <li>Support for Windows® XP, XP Embedded, Windows® Server 2003, Linux®, VxWorks (other OSs may be possible, please contact us for information)</li> </ul>	
Power Consumption	CP6012 equipped with 2GB memory:  » LV 1.66 GHz: max 34 W  » 2.0 GHz: max 52 W	
Environmental		
Operating temp.	0°C to +60°C standard with LV 1.66 GHz (with forced airflow)	
Storage temp.	- 55°C to + 85°C (without battery or HDD)	
Climatic Humidity	93% RH at 40°C, non condensing (acc. to IEC 60068-2-78)	
Altitude	50,000 ft (15,240 m)	

Ordering Information				
Article	Order-No.	Description		
CP6012	34567	Intel® Core™ Duo L2400 LV 1.66 GHz, 2xGigEthernet on FP, 2xGigEthernet on PICMG2.16/RIO, 5V I/O		
CP6012	35324	Intel® Core™ Duo L2400 LV 1.66 GHz, 2xGigEthernet on FP, 2xGigEthernet on PICMG2.16/RIO, 3.3V I/O		
CP6012	1021-9624	Intel® Core™ Duo T2500 2.0 GHz, 2xGigEthernet on FP, 2xGigEthernet on PICMG2.16/RIO, 5V I/O		
CP6012	1021-9625	Intel® Core™ Duo T2500 2.0 GHz, 2xGigEthernet on FP, 2xGigEthernet on PICMG2.16/RIO, 3.3V I/O		
Memory Modules	-			
SODIMM-DDR2-512-ECC	33473	512MB, SODIMM, DDR2 SDRAM, PC400, 200-pin, registered ECC		
SODIMM-DDR2-1G-ECC	33474	1GB, SODIMM, DDR2 SDRAM, PC400, 200-pin, registered ECC		
SODIMM-DDR2-2G-ECC	34847	2GB, SODIMM, DDR2 SDRAM, PC400, 200-pin, registered ECC		
Services				
CP6-RIO-216	27829	Assembly of connectors J4/J5 and rear IO configuration for CP6012		
CP6-RIO-216-NOJ4	27830	Assembly of connector J5 (no J4) and rear IO configuration for CP6012		
CP6012-MK2.5SATA 1)	33477	Mounting kit for 2,5" SATA-HDD onboard, mounting within 4HP		
Rear Transition Modules				
CP-CTM80-3		Various 4HP versions available		
Software Support	-			
KIT-CP6012 <sup>2)</sup>	33475	Documentation and Windows driver kit on CD-ROM		
LIN-BSP-CP6012 2)	33476	Linux BSP CP6012 for Suse and RedHat		
VXW-BSP-CP6012	36157	VxWorks 6.4 BSP CP6012 with single core support		
VXW-BSP-CP6012-SMP	1021-9791	VxWorks 6.6 BSP CP6012 with SMP support		

HDD must be ordered separately
 Free of charge, downloadable from the Internet
 Please contact your local sales representative for other configuration options.

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