

Notes

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Property	Value	Unit
number of bits	8	bit
ROM size	60	Kbyte
ROM type	Flash	-
RAM size	3	Kbyte
internal data bus width	8	bit
LCD drive capability	80	ch
SEI	2	ch
UART	1	ch
UART/SIO	1	ch
I ² C	1	ch
A/D converters(10bit)	8	ch
Timer/counter(16bit)	2	ch
Timer/counter(8bit)	4	ch
watchdog timer	Available	-
dual clock operation	Available	-
Clock gear function	Available	-

Power-on reset circuit	Available	-
Voltage detecting circuit	Available	-
On-chip debug function	Available	-
Internal Oscillator	Not Available	-
I/O Port	53	port

The minimum instruction execution time in low-speed dual clock operation mode is 122μs (at 32.768kHz). Configurable as UART or SIO. Also, selectable from I2C and SIO. One SIO channel can be used simultaneously. Two ports are reserved for high-speed oscillator pins and cannot be used as I/O ports.

Supply Voltage (V)	Operation Frequency (MHz)	Operating Temp (degC)	Minimum instruction execution time (μsec)
3.2 to 3.6	2 to 6	-40 to 85	0.167
2.7 to 3.6	2 to 4	-40 to 85	0.25

Development Systems




➤ [TLCS-870/C1 Series Development System TMP89FS28LFG](#)

Purchase and Sample

Stock Check & Purchase

➤ [Global Sales](#)


Document

Data sheet	 TMP89FS28LFG Data sheet/English (PDF: 97KB)
Environment Information	 Certificate Regarding EU RoHS(2011/65/EU)Controlled Substances [Jun,2017] (PDF: 55KB)
Environment Information	 Certificate on Content of SVHC of REACH [Jun,2017] (PDF: 44KB)

Related Links

- [Evaluation Kits and Reference Boards](#)
- [Application Notes / Sample Program](#)
- [Partner Information](#)

Notes

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