

SiBRAIN for Kinetis MKV42F256VLH16



PID: MIKROE-6374

SiBRAIN Card is a standardized add-on board, which allows very simple installation and replacement of the microcontroller unit (MCU) on a development board equipped with the SiBRAIN socket. By introducing the new SiBRAIN standard, we have ensured the absolute compatibility between the development board and any of the supported MCUs, regardless of their pin number and compatibility. SiBRAIN Cards are equipped with two 168-pin mezzanine connectors, allowing them to support even MCUs with extremely high pin count. Their clever design allows very simple usage, following the well-established plug & play concept of the Click board™ line of product.

Specifications

Type	8th Generation
Architecture	ARM Cortex-M4
MCU Memory (KB)	256
Silicon Vendor	NXP
Pin count	64
RAM (Bytes)	32768
Supply Voltage	3.3V

Mikroe produces entire development toolchains for all major microcontroller architectures.

Committed to excellency, we are dedicated to helping engineers bring the project development up to speed and achieve outstanding results.



ISO 27001: 2013 certification of informational security management system.
ISO 14001: 2015 certification of environmental management system.
OHSAS 18001: 2008 certification of occupational health and safety management system.



ISO 9001: 2015 certification of quality management system (QMS).

Downloads

[SiBRAIN Standard Specification](#)

[MKV42F256VLH16 datasheet](#)

[SiBRAIN for MKV42F256VLH16 schematic](#)

Mikroe produces entire development toolchains for all major microcontroller architectures.

Committed to excellency, we are dedicated to helping engineers bring the project development up to speed and achieve outstanding results.



ISO 27001: 2013 certification of informational security management system.
 ISO 14001: 2015 certification of environmental management system.
 OHSAS 18001: 2008 certification of occupational health and safety management system.



ISO 9001: 2015 certification of quality management system (QMS).