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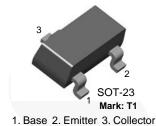


March 2014

BCX17 PNP General-Purpose Amplifier

Description

This device is designed for general-purpose amplifiers and switching applications at currents to 0.5 A. Sourced from process 78.



Ordering Information

Part Number	Marking	Package	Packing Method
BCX17	T1	SOT-23 3L	Tape and Reel

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CEO}	Collector-Emitter Voltage	-45	V
V _{CBO}	Collector-Base Voltage	-50	V
V _{EBO}	Emitter-Base Voltage	-5	V
I _C	Collector Current - Continuous	-500	mA
T_{J} , T_{STG}	Junction and Storage Temperature Range	-55 to +150	°C

Downloaded from Arrow.com.

Thermal Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Max.	Unit
В	Total Device Dissipation: Alumina Substrate, T _A = 25°C ⁽¹⁾	300	mW
P_{D}	Derate Above T _A = 25°C	2.4	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	417	°C/W

Note:

1. Alumina = 0.4 inch x 0.3 inch x 0.024 inch 9.5% alumina.

Electrical Characteristics

Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	$I_C = -10 \text{ mA}, I_B = 0$	-45		V
V _{(BR)CES}	Collector-Emitter Breakdown Voltage	$I_C = -10 \mu A, I_E = 0$	-50		V
I _{CBO}	Collector Cut-Off Current	$V_{CB} = -20 \text{ V}, I_{E} = 0$		-100	nA
		$V_{CB} = -20 \text{ V}, I_{E} = 0,$ $T_{A} = 150^{\circ}\text{C}$		-5	μА
I _{EBO}	Emitter Cut-Off Current	$V_{EB} = -5.0 \text{ V}, I_{C} = 0$		-10	μΑ
h _{FE}	DC Current Gain	$I_C = -100 \text{ mA}, V_{CE} = -1.0 \text{ V}$	100	600	
		$I_C = -300 \text{ mA}, V_{CE} = -1.0 \text{ V}$	70		
		$I_C = -500 \text{ mA}, V_{CE} = -1.0 \text{ V}$	40		
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = -500 \text{ mA}, I_B = -50 \text{ mA}$		-0.62	V
V _{BE} (on)	Base-Emitter On Voltage	$I_C = -500 \text{ mA}, V_{CE} = -1.0 \text{ V}$		-1.2	V

Physical Dimensions

0.95 2.92±0.20 3 1.40 1.30^{+0.20}_{-0.15} 2.20 0.60 0.37 (0.29) -0.95 ⊕ 0.20M A B 1.00 1.90 1.90 LAND PATTERN RECOMMENDATION 1.20 MAX SEE DETAIL A (0.93)0.10 ○ 0.10 M C С 2.40±0.30 NOTES: UNLESS OTHERWISE SPECIFIED **GAGE PLANE** A) REFERENCE JEDEC REGISTRATION TO-236, VARIATION AB, ISSUE H. B) ALL DIMENSIONS ARE IN MILLIMETERS. 0.23 0.08 C) DIMENSIONS ARE INCLUSIVE OF BURRS, 0.25 MOLD FLASH AND TIE BAR EXTRUSIONS. D) DIMENSIONING AND TOLERANCING PER ASME Y14.5M - 1994. 0.20 MIN E) DRAWING FILE NAME: MA03DREV10 SEATING

SOT-23

Figure 1. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE (ACTIVE)

PLANE

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DETAIL A





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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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