

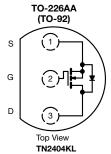
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RoHS

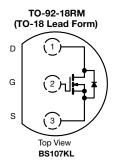
COMPLIANT

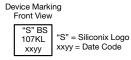
HALOGEN FREE

N-Channel 240 V (D-S) MOSFET









PRODUCT SUMMARY					
PART NUMBER	TN2404K	TN2404K, BS107KL			
V _{DS} (V)	240				
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 10 \text{ V}$	4				
Q _g typ. (nC)	4.87				
I _D (A) a, e	0.2	0.3			
Configuration	Single				

FEATURES

- Low on-resistance: 4 W
- Secondary breakdown free: 260 V
- Low power / voltage driven
- Low input and output leakage
- Excellent thermal stability
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- High-voltage drivers: relays, solenoids, lamps, hammers, displays, transistors, etc.
- Telephone mute switches, ringer circuits
- Power Supply, Converters
- Motor Control

(SOT-23) 3 D TN2404K

TO-236

Marking Code: K1ywl K1 = Part Number Code for TN2404K y = Year Code . w = Week Code I = Lot Traceability

BENEFITS

- · Low offset voltage
- Full-voltage operation
- · Easily driven without buffer
- · Low error voltage
- No high-temperature "Run-Away"

ORDERING INFORMATION					
Package	TO-236 (SOT-23)	TO-226AA (TO-92)	TO-92-18RM (TO-18 Lead Form)		
Lead (Pb)-free	TN2404K-T1-E3				
Lead (Pb)-free and halogen-free	TN2404K-T1-GE3				
Tape and Reel		TN2404KL-TR1-E3	BS107KL-TR1-E3		
Alternate manufacturing location	TN2404K-T1-BE3				

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ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)						
PARAMETER		SYMBOL	TN2404K	TN2404KL/BS107KL	SYMBOL	
Drain-source voltage		V _{DS}	24	V		
Gate-source voltage		V _{GS}	± 20		v	
Continuous drain current (T, I = 150 °C)	T _A = 25 °C	- I _D	0.2	0.3		
Continuous drain current (1) = 150 °C)	T _A = 70 °C		0.16	0.25	Α	
Pulsed drain current (t = 300 μs)		I _{DM}	0.8	1.4		
Maximum power dissipation	T _A = 25 °C	- P _D	0.36	0.8	W	
	T _A = 70 °C		0.23	0.51	vv	
Thermal resistance junction-to-ambient		R _{thJA}	350b	156	°C/W	
Operating junction and storage temperature range		T _J , T _{stg}	- 55 t	°C		

Notes

- a. Pulse width limited by maximum junction temperature
- b. Surface mounted on an FR4 board

SPECIFICATIONS (T _A = 25 °C, unless otherwise noted)							
PARAMETER			LIMITS				
PARAMETER	STWIBUL	SYMBOL TEST CONDITIONS		TYP. a	MAX.	UNIT	
Static							
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 100 \mu\text{A}$	240	257	-	V	
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.8	1.65	2	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA	
Zoro goto voltago drain ourrent		$V_{DS} = 192 \text{ V}, V_{GS} = 0 \text{ V}$	1		1		
Zero gate voltage drain current	I _{DSS}	V _{DS} = 192 V, V _{GS} = 0 V, T _J = 55 °C	-	-	10	μΑ	
On-state drain current ^a	,	V _{DS} = 10 V, V _{GS} = 10 V	0.8	-	=.	Α	
On-state drain current	I _{D(on)}	V _{DS} = 10 V, V _{GS} = 4.5 V	0.5	-	=.		
		$V_{GS} = 10 \text{ V}, I_D = 0.3 \text{ A}$	-	2.2	4		
Drain-source on-state resistance a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 0.2 \text{ A}$	-	2.3	4	W	
		$V_{GS} = 2.5 \text{ V}, I_D = 0.1 \text{ A}$	- 2.4 6				
Forward transconductance a	9 _{fs}	$V_{DS} = 10 \text{ V}, I_D = 0.3 \text{ A}$	-	1.6	=.	S	
Diode forward voltage	V _{SD}	$V_{GS} = 0 \text{ V}, I_S = 0.3 \text{ A}$	-	0.8	1.2	V	
Dynamic ^b							
Total gate charge	Qg		-	4.87	8		
Gate-source charge	Q _{gs}	$V_{DS} = 192 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 0.5 \text{ A}$	-	0.56	=.	nC	
Gate-drain charge	Q _{gd}		-	1.53	=.		
Turn-on delay time	t _{d(on)}		-	5	10		
Rise time	t _r	$V_{DD} = 60 \text{ V}, R_{L} = 200 \Omega$	-	12	20	no	
Turn-off delay time	t _{d(off)}	$I_D \cong 0.3 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 25 \Omega$	-	35	60	ns	
Fall time	t _f		-	16	25		

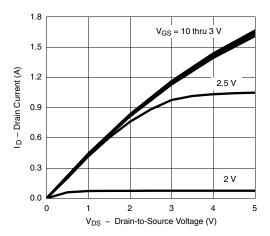
Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing

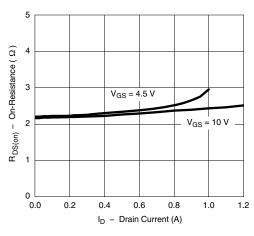
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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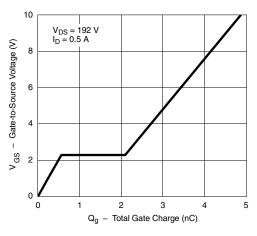
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



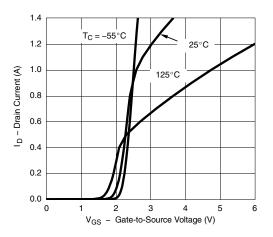
Output Characteristics



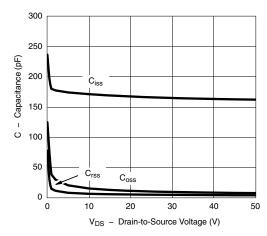
On-Resistance vs. Drain Current



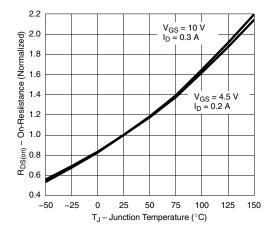
Gate Charge



Transfer Characteristics



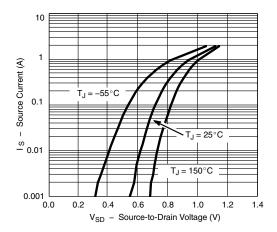
Capacitance

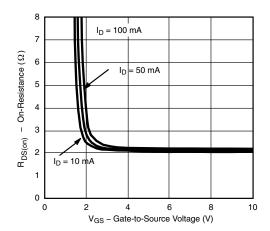


On-Resistance vs. Junction Temperature

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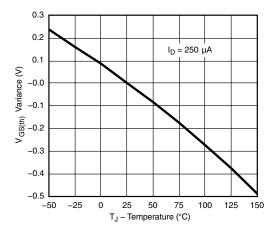
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





Source-Drain Diode Forward Voltage

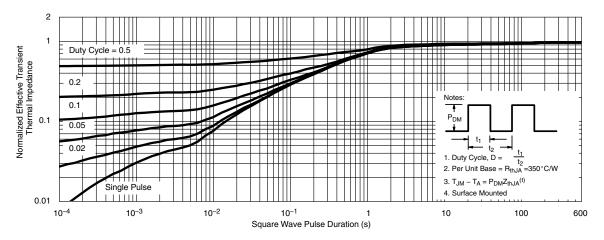
On-Resistance vs. Gate-to-Source Voltage



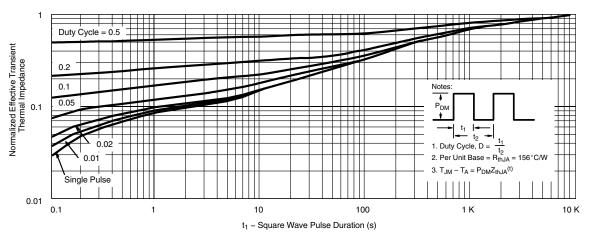
Threshold Voltage

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient (TO-236, TN2404K only)

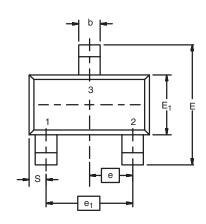


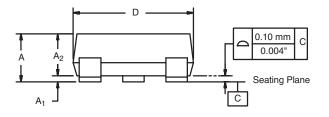
Normalized Thermal Transient Impedance, Junction-to-Ambient (TO-226AA, TN2404KL and TO-92-18RM, BS107KL only)

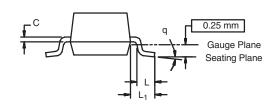
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SOT-23 (TO-236): 3-LEAD







Dim	MILLIN	IETERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.64 Ref 0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	

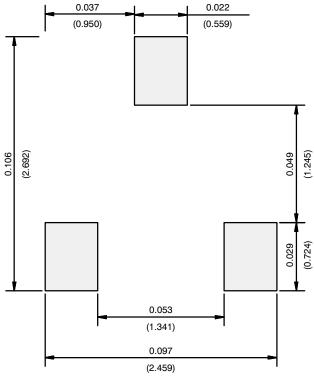
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RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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