TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX02F, TC74LCX02FT, TC74LCX02FK

Low-Voltage Quad 2-Input NOR Gate with 5-V Tolerant Inputs and Outputs

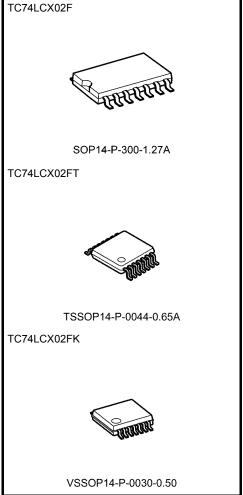
The TC74LCX02 is a high-performance CMOS 2-input NOR gate. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

The device is designed for low-voltage (3.3 V) V_{CC} applications, but it could be used to interface to 5-V supply environment for inputs.

All inputs are equipped with protection circuits against static discharge.

Features

- Low-voltage operation: V_{CC} = 1.65 to 3.6 V
- High-speed operation: $t_{pd} = 5.2 \text{ ns (max) (V}_{CC} = 3.0 \text{ to } 3.6 \text{ V)}$
- Output current: $|I_{OH}|/I_{OL} = 24 \text{ mA (min) (V}_{CC} = 3.0 \text{ V)}$
- Latch-up performance: -500 mA
- Available in JEITA SOP, TSSOP and VSSOP(US)
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 02 type



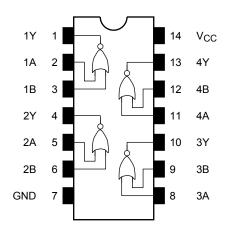
Weight

SOP14-P-300-1.27A : 0.18 g (typ.) TSSOP14-P-0044-0.65A : 0.06 g (typ.) VSSOP14-P-0030-0.50 : 0.02 g (typ.)

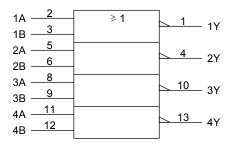
Note: The Electrical Characteristics of V_{CC} =1.8±0.15V is only applicable for products which manufactured from January 2009 onward.

Start of commercial production 1994-10

Pin Assignment (top view)



IEC Logic Symbol



Truth Table

Inp	uts	Outputs		
Α	В	Υ		
L	L	Н		
L	Н	L		
Н	L	L		
Н	Н	L		

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	-0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to 7.0	V
		-0.5 to 7.0 (Note 2)	
DC output voltage	Vout	-0.5 to V _{CC} 0.5 (Note 3)	V
Input diode current	I _{IK}	-50	mA
Output diode current	lok	±50 (Note 4)	mA
DC output current	Гоит	±50	mA
Power dissipation	PD	180	mW
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: $V_{CC} = 0 V$

Note 3: High or low state. I_{OUT} absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$



Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	1.65 to 3.6	V
rower supply voltage	VCC.	1.5 to 3.6 (Note 2)	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to 5.5 (Note 3)	V
Output voltage		0 to V _{CC} (Note 4)	V
Output current	I _{OH} /I _{OI}	±24 (Note 5)	mA
Output current	iOH/iOL	±12 (Note 6)	ША
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Note 2: Data retention only

Note 3: $V_{CC} = 0 V$

Note 4: High or low state (However, it can not exceed I_{OUT} of absolute maximum ratings.)

Note 5: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ Note 6: $V_{CC} = 2.7 \text{ to } 3.0 \text{ V}$

Note 7: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

3

Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Characteristics		Symbol Test Condition		adition		Min	Max	Unit
Characteristi	ıcs	Syllibol	V _{CC} (V)		IVIIII	IVIAX		
					1.65 to 2.3	V _{CC} ×0.8	_	
	H-level	V _{IH}	_		2.3 to 2.7	1.7	_	
Input voltage					2.7 to 3.6	2.0	_	V
iliput voltage					1.65 to 2.3	_	V _{CC} ×0.2	V
	L-level	V _{IL}	_		2.3 to 2.7	_	0.7	
				_	2.7 to 3.6	_	0.8	
				I _{OH} = -100 μA	1.65 to 3.6	V _{CC} -0.2	_	
				$I_{OH} = -4 \text{ mA}$	1.65	1.05	_	
	H-level	V	Mar. Ma	$I_{OH} = -8 \text{ mA}$	2.3	1.7	_	V
	n-ievei	V _{OH}	$V_{IN} = V_{IL}$	$I_{OH} = -12 \text{ mA}$	2.7	2.2	_	
				I _{OH} = -18 mA	3.0	2.4	_	
Out = 1 + 1 = 1 + 1 = 1 = 1				I _{OH} = -24 mA	3.0	2.2	_	
Output voltage			V _{IN} = V _{IH} or V _{IL}	I _{OL} = 100 μA	1.65 to 3.6	_	0.2	
				I _{OL} = 4mA	1.65	_	0.45	
	I laval	V _{OL}		I _{OL} = 8 mA	2.3	_	0.7	
	L-level			I _{OL} = 12 mA	2.7	_	0.4	
				I _{OL} = 16 mA	3.0	_	0.4	
				I _{OL} = 24 mA	3.0	_	0.55	
Input leakage current		I _{IN}	V _{IN} = 0 to 5.5 V		1.65 to 3.6	_	±5.0	μА
Power-off leakage curr	ent	loff	V _{IN} /V _{OUT} = 5.5 V		0	_	10.0	
Quiescent supply curre	0		V _{IN} = V _{CC} or GND		1.65 to 3.6	_	10.0	
Quiescent supply curre		Icc	V _{IN} = 3.6 to 5.5 V		1.65 to 3.6	_	±10.0	μΑ
Increase in I _{CC} per inp	out	Δl _{CC}	$V_{IH} = V_{CC} - 0.6V$		2.7 to 3.6	_	500	



AC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteristics	Symbol	Test Condition V _{CC} (V)		Min	Max	Unit
			1.8±0.15		20.0	
Propagation delay time	t _{pLH} t _{pHL}	Figure 1 Figure 2	2.5±0.2		7.0	
		Figure 1, Figure 2	2.7		6.0	ns
			3.3 ± 0.3	1.5	5.2	
Output to output skew		osLH (Note)	2.7		_	ns
Output to output skew	t _{osHL}	(Note)	3.3 ± 0.3		1.0	113

Note: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{PLHm} - t_{PLHn}|, t_{OSHL} = |t_{PHLm} - t_{PHLn}|)$

Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500$ Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}	V _{IH} = 3.3 V, V _{IL} = 0 V	3.3	0.8	V
Quiet output minimum dynamic V _{OL}	V _{OLV}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}	_	3.3	7	pF
Output capacitance	C _{OUT}	_	0	8	pF
Power dissipation capacitance	C _{PD}	$f_{IN} = 10 \text{ MHz}$ (Not	e) 3.3	25	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC} / 4 \text{ (per gate)}$



AC Test Circuit

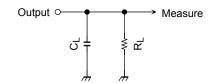


Figure 1

AC Waveform

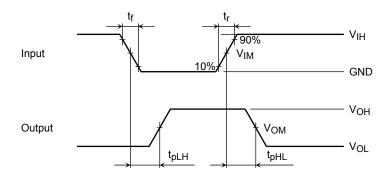


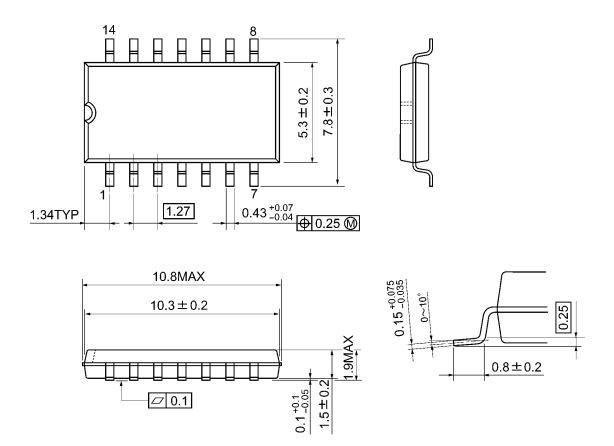
Figure 2 t_{pLH} , t_{pHL}

		Vcc					
	Symbol	3.3±0.3V 2.7V	2.5±0.2V	1.8±0.15V			
	V _{IH}	2.7V	V _{CC}	V _{CC}			
Input	V_{IM}	1.5V	V _{CC} /2	V _{CC} /2			
	t _r , t _f	2.5ns	2.0ns	2.0ns			
Output	V _{OM}	1.5V	V _{OH} /2	V _{OH} /2			
Load	C_{L}	50pF	30pF	30pF			
Loau	R_{L}	500Ω	500Ω	1kΩ			



Package Dimensions

SOP14-P-300-1.27A Unit: mm

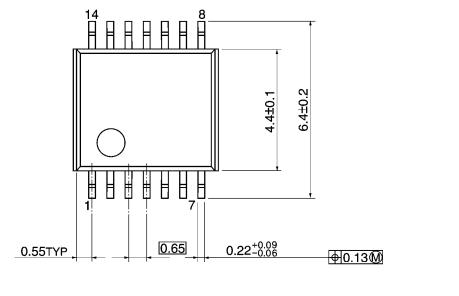


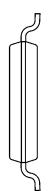
Weight: 0.18 g (typ.)

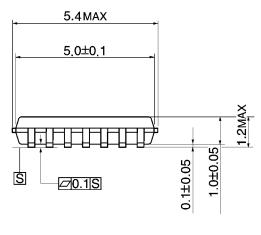
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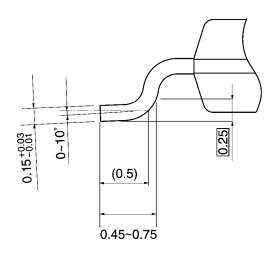
TSSOP14-P-0044-0.65A

Unit: mm





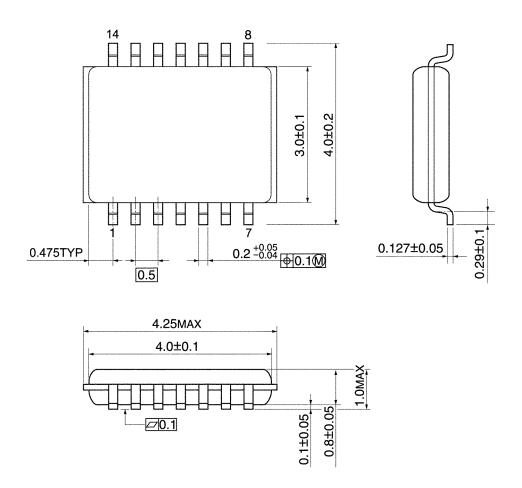




Weight: 0.06 g (typ.)

Package Dimensions

VSSOP14-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)

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