

GD2X60MPS06N

650V 120A SiC Schottky MPS™ Diode



Silicon Carbide Schottky Diode

V_{RRM}	=	650 V
$I_F(T_c = 96^\circ\text{C})$	=	120 A *
Q_c	=	184 nC *

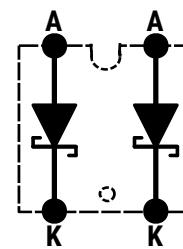
Features

- Gen4 Thin Chip Technology for Low V_F
- Superior Figure of Merit Q_c/I_F
- 100% Avalanche Tested
- Enhanced Surge Current Robustness
- Temperature Independent Fast Switching
- Low Thermal Resistance
- Positive Temperature Coefficient of V_F
- High dV/dt Ruggedness

Package



SOT-227



Advantages

- Optimal Price Performance
- Improved System Efficiency
- Reduced Cooling Requirements
- Increased System Power Density
- Zero Reverse Recovery Current
- High System Reliability
- Easy to Parallel without Thermal Runaway
- Enables Extremely Fast Switching

Applications

- Power Factor Correction (PFC)
- Electric Vehicles and Battery Chargers
- Solar Inverters
- High Frequency Converters
- Switched Mode Power Supply (SMPS)
- Motor Drives
- Anti-Parallel / Free-Wheeling Diode
- Induction Heating & Welding

Absolute Maximum Ratings (At $T_c = 25^\circ\text{C}$ Unless Otherwise Stated)

Parameter	Symbol	Conditions	Values	Unit	Note
Repetitive Peak Reverse Voltage (Per Leg)	V_{RRM}		650	V	
Continuous Forward Current (Per Leg / Per Device)	I_F	$T_c = 75^\circ\text{C}, D = 1$	70 / 140	A	Fig. 4
		$T_c = 100^\circ\text{C}, D = 1$	58 / 116		
		$T_c = 96^\circ\text{C}, D = 1$	60 / 120		
Non-Repetitive Peak Forward Surge Current, Half Sine Wave (Per Leg)	$I_{F,SM}$	$T_c = 25^\circ\text{C}, t_p = 10 \text{ ms}$	420	A	
		$T_c = 150^\circ\text{C}, t_p = 10 \text{ ms}$	336		
Repetitive Peak Forward Surge Current, Half Sine Wave (Per Leg)	$I_{F,RM}$	$T_c = 25^\circ\text{C}, t_p = 10 \text{ ms}$	252	A	
		$T_c = 150^\circ\text{C}, t_p = 10 \text{ ms}$	176		
Non-Repetitive Peak Forward Surge Current (Per Leg)	$I_{F,MAX}$	$T_c = 25^\circ\text{C}, t_p = 10 \mu\text{s}$	2100	A	
i^2t Value (Per Leg)	$\int i^2 dt$	$T_c = 25^\circ\text{C}, t_p = 10 \text{ ms}$	882	A^2s	
Non-Repetitive Avalanche Energy (Per Leg)	E_{AS}	$L = 0.3 \text{ mH}, I_{AS} = 60 \text{ A}$	548	mJ	
Diode Ruggedness (Per Leg)	dV/dt	$V_R = 0 \sim 520 \text{ V}$	200	V/ns	
Power Dissipation (Per Leg / Per Device)	P_{TOT}	$T_c = 25^\circ\text{C}$	204 / 408	W	Fig. 3
Operating and Storage Temperature	T_j, T_{stg}		-55 to 175	$^\circ\text{C}$	

* Per Device

Electrical Characteristics (Per Leg)

Parameter	Symbol	Conditions	Values			Unit	Note
			Min.	Typ.	Max.		
Diode Forward Voltage	V _F	I _F = 60 A, T _j = 25°C	1.5	1.8		V	Fig. 1
		I _F = 60 A, T _j = 175°C	1.8				
Reverse Current	I _R	V _R = 650 V, T _j = 25°C	1	10		μA	Fig. 2
		V _R = 650 V, T _j = 175°C	11				
Total Capacitive Charge	Q _C	V _R = 200 V	63			nC	Fig. 7
		I _F ≤ I _{FMAX} V _R = 400 V	92				
Switching Time	t _S	dI _F /dt = 200 A/μs	V _R = 200 V		< 10	ns	
			V _R = 400 V				
Total Capacitance	C	V _R = 1 V, f = 1MHz	1463			pF	Fig. 6
		V _R = 400 V, f = 1MHz	125				

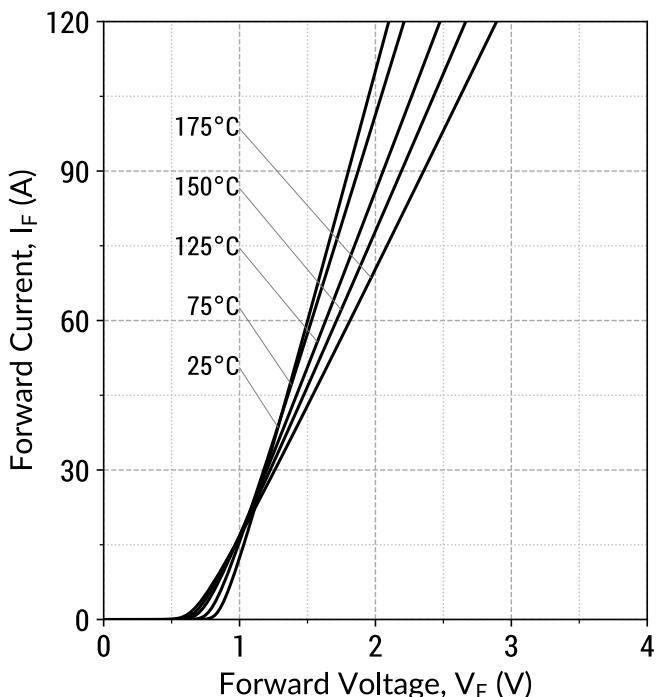
Thermal/Package Characteristics

Parameter	Symbol	Conditions	Values			Unit	Note
			Min.	Typ.	Max.		
Thermal Resistance, Junction - Case (Per Leg)	R _{thJC}		0.74			°C/W	Fig. 9
Weight	W _T		28.0			g	
Mounting Torque	T _M	Screws to Heatsink		1.5		Nm	
Terminal Connection Torque	T _C	M4 Screws		1.3		Nm	
Isolation Voltage(RMS)	V _{iso}	t = 1s (50/60 Hz)	3000			V	
		t = 60s (50/60 Hz)	2500				
Creepage Distance on Surface	d _{ctt}	Terminal to Terminal	10.5			mm	
	d _{ctb}	Terminal to Backside	8.5				
Striking Distance Through Air	d _{stt}	Terminal to Terminal	3.2			mm	
	d _{stb}	Terminal to Backside	6.8				

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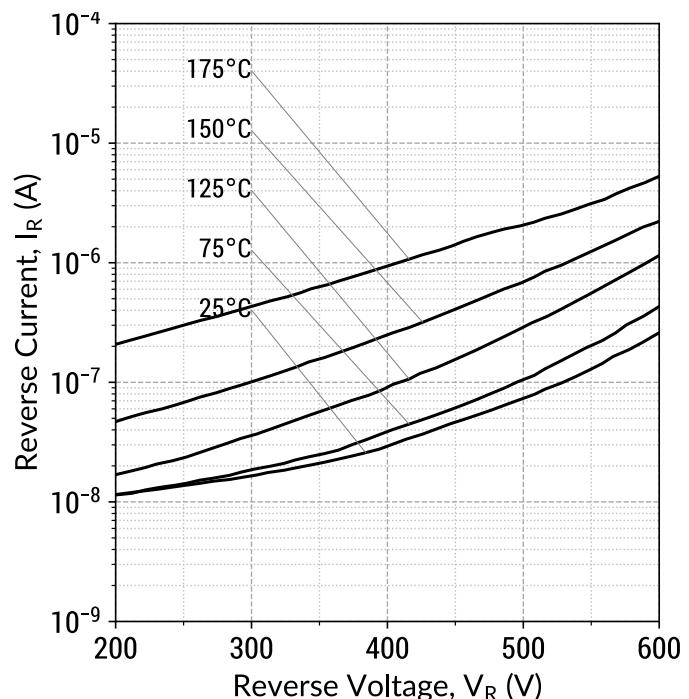
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Figure 1: Typical Forward Characteristics (Per Leg)



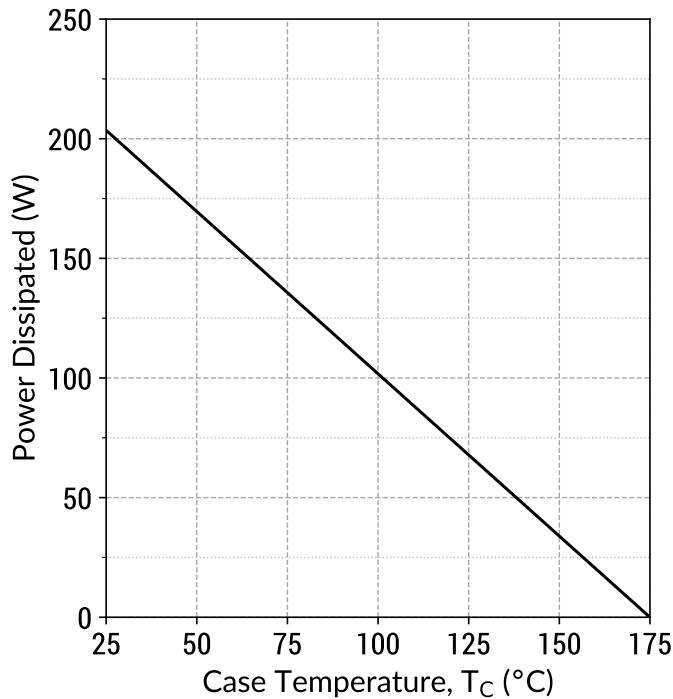
$$I_F = f(V_F, T_j); t_P = 250 \mu\text{s}$$

Figure 2: Typical Reverse Characteristics (Per Leg)



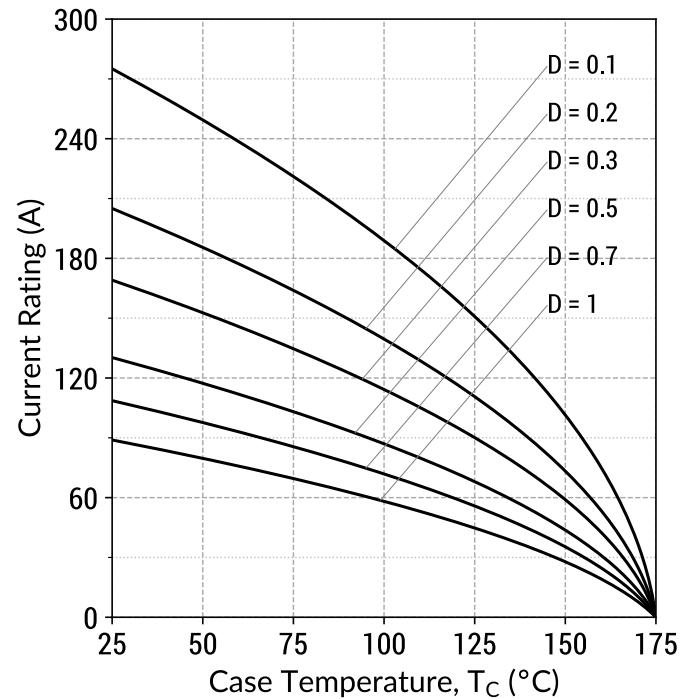
$$I_R = f(V_R, T_j)$$

Figure 3: Power Derating Curves (Per Leg)



$$P_{TOT} = f(T_c); T_j = 175^\circ\text{C}$$

Figure 4: Current Derating Curves (Typical V_F) (Per Leg)

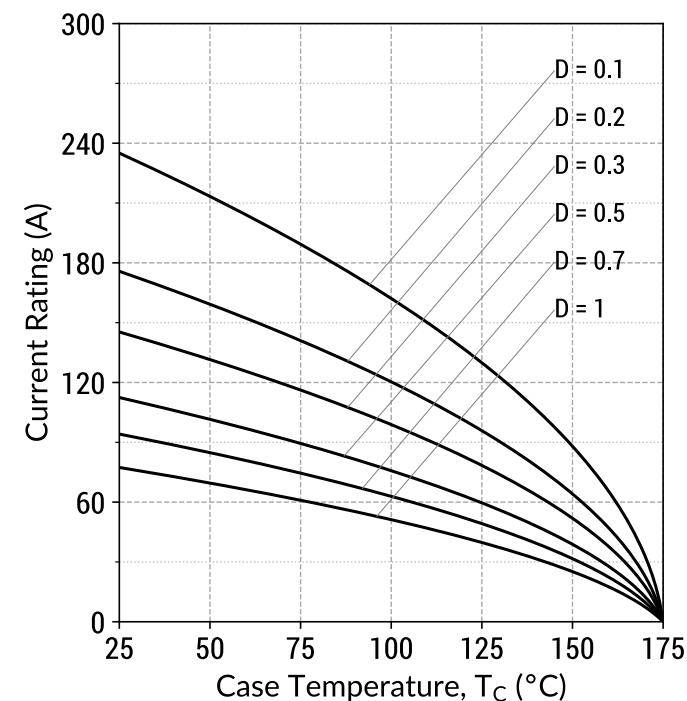


$$I_F = f(T_c); D = t_P/T; T_j \leq 175^\circ\text{C}; f_{SW} > 10\text{kHz}$$

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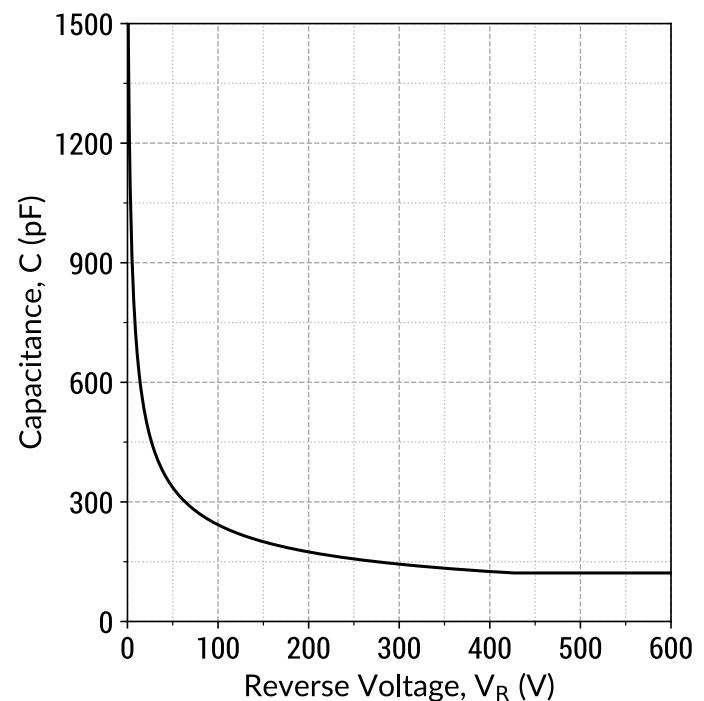
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Figure 5: Current Derating Curves (Maximum V_F) (Per Leg)



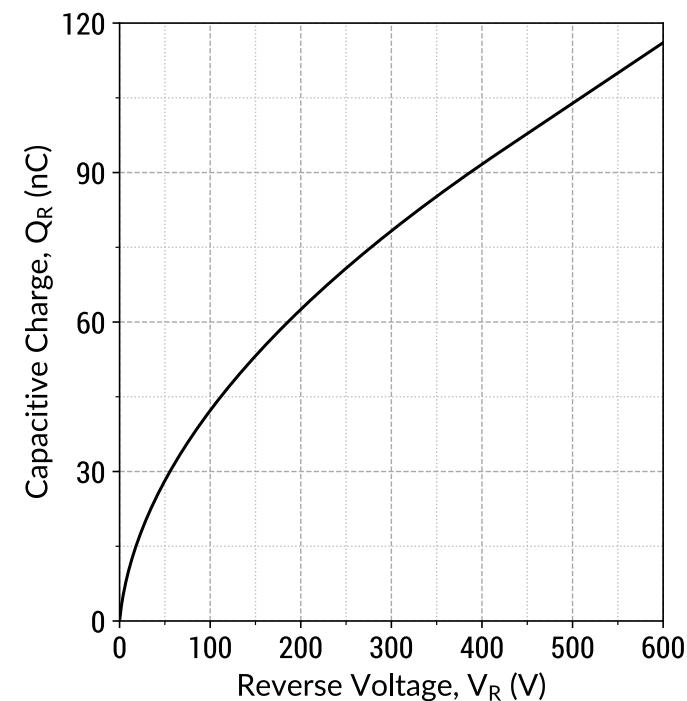
$$I_F = f(T_C); D = t_P/T; T_j \leq 175^\circ\text{C}; f_{sw} > 10\text{kHz}$$

Figure 6: Typical Junction Capacitance vs Reverse Voltage Characteristics (Per Leg)



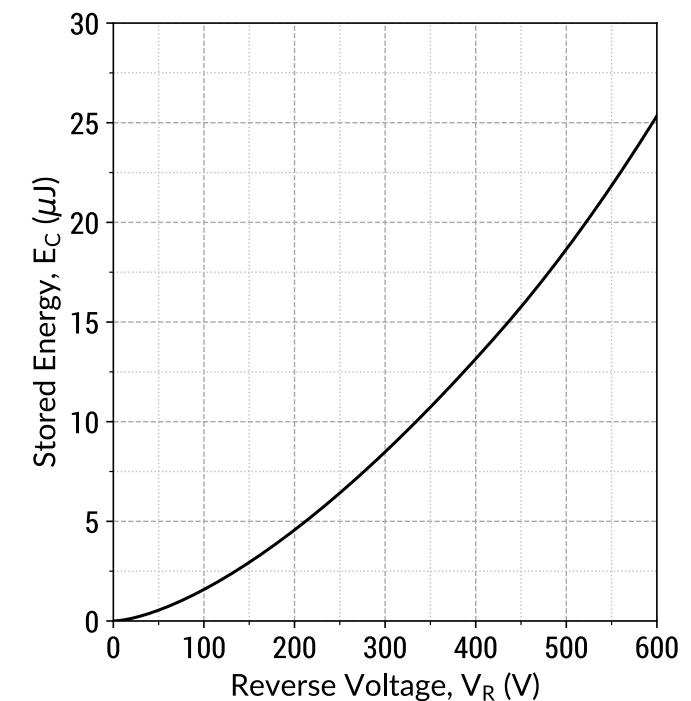
$$C = f(V_R); f = 1\text{MHz}$$

Figure 7: Typical Capacitive Charge vs Reverse Voltage Characteristics (Per Leg)



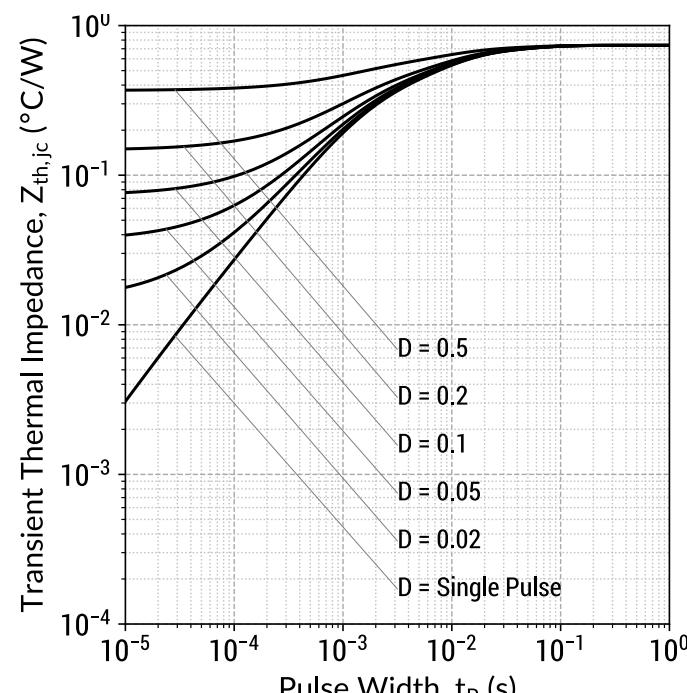
$$Q_C = f(V_R); f = 1\text{MHz}$$

Figure 8: Typical Capacitive Energy vs Reverse Voltage Characteristics (Per Leg)



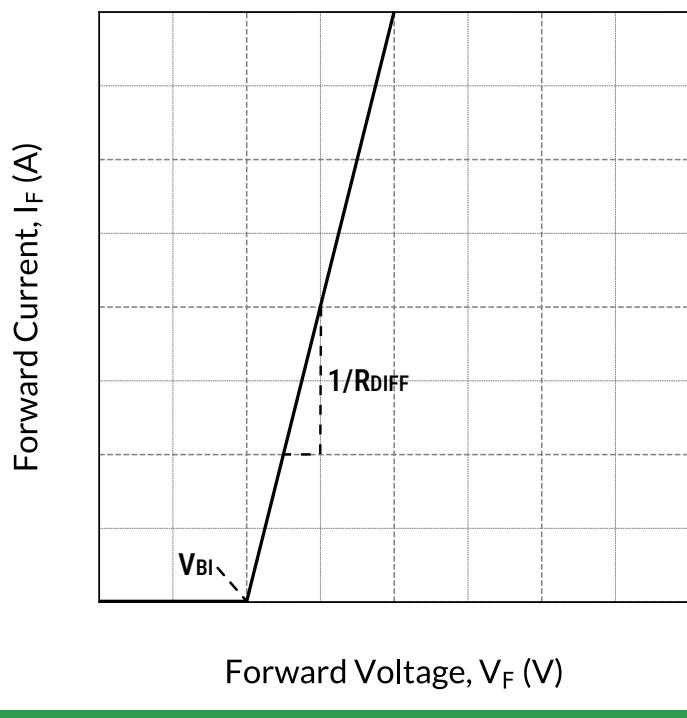
$$E_C = f(V_R); f = 1\text{MHz}$$

Figure 9: Transient Thermal Impedance (Per Leg)



$$Z_{th,jc} = f(t_P, D); D = t_P/T$$

Figure 10: Forward Curve Model (Per Leg)



$$I_F = f(V_F, T_j)$$

Forward Curve Model Equation:

$$I_F = (V_F - V_{BI})/R_{DIFF} \text{ (A)}$$

Built-In Voltage (V_{BI}):

$$V_{BI}(T_j) = m \times T_j + n \text{ (V)}$$

$$m = -0.00114 \text{ (V/°C)}$$

$$n = 0.931 \text{ (V)}$$

Differential Resistance (R_{DIFF}):

$$R_{DIFF}(T_j) = a \times T_j^2 + b \times T_j + c \text{ (Ω)}$$

$$a = 2.55e-07 \text{ (Ω/°C}^2)$$

$$b = 2.76e-06 \text{ (Ω/°C)}$$

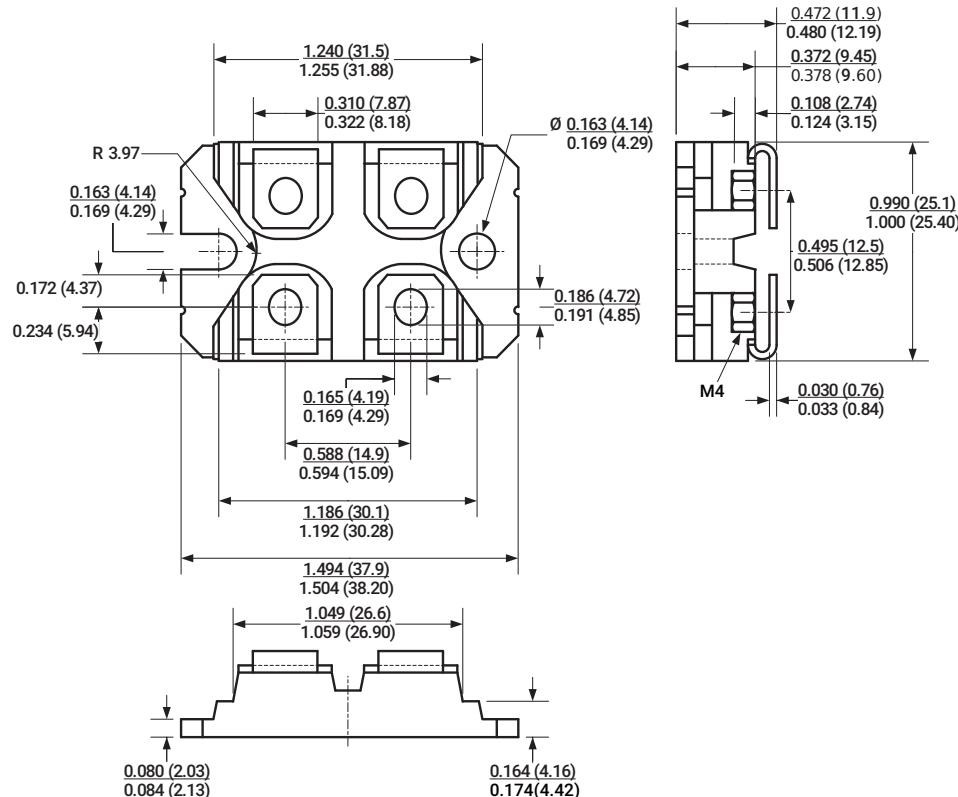
$$c = 0.00976 \text{ (Ω)}$$

Forward Power Loss Equation:

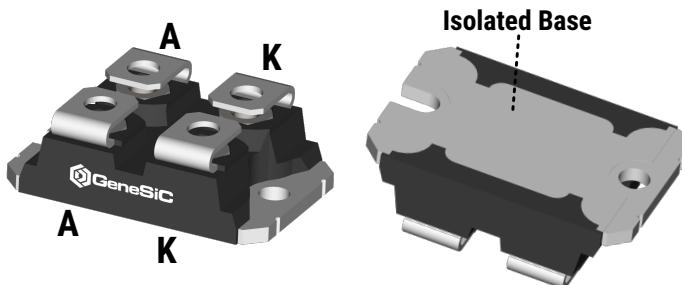
$$P_{LOSS} = V_{BI}(T_j) \times I_{AVG} + R_{DIFF}(T_j) \times I_{RMS}^2$$

Package Dimensions

SOT-227 Package Outline



Package View



NOTE

1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS.

Compliance

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS 2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863. RoHS Declarations for this product can be obtained from your GeneSiC representative.

REACH Compliance

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

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Related Links

- SPICE Models: https://www.genesicsemi.com/sic-schottky-mps/GD2X60MPS06N/GD2X60MPS06N_SPICE.zip
- PLECS Models: https://www.genesicsemi.com/sic-schottky-mps/GD2X60MPS06N/GD2X60MPS06N_PLECS.zip
- CAD Models: https://www.genesicsemi.com/sic-schottky-mps/GD2X60MPS06N/GD2X60MPS06N_3D.zip
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Revision History

- Rev 22/May: Initial Release



www.genesicsemi.com/sic-schottky-mps/



Rev 22/May

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