

# MOSFET - Power, Single N-Channel, TOLL 100 V, 1.8 mΩ, 272 A

# NTBLS1D7N10MC

#### **Features**

- Low R<sub>DS(on)</sub> to Minimize Conduction Losses
- Low Q<sub>G</sub> and Capacitance to Minimize Driver Losses
- Lowers Switching Noise/EMI
- These Devices are Pb-Free and are RoHS Compliant

#### MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			$V_{DSS}$	100	V
Gate-to-Source Voltage	Э		$V_{GS}$	±20	V
Continuous Drain		T <sub>C</sub> = 25°C	I <sub>D</sub>	272	Α
Current R <sub>0JC</sub> (Notes 1, 3)	Steady	T <sub>C</sub> = 100°C		192	
Power Dissipation	State	T <sub>C</sub> = 25°C	$P_{D}$	295	W
R <sub>θJC</sub> (Note 1)		T <sub>C</sub> = 100°C		147	
Continuous Drain	Steady State	T <sub>A</sub> = 25°C	I <sub>D</sub>	29	Α
Current R <sub>0JA</sub> (Notes 1, 2, 3)		T <sub>A</sub> = 100°C		20	
Power Dissipation		T <sub>A</sub> = 25°C	$P_{D}$	3.4	W
R <sub>θJA</sub> (Notes 1, 2)		T <sub>A</sub> = 100°C		1.7	
Pulsed Drain Current	$T_A = 25$	°C, t <sub>p</sub> = 10 μs	I <sub>DM</sub>	2137	Α
Operating Junction and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C
Source Current (Body Diode)			I <sub>S</sub>	226	Α
Single Pulse Drain-to-Source Avalanche Energy (I <sub>L(pk)</sub> = 97 A)			E <sub>AS</sub>	606	mJ
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			TL	260	°C

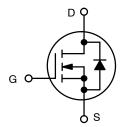
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

### THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Junction-to-Case - Steady State	$R_{\theta JC}$	0.51	°C/W
Junction-to-Ambient - Steady State (Note 2)	$R_{\theta JA}$	43	

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- Surface-mounted on FR4 board using a 650 mm<sup>2</sup>, 2 oz. Cu pad.
- 3. Maximum current for pulses as long as 1 second is higher but is dependent on pulse duration and duty cycle.

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX		
100 V	1.8 m $\Omega$ @ 10 V	272 A		



**N-CHANNEL MOSFET** 



H-PSOF8L CASE 100CU

# **MARKING DIAGRAM**



A = Assembly Location
Y = Year
WW = Work Week
ZZ = Lot Traceability
1D7N10MC = Specific Device Code

#### **ORDERING INFORMATION**

See detailed ordering, marking and shipping information in the package dimensions section on page 5 of this data sheet.

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# **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS	•						
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		100			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /				60		mV/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V,	T <sub>J</sub> = 25 °C			10	
		$V_{DS} = 100 \text{ V}$ $T_{J} = 125^{\circ}$				100	μΑ
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub>	= 20 V			100	nA
ON CHARACTERISTICS (Note 4)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{GS} = V_{DS}, I_D =$	: 698 μA	2.0		4.0	V
Threshold Temperature Coefficient	V <sub>GS(TH)</sub> /T <sub>J</sub>				-10.5		mV/°C
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 80 A		1.5	1.8	mΩ
Forward Transconductance	9FS	$V_{DS}$ = 10 V, $I_{D}$	= 80 A		220		S
CHARGES AND CAPACITANCES							
Input Capacitance	C <sub>ISS</sub>				9200		
Output Capacitance	Coss	V <sub>GS</sub> = 0 V, f = 1 MHz	z, V <sub>DS</sub> = 50 V		4600		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			79		1	
Total Gate Charge	Q <sub>G(TOT)</sub>				115		_
Threshold Gate Charge	Q <sub>G(TH)</sub>				24		
Gate-to-Source Charge	Q <sub>GS</sub>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 50 V; I <sub>D</sub> = 80 A			47		nC
Gate-to-Drain Charge	$Q_{GD}$				16		
Plateau Voltage	$V_{GP}$				5		V
SWITCHING CHARACTERISTICS (Note 5)					-		
Turn-On Delay Time	t <sub>d(ON)</sub>				48		
Rise Time	t <sub>r</sub>	VGS = 10 V. VDS	s = 50 V.		38		1
Turn-Off Delay Time	t <sub>d(OFF)</sub>	$V_{GS} = 10 \text{ V}, V_{DS} = 50 \text{ V},$ $I_{D} = 80 \text{ A}, R_{G} = 6 \Omega$			76		ns -
Fall Time	t <sub>f</sub>				31		
DRAIN-SOURCE DIODE CHARACTERIST	rics						
Forward Diode Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 80 A	T <sub>J</sub> = 25°C		0.82	1.3	.,
			T <sub>J</sub> = 125°C		0.70		_ V
Reverse Recovery Time	t <sub>RR</sub>	$V_{GS} = 0 \text{ V, dIS/dt} = 100 \text{ A/}\mu\text{s,}$ $I_{S} = 62 \text{ A}$			98		ns
Reverse Recovery Charge	Q <sub>RR</sub>				160		nC
Charge Time	ta				47		ns
Discharge Time	t <sub>b</sub>				52		ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Pulse Test: pulse width  $\leq 300~\mu s$ , duty cycle  $\leq 2\%$ .

5. Switching characteristics are independent of operating junction temperatures.

#### **TYPICAL CHARACTERISTICS**

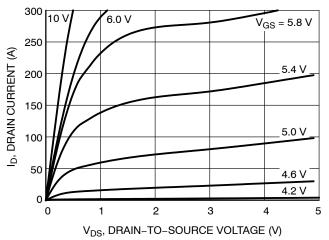


Figure 1. On-Region Characteristics

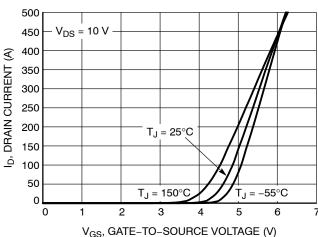


Figure 2. Transfer Characteristics

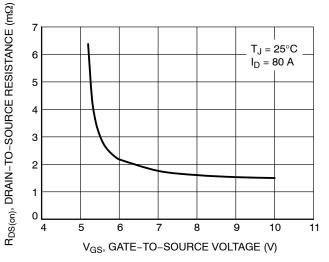


Figure 3. On-Resistance vs. Gate-to-Source Voltage

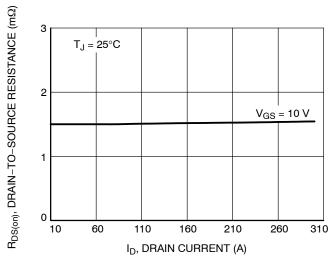


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

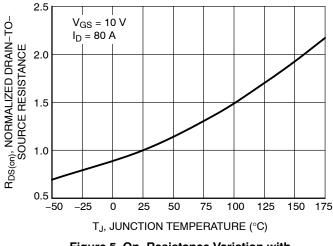


Figure 5. On–Resistance Variation with Temperature

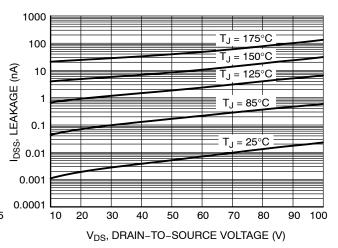


Figure 6. Drain-to-Source Leakage Current vs. Voltage

#### **TYPICAL CHARACTERISTICS**

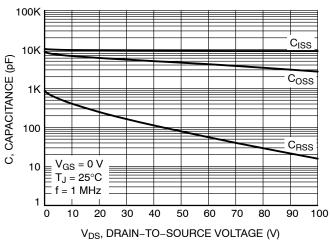


Figure 7. Capacitance Variation

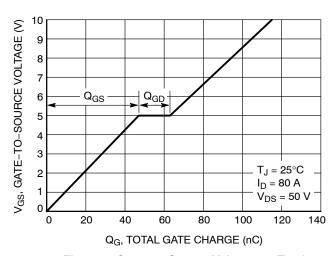


Figure 8. Gate-to-Source Voltage vs. Total Charge

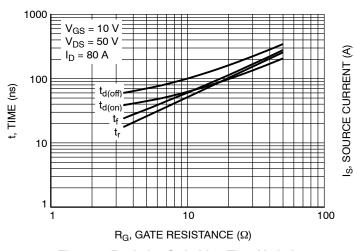


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

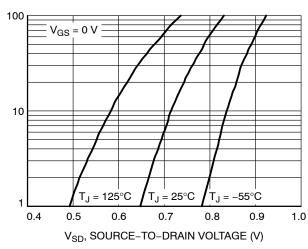


Figure 10. Diode Forward Voltage vs. Current

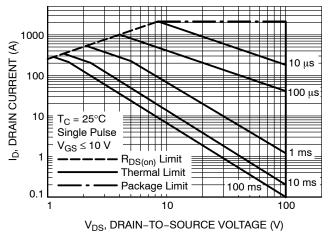


Figure 11. Maximum Rated Forward Biased Safe Operating Area

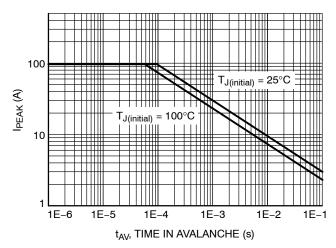


Figure 12. Maximum Drain Current vs. Time in Avalanche

# **TYPICAL CHARACTERISTICS**

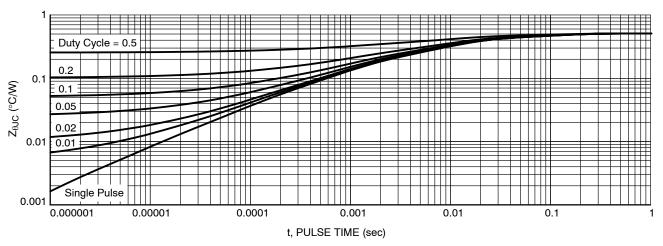


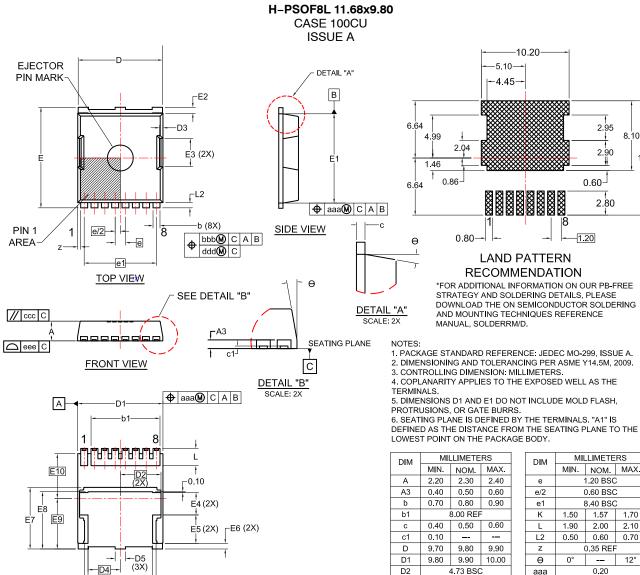
Figure 13. Transient Thermal Impedance

# **DEVICE ORDERING INFORMATION**

Device	Marking	Package	Shipping <sup>†</sup>		
NTBLS1D7N10MCTXG	1D7N10MC	H-PSOF8L (Pb-Free)	2000 / Tape & Reel		

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### PACKAGE DIMENSIONS



	OIV IIIL	17.010	.OL	DOD1.			
MILLIMETERS			DIM	MILLIMETERS			
MIN.	NOM.	MAX.			MIN.	NOM.	MAX.
2.20	2.30	2.40		е		1.20 BSC	;
0.40	0.50	0.60		e/2	0.60 BSC		
0.70	0.80	0.90		e1	8.40 BSC		
	3.00 REF			K	1.50	1.57	1.70
0.40	0.50	0.60		L	1.90	2.00	2.10
0.10				L2	0.50	0.60	0.70
9.70	9.80	9.90		Z	0.35 REF		
9.80	9.90	10.00		θ	0°		12°
4	4.73 BSC	;		aaa	0.20		
(	0.40 REF			bbb	0.25		
;	3.75 BSC	;		ccc	0.20		
_	1.20			ddd	0.20		
7.40	7.50	7.60		eee	0.10		
	(8.30)			E5	3.30 <b>-</b>		_
11.58	11.68	11.78		E6		0.65	_
10.28	10.38	10.48		E7	7.15 REF		
0.60	0.70	0.80		E8	6.55	6.65	6.75
3.30 REF			E9	5.89 BSC			
_	2.60			E10	5.19 BSC		

2.95

2.90

0.60-2.80

1.20

13 28

D2

D3

D4

D5

D6

D7

Е E1

E2

E3

E4

-D7

**BOTTOM VIEW** 

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