



4D LCD PTY LTD
UNIT 7, 103 SARGENTS RD
MINCHINBURY, NSW 2770
AUSTRALIA
www.4dlcd.com

4DLCD-24320240-[RTP]

2.4" TFT Liquid Crystal Display

DATASHEET

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Revision History

REVISION	DATE	COMMENT	REMARKS
1.0	18/01/2019	Initial Version	Initial Draft Version

Table of Contents

1. General Specification	3
2. TFT LCD Display Drawing (Non Touch Version)	4
3. TFT LCD Display Drawing (Resistive Touch Version)	5
4. Absolute Maximum Ratings	6
5. Electrical Characteristics	6
6. Electro-Optical Characteristics	6
7. Backlight Characteristics	7
8. Interface Descriptions	9
8.1 LCD Interface	9
9. Initialisation Code	11
10. LCD Timing Details	13
10.1 Timing Chart	13
10.2 Reset Timing	14
10.3 Power On Sequence	14
10.3.1 Case 1 - RES line is held High or Unstable by Host at Power ON	14
10.3.2 Case 2 - RES line is held Low by Host at Power ON	15
10.4 Power-off Sequence - Uncontrolled Power Off	15
11. Reliability Test	16
12. Precautions for Using LCD Modules	17
12.1 Handling Precautions	17
12.2 Storage Precautions	18
13. Legal Information	19

1. General Specification

4DLCD-24320240 is a colour active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a colour TFT-LCD panel, driver IC, FPC and a back light unit and with/without a Resistive Touch Panel (RTP). The module display area contains 240 x 320 pixels. This product accords with RoHS environmental criterion.

ITEM		CONTENTS	UNIT
LCD Type		TFT / Transmissive / Normally white	
Size		2.4	Inch
Viewing Direction		6:00 (without image inversion)	O'Clock
Gray Scale Inversion Direction		12:00	O'Clock
LCD (W × H × T)		42.72 × 60.26 × 2.25	mm
Active Area (W × H)		36.72 × 48.96	mm
Dot Pitch (W × H)		0.153 × 0.153	mm
Number of Dots (Pixels)		240 (RGB) × 320	
Driver IC		ILI9341V	
Backlight Type		4 LEDs	
Surface Luminance	4DLCD-24320240	228 (typical)	cd/m ²
	4DLCD-24320240-RTP	182 (typical)	
Interface Type		MCU-16bit /SPI	
Color Depth		262K	
Pixel Arrangement		RGB Vertical Stripe	
Surface Treatment		AG	
Input Voltage		2.8 (typical)	V
With/Without TP (Touch Panel)		4DLCD-24320240 - Without Touch Panel 4DLCD-24320240-RTP – With Resistive Touch	
Weight	4DLCD-24320240	11.0	g
	4DLCD-24320240-RTP	16.2	

Note 1: RoHS compliant

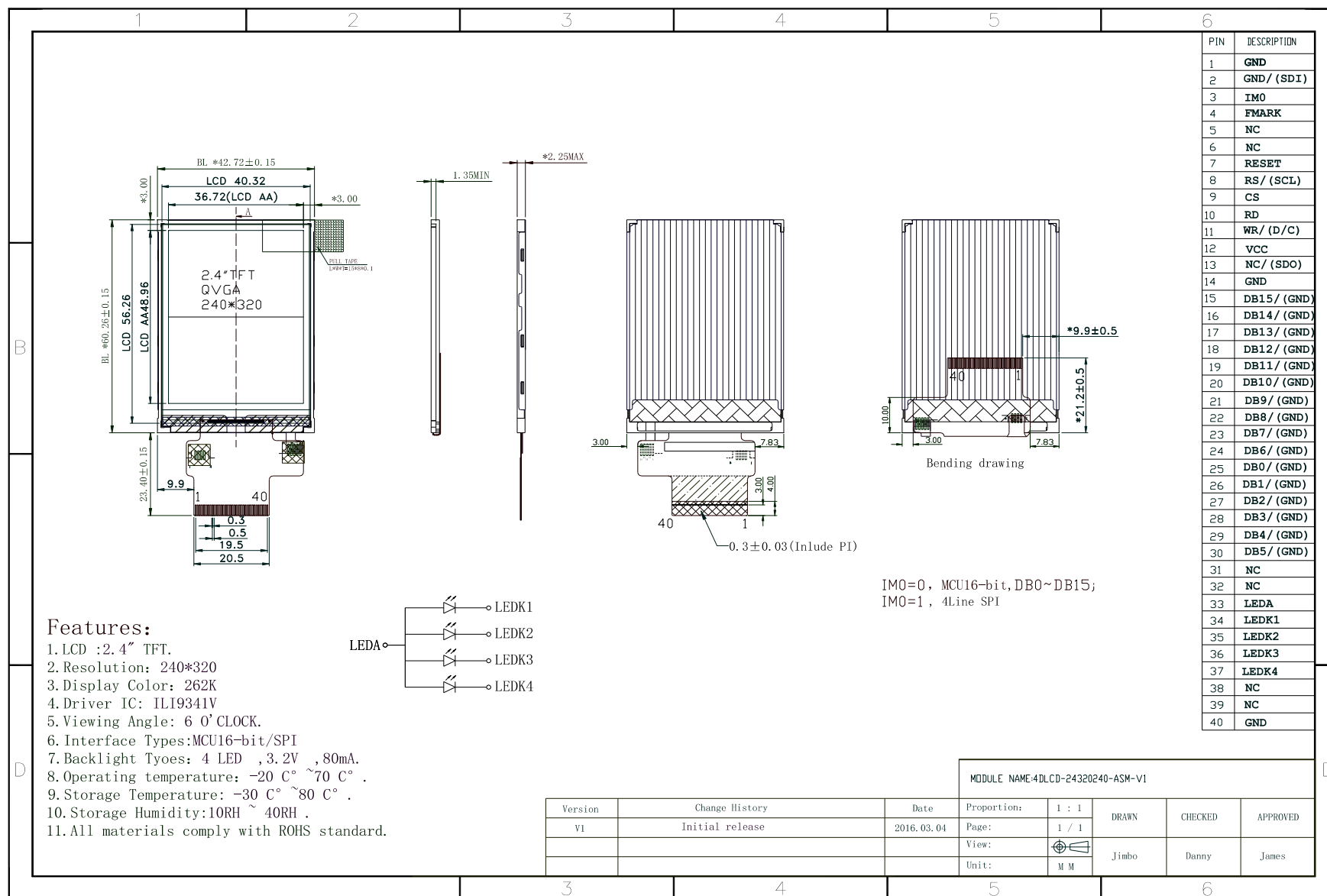
Note 2: LCD weight tolerance: ± 5%.

Part Number Details:

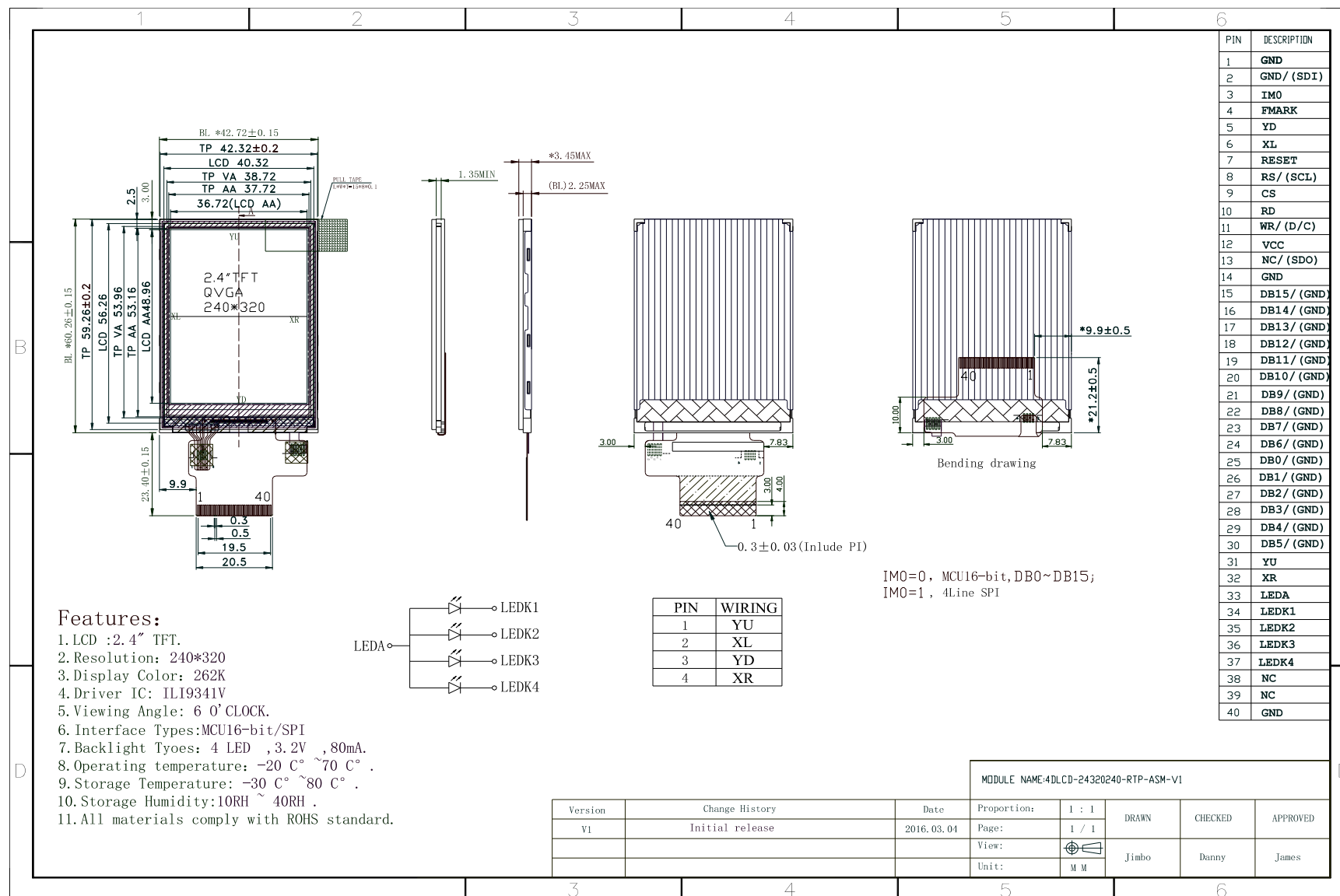
4DLCD	4DLCD LCD Display
24320240	2.4 inch, 240 x 320 Resolution
RTP	Resistive Touch



2. TFT LCD Display Drawing (Non Touch Version)



3. TFT LCD Display Drawing (Resistive Touch Version)



4. Absolute Maximum Ratings

PARAMETER	SYMBOL	MIN	MAX	UNIT
Supply Voltage for LCD Logic	VDD/VCC	-0.3	4.6	V
Supply Voltage for TP Logic	VDD/VCC-VSS	-	-	V
Input Voltage for Logic	VIN	VSS-0.5	VDD	V
LED forward current (each LED)	IF	-	25	mA
Operating Temperature	T _{OP}	-20	70	°C
Storage Temperature	T _{ST}	-30	80	°C
Humidity	RH	-	90% (Max 60°C)	RH

5. Electrical Characteristics

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Power Voltage	VDD/DCC	2.6	2.8	3.3	V
Input Current	IVDD	-	-	-	mA
Input Voltage 'H' Level	V _{IH}	0.7 VDD	-	VDD	V
Input Voltage 'L' Level	V _{IL}	0	-	0.3 VDD	V

6. Electro-Optical Characteristics

ITEM	SYM	CONDITION	MIN	TYP	MAX	UNIT	REMARK
Response Time	Tr+Tf	$\theta=0$	-	30	-	ms	Figure 1 (4)
Contrast Ratio	Cr	$^{\circ}$	-	250	-	-	Figure 2 (1)
Luminance Uniformity	δ WHITE	$\phi=0$	75	80	-	%	Figure 2 (3)
Surface Luminance	Lv	4DLCD-24320240	205	228	-	cd/m ²	Figure 2 (2)
		4DLCD-24320240-RTP	164	182	-		
Viewing Angle Range	θ	$\phi = 90^{\circ}$	-	35	-	deg	Figure 3 (6)
		$\phi = 270^{\circ}$	-	55	-	deg	
		$\phi = 0^{\circ}$	-	55	-	deg	
		$\phi = 180^{\circ}$	-	55	-	deg	
CIE (x,y) Chromaticity	Red	x	$\theta=0^{\circ}$ $\phi=0^{\circ}$ Ta=25	0.574	0.624	0.674	Figure 2 (5)
		y		0.318	0.368	0.418	
	Green	x		0.3	0.35	0.4	
		y		0.5	0.55	0.6	
	Blue	x		0.093	0.143	0.193	
		y		0.069	0.119	0.169	
	White	x		0.26	0.31	0.36	
		y		0.283	0.333	0.383	

7. Backlight Characteristics

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Voltage for LED backlight	V_i	-	3.2	3.4	V
Current for LED backlight	I_i	-	80	100	mA
LED Life Time	-	30000	-	-	Hrs

Note: The LED life time is defined as the module brightness decrease to 50% original brightness at $T_a=25^{\circ}\text{C}$.

Note 1: Contrast Ratio(CR) is defined mathematically as below, for more information see Figure 1.

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$$

Note 2: Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information, see Figure 2.

L_v = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Note 3: The uniformity in surface luminance δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information, see Figure 2.

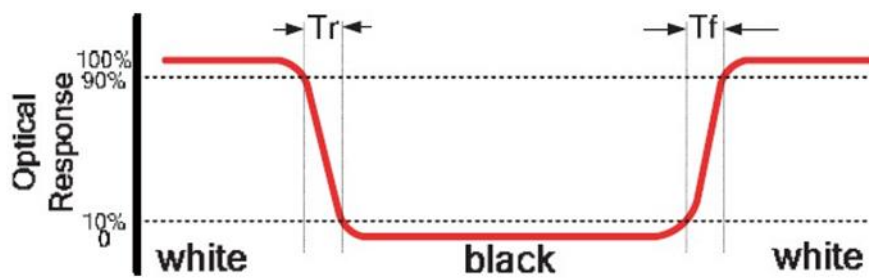
$$\delta \text{ WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$$

Note 4: Response time is the time required for the display to transition from white to black (Rise Time, T_r) and from black to white (Decay Time, T_f). For additional information see FIG 1. The test equipment is Autronic-Melchers ConoScope series.

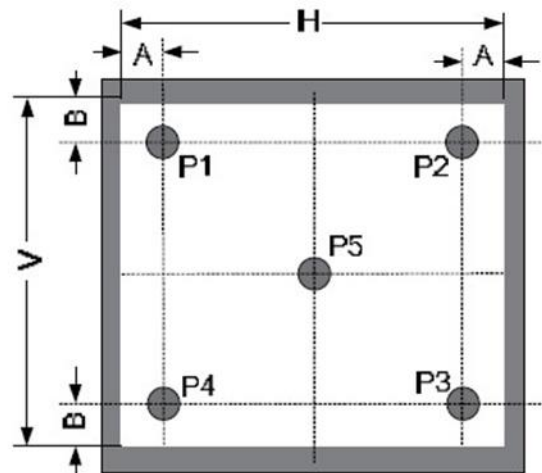
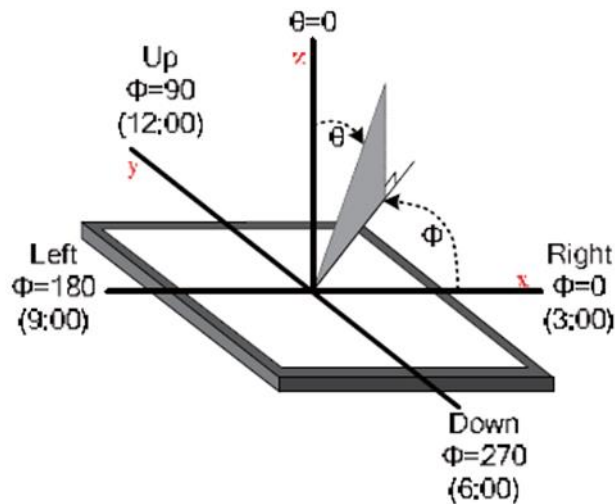
Note 5: CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.

Note 6: Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information, see Figure3.

Note 7: For viewing angle and response time testing, the testing data is based on Autronic-Melchers ConoScope series. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, CIE the test data is based on TOPCONS BM-5 photo detector.

Figure 1. The definition of response time**Figure 2.** Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

A : 5 mm
 B : 5 mm
 H, V : Active Area
 Light spot size $\varnothing=5\text{mm}$, 500mm distance from the LCD surface to detector lens
 measurement instrument is TOPCON's luminance meter BM-5

**Figure 3.** The definition of viewing angle

8. Interface Descriptions

8.1 LCD Interface

PIN NO.	SYMBOL	DESCRIPTION	REMARK
1	GND	Ground	
2	GND/(SDI)	Ground for MCU mode/ Serial Data Input signal for SPI mode	
3	IM0	MCU-16bit /SPI mode selection pin	Note 1
4	FMARK	Tearing effect output signal	
5	YD/NC	The touch panel Y Down pin	Note 3
6	XL/NC	The touch panel X left pin	Note 3
7	RESET	Reset input signal	
8	RS/SCL	Instruction Register/Data Register selection pin for MCU mode / SPI Serial Clock for SPI mode	
9	CS	Chip select input pin	
10	RD/(VCC)	Read signal for MCU mode / Tie to VCC for SPI mode	
11	WR/(D/C)	Write signal for MCU mode / Data or Command selection for SPI mode	
12	VCC	Power supply	
13	NC/(SDO)	No Connect for MCU mode / Serial Data Output for SPI mode	
14	GND	Ground	
15	DB15/(GND)	Databus DB15 for MCU mode / Tie to GND for SPI mode	Note 2
16	DB14/(GND)	Databus DB14 for MCU mode / Tie to GND for SPI mode	Note 2
17	DB13/(GND)	Databus DB13 for MCU mode / Tie to GND for SPI mode	Note 2
18	DB12/(GND)	Databus DB12 for MCU mode / Tie to GND for SPI mode	Note 2
19	DB11/(GND)	Databus DB11 for MCU mode / Tie to GND for SPI mode	Note 2
20	DB10/(GND)	Databus DB10 for MCU mode / Tie to GND for SPI mode	Note 2
21	DB9/(GND)	Databus DB9 for MCU mode / Tie to GND for SPI mode	Note 2
22	DB8/(GND)	Databus DB8 for MCU mode / Tie to GND for SPI mode	Note 2
23	DB7/(GND)	Databus DB7 for MCU mode / Tie to GND for SPI mode	Note 2
24	DB6/(GND)	Databus DB6 for MCU mode / Tie to GND for SPI mode	Note 2
25	DB0/(GND)	Databus DB0 for MCU mode / Tie to GND for SPI mode	Note 2
26	DB1/(GND)	Databus DB1 for MCU mode / Tie to GND for SPI mode	Note 2
27	DB2/(GND)	Databus DB2 for MCU mode / Tie to GND for SPI mode	Note 2
28	DB3/(GND)	Databus DB3 for MCU mode / Tie to GND for SPI mode	Note 2
29	DB4/(GND)	Databus DB4 for MCU mode / Tie to GND for SPI mode	Note 2
30	DB5/(GND)	Databus DB5 for MCU mode / Tie to GND for SPI mode	Note 2
31	YU/NC	The touch panel Y up pin	Note 3
32	XR/NC	The touch panel X right pin	Note 3
33	LEDA	Anode of LED Backlight	
34	LEDK1	Cathode1 of LED Backlight	
35	LEDK2	Cathode2 of LED Backlight	
36	LEDK3	Cathode3 of LED Backlight	
37	LEDK4	Cathode4 of LED Backlight	
38	NC	No Connect	
39	NC	No Connect	
40	GND	Ground	

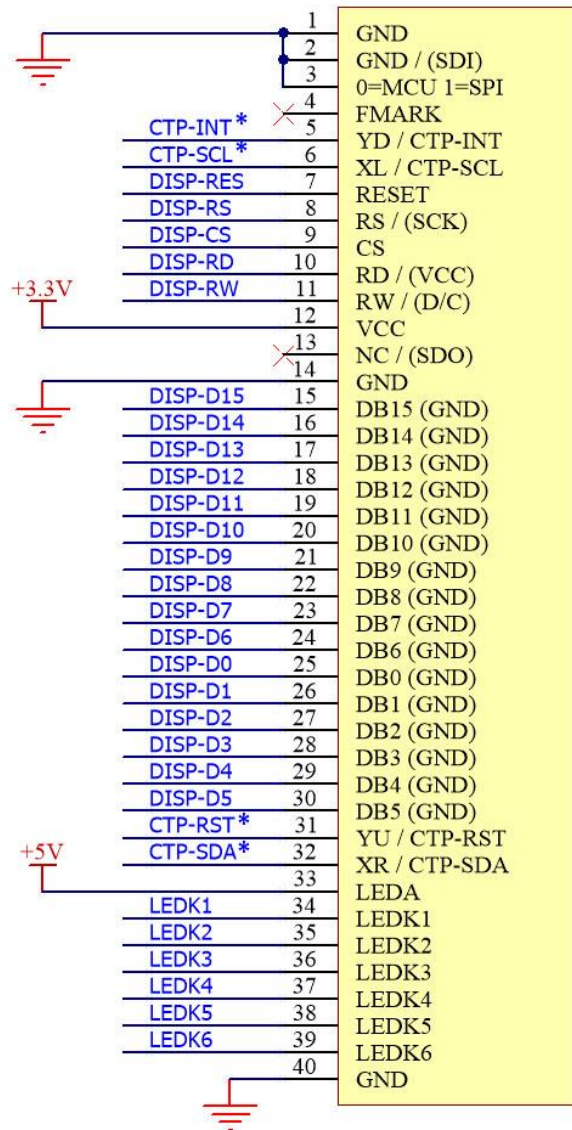
Recommended LCD connector: 40-way, 0.5 mm pitch FFC connector

Note 1: IM0(MCU/SPI) (pin 3)

IM0(8/16)	Interface	Remark
0	MCU 16bit	Databus:DB0~DB15
1	SPI	

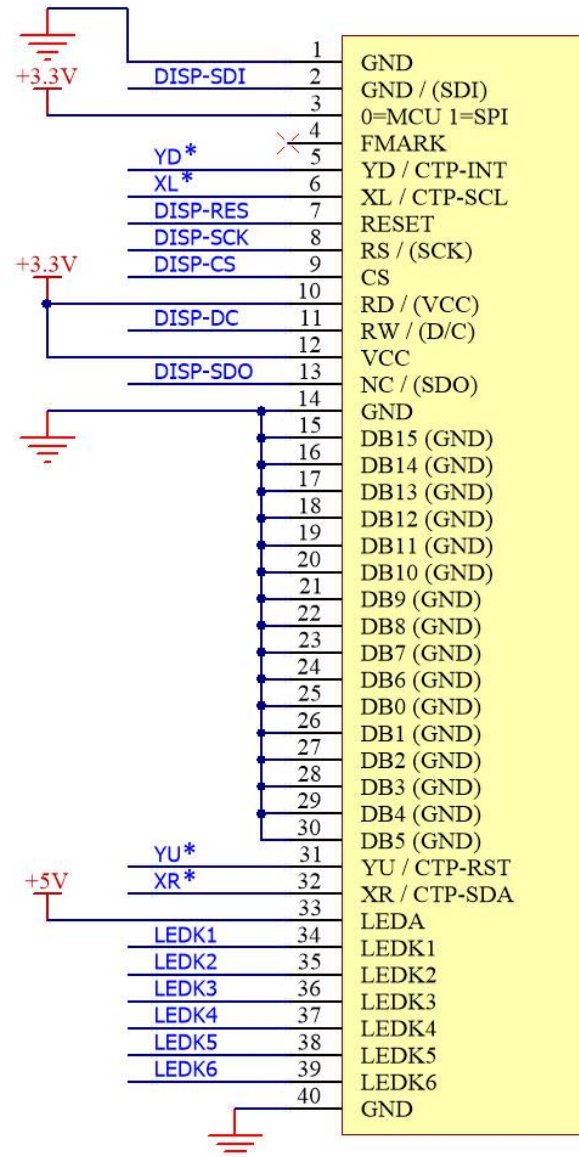
Note 2: Pins 15 to 30 are grounded on SPI mode.

Note 3: Pins 5,6,31 and 32 are only applicable to touch screen displays (4DLCD-xxxxxxx-RTP).



* This has no connection (NC) for Non-touch displays

MCU Mode



* This has no connection (NC) for Non-touch displays

SPI Mode

9. Initialisation Code

```
//*****Hardware reset*****// s
```

```
LCD_RESET=1;
```

```
Delayms(15);
```

```
LCD_RESET=0;
```

```
Delayms(120);
```

```
LCD_RESET=1;
```

```
Delayms(120);
```

```
//*****Start Initial Sequence*****//
```

```
write_reg(0xCF);
```

```
write_dat(0x00);
```

```
write_dat(0xC1);
```

```
write_dat(0x30);
```

```
write_reg(0xED);
```

```
write_dat(0x64);
```

```
write_dat(0x03);
```

```
write_dat(0X12)
```

```
write_dat(0X81) ;
```

```
write_reg(0xE8);
```

```
write_dat(0x85);
```

```
write_dat(0x10);
```

```
write_dat(0x7A);
```

```
write_reg(0xCB);
```

```
write_dat(0x39);
```

```
write_dat(0x2C);
```

```
write_dat(0x00);
```

```
write_dat(0x34);
```

```
write_dat(0x02);
```

```
write_reg(0xF7);
```

```
write_dat(0x20);
```

```
write_reg(0xEA);
```

```
write_dat(0x00);
```

```
write_dat(0x00);
```

```
write_reg(0xC0); //Power control
```

```
write_dat(0x21); //VRH[5:0]
```

```
write_reg(0xC1); //Power control
```

```
write_dat(0x13); //SAP[2:0]; BT[3:0]
```

```
write_reg(0xC5); //VCM control
```

```
write_dat(0x32);
```

```
write_dat(0x3C);
```

```
write_reg(0xC7); //VCM control2
```

```
write_dat(0X9C);
```

```
write_reg(0x36); // Memory Access Control
```

```
write_dat(0x08);
```

```
write_reg(0x3A);
```

```
write_dat(0x55);
```

```
write_reg(0xB1);
```

```
write_dat(0x00);
```

```
write_dat(0x16); write_reg(0xB1);
```

```
write_dat(0x00);
```

```
write_dat(0x16);
```

```
write_reg(0xB6); // Display Function Control  
write_dat(0x0A);  
write_dat(0xA2);
```

```
write_reg(0xF6);  
write_dat(0x01);  
write_dat(0x30);
```

```
write_reg(0xF2); // 3Gamma Function Disable  
write_dat(0x00);
```

```
write_reg(0x26); //Gamma curve selected  
write_dat(0x01);
```

```
write_reg(0xE0); //Set Gamma  
write_dat(0x0F);  
write_dat(0x1E);  
write_dat(0x1B);  
write_dat(0x0B);  
write_dat(0x0E);  
write_dat(0x08);  
write_dat(0x47);  
write_dat(0xB7);  
write_dat(0x37);  
write_dat(0x0B);  
write_dat(0x14);  
write_dat(0x05);  
write_dat(0x0C);  
write_dat(0x07);  
write_dat(0x00);
```

```
write_reg(0xE1); //Set Gamma  
write_dat(0x00);  
write_dat(0x21);  
write_dat(0x24);  
write_dat(0x04);  
write_dat(0x11);  
write_dat(0x07);  
write_dat(0x38);  
write_dat(0x48);  
write_dat(0x48);  
write_dat(0x04);  
write_dat(0x0B);  
write_dat(0x0A);  
write_dat(0x33);  
write_dat(0x38);  
write_dat(0x0F);
```

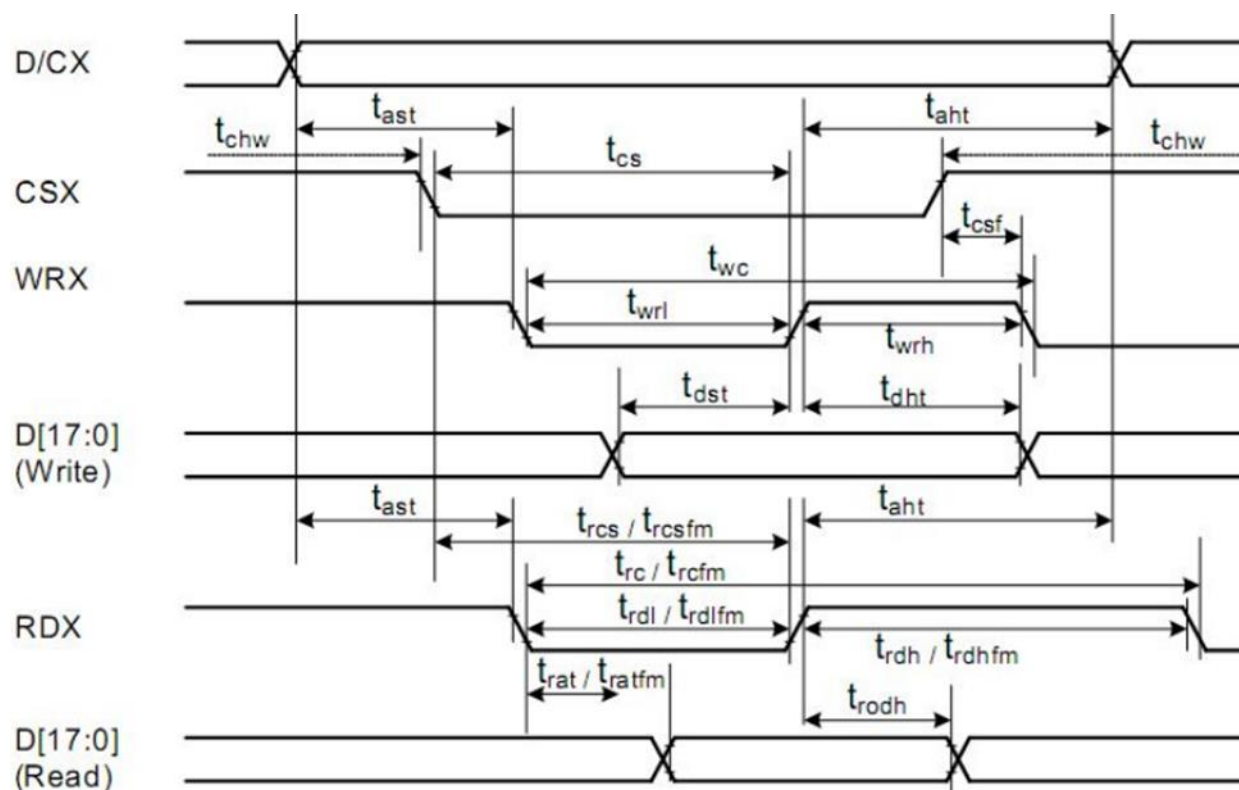
```
write_reg(0x11); //Exit Sleep  
Delayms(120);  
write_reg(0x29); //Display on
```

10. LCD Timing Details

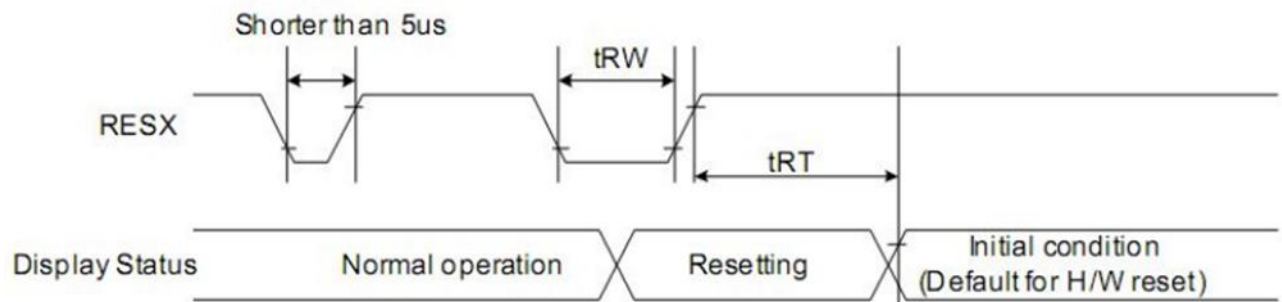
10.1 Timing Chart

SIGNAL	SYMBOL	PARAMETER	MIN	MAX	UNIT	DESCRIPTION
DCX	tast	Address setup time	0	-	ns	-
	taht	Address hold time (Write/Read)	0	-	ns	-
CSX	tchw	CSX "H" pulse width	0	-	ns	-
	tcs	Chip Select setup time	15	-	ns	-
	trcs	Chip Select setup time (Read ID)	45	-	ns	-
	trcsfm	Chip Select setup time (Read FM)	355	-	ns	-
	tcsf	Chip Select Wait time (Write/Read)	10	-	ns	-
WRX	twc	Write cycle	66	-	ns	-
	twrh	Write Control Pulse H duration	15	-	ns	-
	twrl	Write Control Pulse L duration	15	-	ns	-
RDX(FM)	trcfm	Read cycle (FM)	450	-	ns	-
	trdhfm	Read Control Pulse H duration (FM)	90	-	ns	-
	trdlfm	Read Control Pulse L duration (FM)	355	-	ns	-
RDX(ID)	trc	Read cycle (ID)	160	-	ns	-
	trdh	Read Control Pulse H duration	90	-	ns	-
	trdl	Read Control Pulse L duration	45	-	ns	-
D[17:0]	tdst	Write data setup time	10	-	ns	-
	tdht	Write data hold time	10	-	ns	-
	trat	Read access time	-	40	ns	-
	Tratfm	Read access time	-	340	ns	-
	trod	Read output disable time	20	80	ns	-

Timing parameter (VDD=3.3V, GND=0V, Ta=25°C)



10.2 Reset Timing



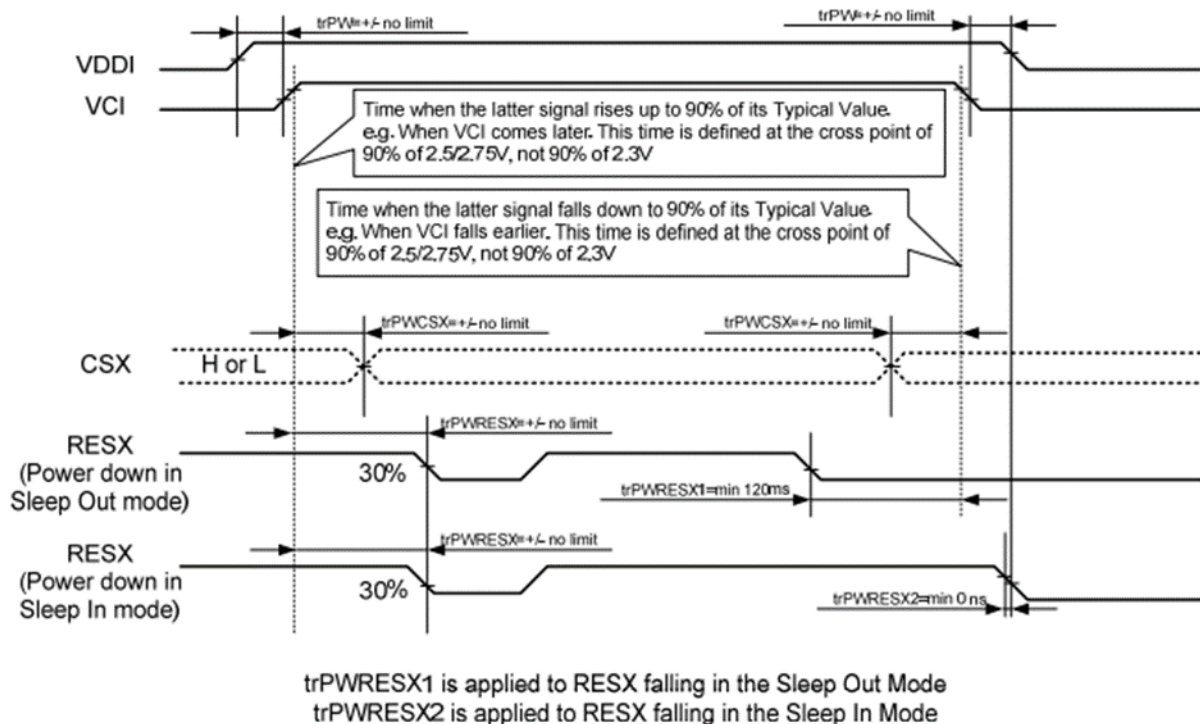
SIGNAL	SYMBOL	PARAMETER	MIN	MAX	UNIT
RESET	t_{RW}	Reset low pulse width	10	-	us
	t_{RT}	Reset complete time	-	5 (note1)	ms
			-	120 (note2)	ms

Note 1: When reset applied during SLPIN mode

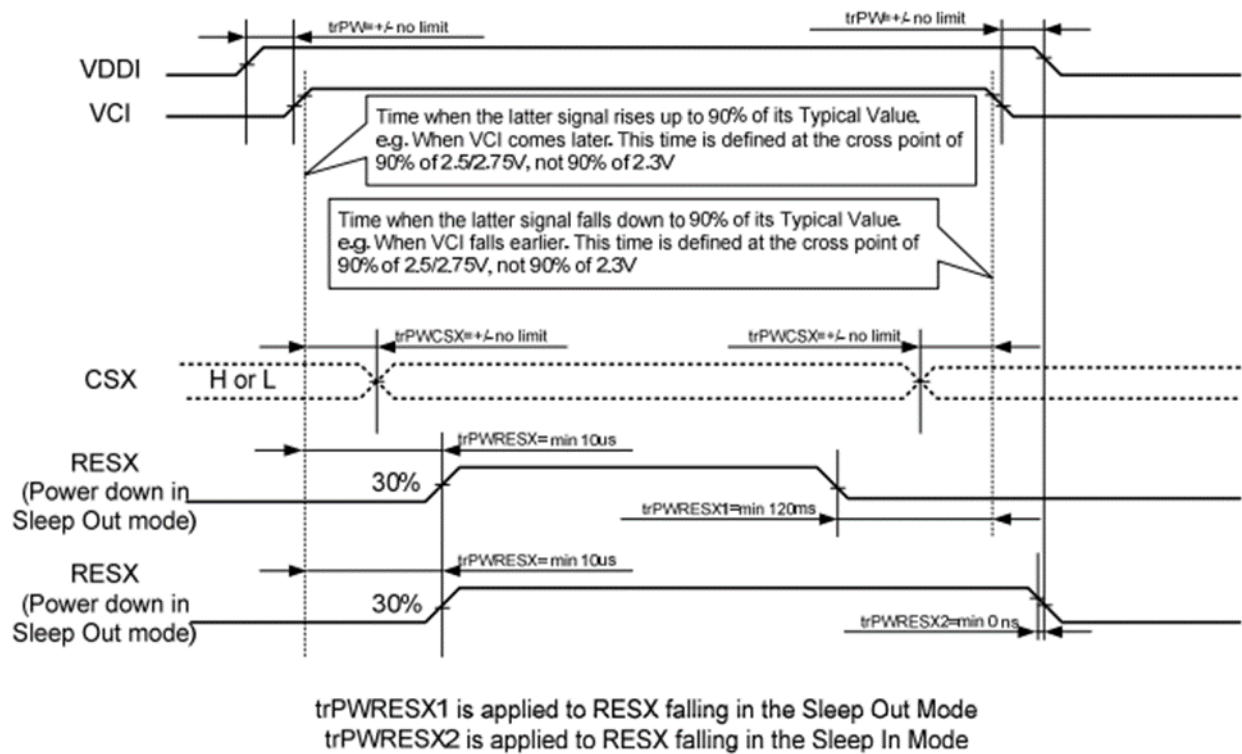
Note 2: When reset applied during SLPOUT mode.

10.3 Power On Sequence

10.3.1 Case 1 - RES line is held High or Unstable by Host at Power ON



10.3.2 Case 2 - RES line is held Low by Host at Power ON



10.4 Power-off Sequence - Uncontrolled Power Off

Uncontrolled power off is a situation where power is removed unexpectedly, e.g. a battery powering a device is disconnected without using the controlled power off sequence. There will not be any damage to the display module, nor will the display module cause any damage to the host. During an uncontrolled power off event, ILI9341V will force the display to blank its content and there will not be any further abnormal visible effects on the display after 1 second of the power being removed. The display will remain blank until the Power On Sequence occurs.

11. Reliability Test

No.	SYMBOL	TEST CONDITION	REMARK
1	High Temperature Storage	80°C±2°C 96H Restore 2H at 25°C Power off	After test cosmetic and electrical defects should not happen.
2	Low Temperature Storage	-30°C±2°C 96H Restore 2H at 25°C Power off	
3	High Temperature Operation	70°C±2°C 96H Power on	
4	Low Temperature Operation	-20°C±2°C 96H Power on	
5	High Temperature & Humidity Operation	60°C±2°C 90%RH 96H Power on	
6	Temperature Cycle	-20°C←→25°C←→70°C 30min 5min 30min After 10 cycles, restore 2H at 25°C Power off	
7	Vibration Test	10Hz~150Hz, 100m/s ² , 120min	
8	Shock Test	Half-sinewave, 300m/s ² , 11ms	

12. Precautions for Using LCD Modules

12.1 Handling Precautions

- The display panel is made of glass and polarizer. The glass is fragile. It tends to be chipped during handling especially on the edges. Please avoid dropping or jarring. Please be careful not subject it to a mechanical shock by dropping it on impact.
- If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any of it in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- Do not apply excessive force to the display surface or to the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined by the polarizer).
- The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizer with anything harder than an HB pencil lead (e.g., glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on it. Condensation on the surface and contact with terminals due to cold temperature will damage, stain or contaminate the polarizer. After products are tested at low temperature they must be warmed up in a container before coming into contact with room temperature air.
- If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten the cloth with one of the following solvents
 - Isopropyl alcohol
 - Ethyl alcohol

Do not scrub hard as it might damage the display surface.

- Solvents other than those mentioned above may damage the polarizer. Especially the following.
 - Water
 - Ketone
 - Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contact with oil and fat.

- Take necessary precautions to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or current flow in a high-humidity environment.
- Install the LCD Module by using the mounting holes. When mounting the LCD module, make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- Do not attempt to disassemble or process the LCD module.
- NC terminal should be open. Do not connect anything on it.
- If the logic circuit power is off, do not apply input signals.
- Control Electro-Static Discharge. Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, ensure that an optimum work environment is maintained.

- Before removing the LCM from its packing case or incorporating it into a set, be sure that the module and your body has the same electric potential. Be sure to ground your body when handling the LCD modules.
- To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions. To reduce the generation of static electricity, please ensure that the air in the work environment is not too dry. A relative humidity of 50%-60% is recommended. As much as possible, make the electric potential of your work clothes and that of the work bench the ground potential.
- The LCD module is coated with a film to protect the display surface. Be careful when peeling off this protective film since static electricity may be generated.
- Since the LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
 - Do not alter, modify or change the shape of the tab on the metal frame.
 - Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
 - Do not damage or modify the pattern writing on the printed circuit board.
 - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
 - Do not drop, bend or twist the LCM.

12.2 Storage Precautions

When storing the LCD modules, the following precautions are necessary.

- Store them in a sealed polyethylene bag. If properly sealed, there is no need for the desiccant.
- Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
- The polarizer surface should not come in contact with any other objects. (We advise you to store them in an anti-static electricity container in which they were shipped. Some Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subjected to low temperature.
- If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., please avoid holding the following sections when handling the modules'
 - Exposed area of the printed circuit board
 - Terminal electrode sections

13. Legal Information

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