

μPC811

High Stability,

Low Offset Voltage J-FET Input Operational Amplifier

R03DS0148EJ0100

Rev.1.00

2019.06.13

DESCRIPTION

μPC811 is a unique J-FET input operational amplifier that uses a high-speed PNP transistor ($f_T = 300$ MHz) for the output stage to achieve fast response and high stability.

The resistor-trimming method proven in other Renesas High-Precision Op-Amp and High-Precision reference voltage is incorporate in this Op-Amp input stage, thus producing an excellent low offset voltage characteristics that has surpasses conventional general purpose op-amp in spite of being JFET input.

Along with this series of lineup, the dual type operational amplifier, μPC812 and 4092 with the same circuit configuration are also available.

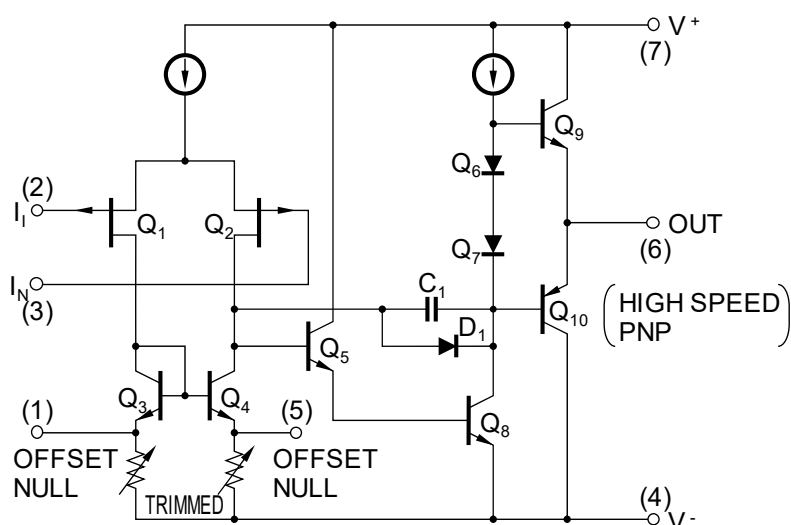
FEATURES

- Input Offset Voltage ± 1 mV (TYP.) (± 2.5 mV MAX.)
- V_{IO} Temperature Drift ± 7 μ V/ $^{\circ}$ C (TYP.)
- Input Bias Current 50 pA (TYP.)
- Slew Rate 15 V/ μ s (TYP.)
- Unity Gain Frequency 4 MHz (TYP.)
- Input Equivalent Noise Voltage Density 19 nV/ $\sqrt{\text{Hz}}$ (TYP.) ($f = 1$ kHz)
- Stable operation against capacitive load (Capacitive Load at 10000 pF, $A_v = +1$)
- Built-In Phase Compensation Circuit
- Built-In Output Short Circuit Protection
- Standard Single Op-Amp terminal connection (pin compatible)

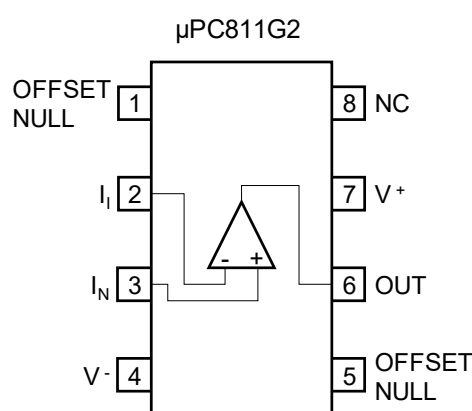
ORDERING INFORMATION

Ordering Name	Package
μPC811G2-A	8-pin plastic SOP (225 mil)

EQUIVALENT CIRCUIT



PIN CONFIGURATION (Top View)



Note NC : No Connection

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C)

PARAMETER	SYMBOL	μPC811G2	UNIT
Supply Voltage ^{Note1}	V ⁺ - V ⁻	-0.3 ~ +36	V
Differential Input Voltage	V _{ID}	±30	V
Input Voltage ^{Note2}	V _I	V ⁻ -0.3 ~ V ⁺ +0.3	V
Output Applied Voltage ^{Note3}	V _O	V ⁻ -0.3 ~ V ⁺ +0.3	V
Total Power Dissipation ^{Note4}	P _T	440	mW
Output Short Circuit Duration ^{Note5}		Indefinite	s
Operating Ambient Temperature	T _A	-40 ~ +85	°C
Storage Temperature	T _{stg}	-55 ~ +125	°C

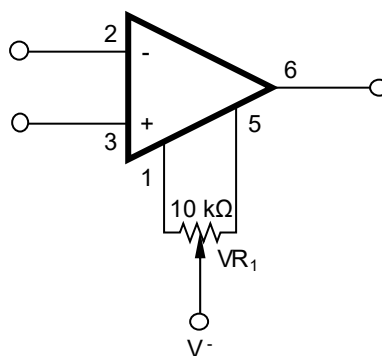
- 【Note】
- Note that reverse connections of the power supply may damage the ICs.
 - The input terminal must be apply within the input voltage range to avoid deteriorating or damaging the device characteristic. Do not exceed the ratings including during transition state such as ON/OFF, etc. The Op-Amp input voltage must operates within the electrical characteristics range of input common-mode voltage.
 - The output terminal must be apply within the output voltage range to avoid deteriorating or damaging the device characteristic. Do not exceed the ratings including during transition state such as ON/OFF, etc. The Op-Amp output voltage must operates within the electrical characteristics range of maximum output voltage.
 - This is the value at T_A ≤ +25 °C. De-rate value at -4.4 mW/°C when T_A > 25 °C
 - Please use the total loss and the de-rating value from Note 4.

RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
Power Supply Voltage	V [±]	±5		±16	V
Load Current	I _O			±10	mA
Load Capacitance (When A _v = +1)	C _L			10000 ^{Note6}	pF

- 【Note】
- This is the value when the feedback resistor (R_f) = 0 Ω. The higher the R_f value, the more likely it is to oscillate due to the influence of the input capacitance. So connect a capacitor of about 100 pF in parallel with R_f

OFFSET ADJUSTMENT METHOD



Note OFFSET NULL pin should be open or connected to V⁻ via a resistor as shown above. Any connection other than V⁻ may cause malfunction, characteristics degradation or damage

ELECTRICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, $V^{\pm} = \pm 15\text{ V}$)

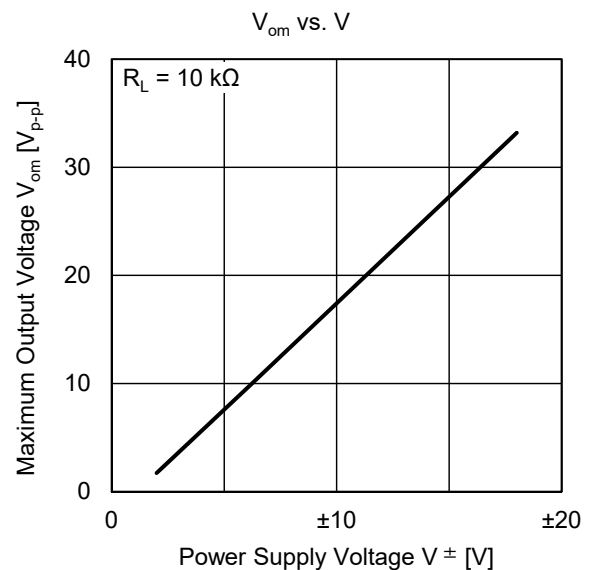
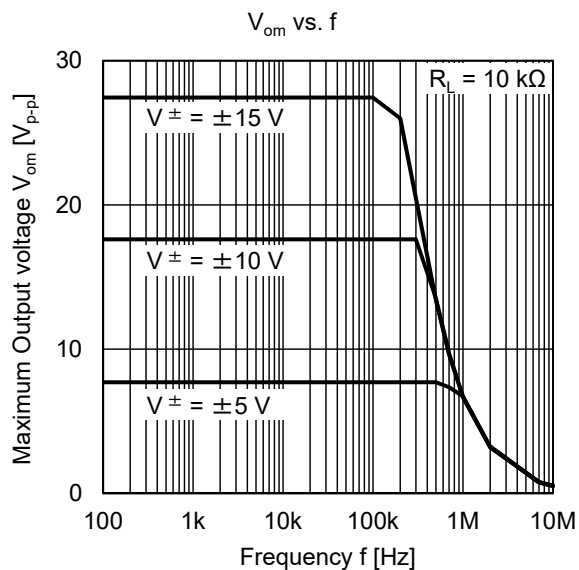
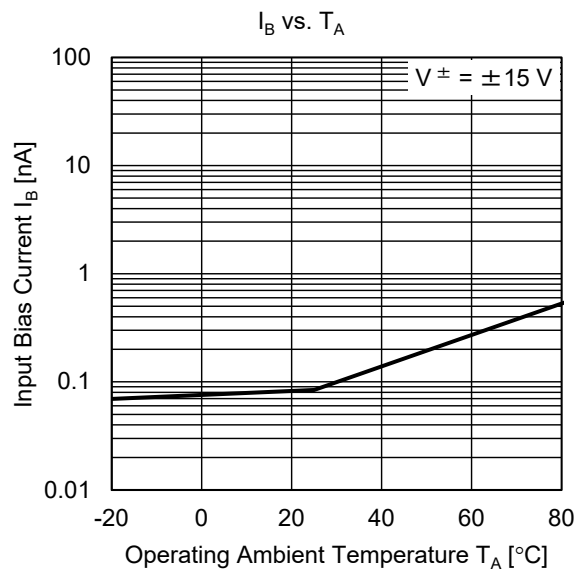
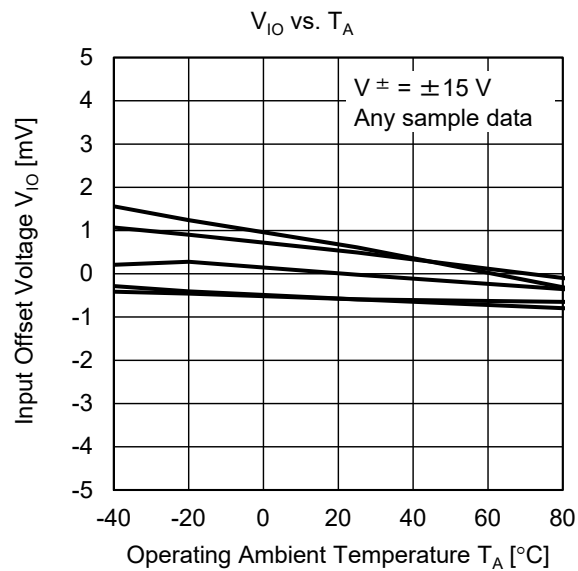
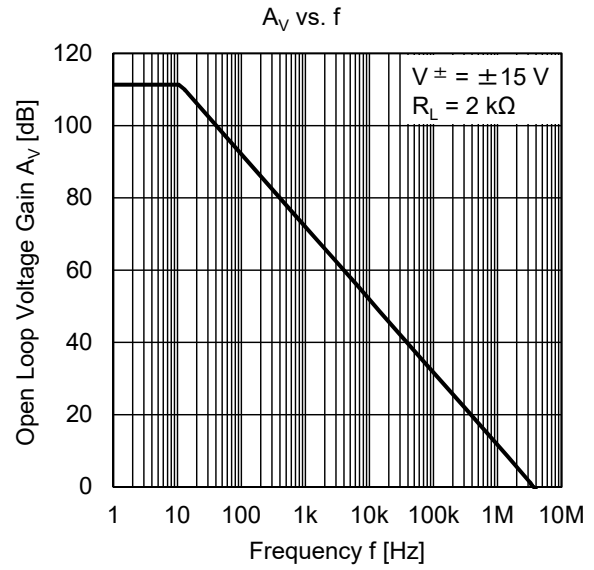
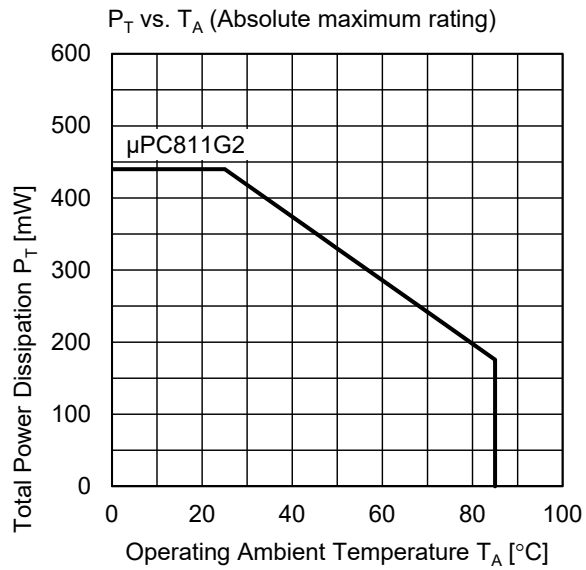
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION
Input Offset Voltage	V_{IO}		± 1	± 2.5	mV	$R_s \leq 50\text{ }\Omega$
Input Offset Current ^{Note 7}	I_{IO}		± 25	± 100	pA	
Input Bias Current ^{Note 7}	I_B		50	200	pA	
Large Signal Voltage Gain	A_V	25	200		V/mV	$R_L \geq 2\text{ k}\Omega$, $V_O = \pm 10\text{ V}$
Circuit Current	I_{CC}		2.5	3.4	mA	$I_O = 0\text{ A}$
Common Mode Rejection Ratio	CMR	70	100		dB	
Supply Voltage Rejection Ratio	SVR	70	100		dB	
Output Voltage Swing	V_{om}	± 12	+14.0 -13.3		V	$R_L \geq 10\text{ k}\Omega$
Output Voltage Swing	V_{om}	± 10	+13.5 -12.8		V	$R_L \geq 2\text{ k}\Omega$
Common Mode Input Voltage Range	V_{ICM}	± 11	+14 -12		V	
Slew Rate	SR		15		V/ μ s	$A_V = 1$
Unity Gain Frequency	f_{unity}		4		MHz	
Input Equivalent Noise Voltage Density	e_n		19		nV/ $\sqrt{\text{Hz}}$	$R_s = 100\text{ }\Omega$, $f = 1\text{ kHz}$
Input Offset Voltage	V_{IO}			± 5	mV	$R_s \leq 50\text{ }\Omega$, $T_A = -20 \sim +70\text{ }^{\circ}\text{C}$
Average V_{IO} Temperature Drift	$\Delta V_{IO}/\Delta T$		± 7		$\mu\text{V}/^{\circ}\text{C}$	$T_A = -20 \sim +70\text{ }^{\circ}\text{C}$
Input Offset Current ^{Note 7}	I_{IO}			± 2	nA	$T_A = -20 \sim +70\text{ }^{\circ}\text{C}$
Input Bias Current ^{Note 7}	I_B			7	nA	$T_A = -20 \sim +70\text{ }^{\circ}\text{C}$

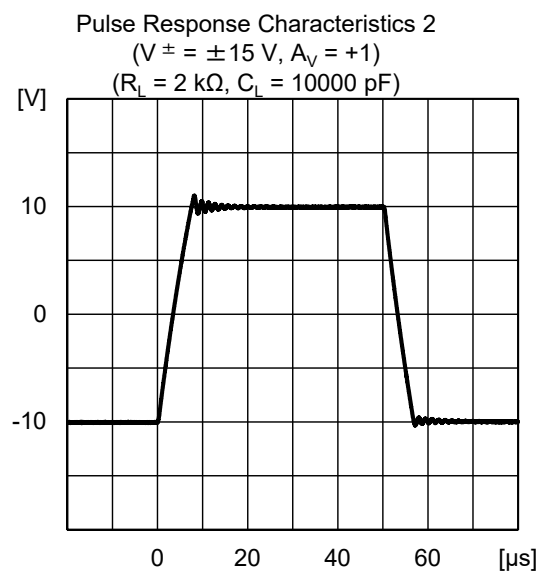
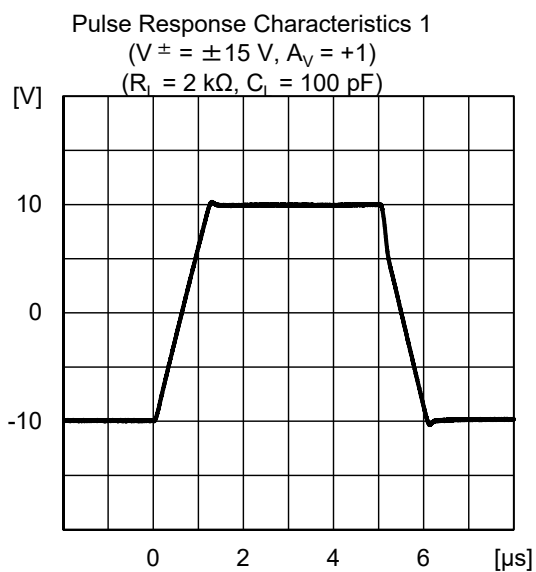
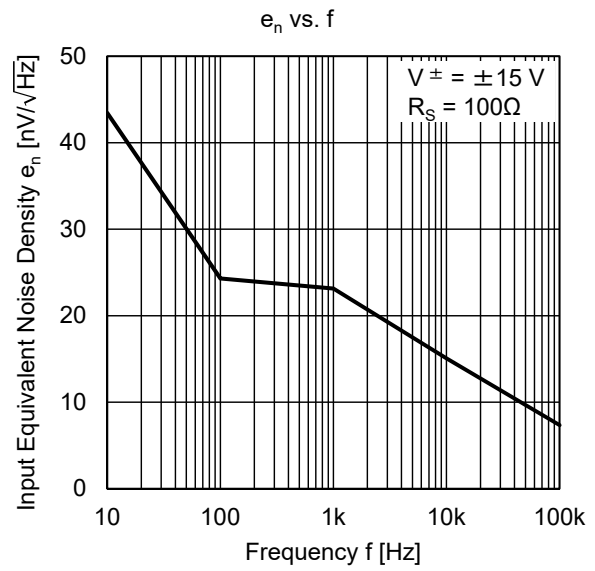
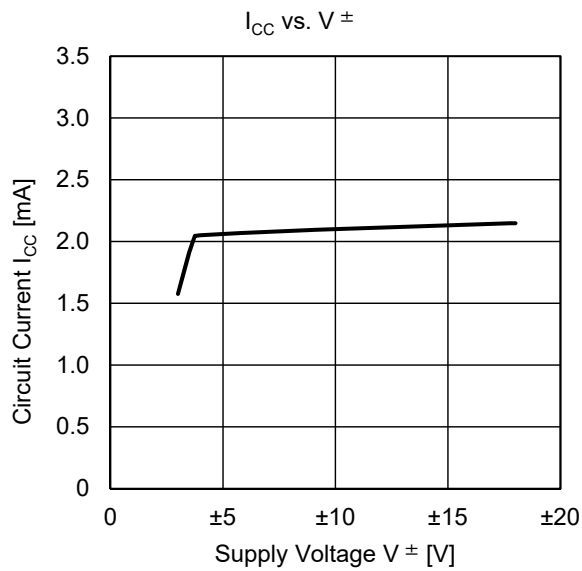
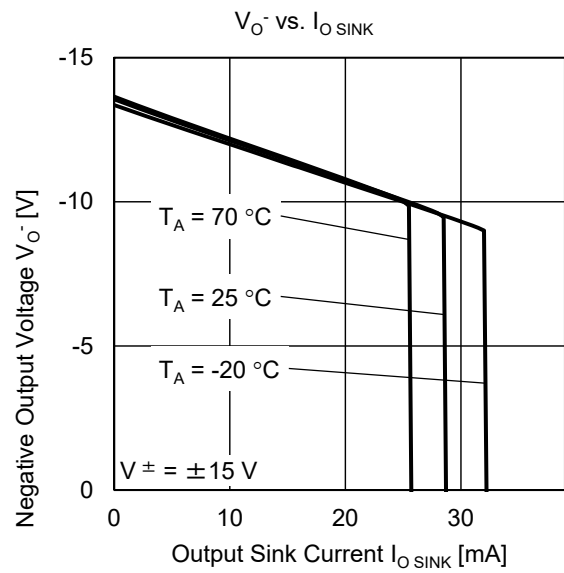
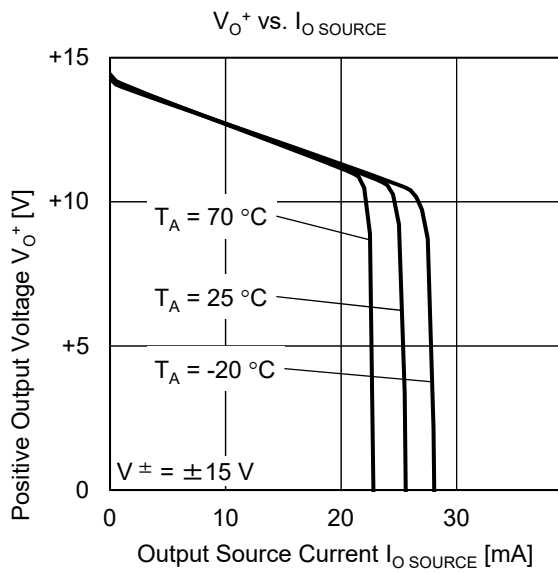
【Note】 7. The direction of the input bias current is the same direction that flows into the IC because the first stage is composed of Pch J-FET. When $T_J = 25\text{ }^{\circ}\text{C}$ or higher, it increases exponentially with increase in temperature (please see $I_B - T_A$ characteristics). During measurement, please kindly take care of $T_J \cong T_A$

Caution

Since μPC811 has high input impedance characteristics, please be careful of insulation between the terminals on the board

ELECTRICAL CHARACTERISTICS CURVE ($T_A = 25\text{ }^{\circ}\text{C}$, TYP.)





USE WITH PRECAUTIONS

- **Power Supply (Dual Power Supply / Single Power Supply)**

The op amp operates when a predetermine voltage is applied between V^+ - V^- . Therefore, while it operates from a single power supply ($V^- = \text{GND}$), it is not possible to operate the input and output near GND. So please be careful of the common-mode input voltage range and maximum output voltage.

- **Ratings of input/output pin voltage**

When the voltage of input/output pin exceeds the absolute maximum rating, the parasitic diode within the IC may conduct, causing characteristics degradation or damage. In addition, if the input pin is lower than V^- , or the output pin exceeds the power supply voltage, it is recommended to make a clamping circuit using a diode with low forward voltage (e.g.: Schottky diode) as protection.

- **Range of common-mode input voltage**

When the supply voltage does not meet the condition of electrical characteristics, the range of common-mode input voltage is as follows.

$$V_{\text{ICM}} (\text{TYP.}) : V^- + 3 \sim V^+ - 1 [\text{V}] (T_A = 25^\circ \text{C})$$

During designing, do include some margin by considering characteristics variation, temperature characteristics etc.

- **Maximum Output Voltage**

The TYP. value range of the maximum output voltage when the supply voltage does not meet the condition of electrical characteristics is as follows:

$$V_{\text{om}^+} (\text{TYP.}) : V^+ - 1 [\text{V}] (T_A = 25^\circ \text{C}), V_{\text{om}^-} (\text{TYP.}) : V^- + 1.7 [\text{V}] (T_A = 25^\circ \text{C})$$

During designing, do include some margin by considering characteristics variation, temperature characteristics and so on. In addition, also note that the output voltage range ($V_{\text{om}^+} - V_{\text{om}^-}$) will become narrow when the output current increases.

- **Handling of ICs**

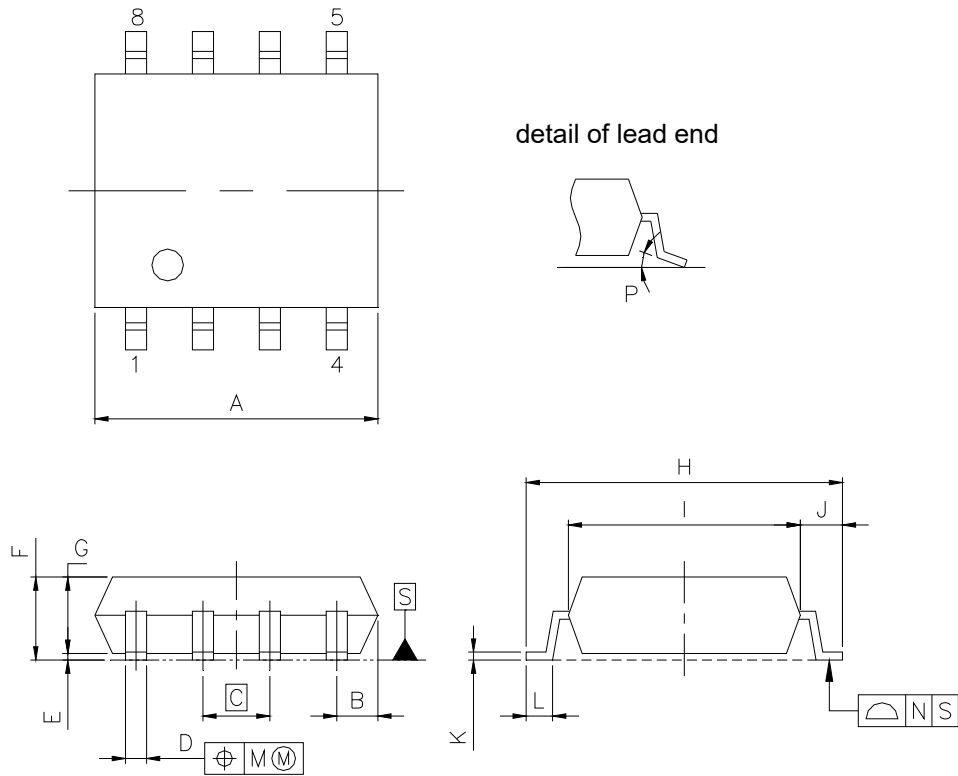
When stress is added to the ICs due to warpage or bending of a board, the characteristic may fluctuates due to piezoelectric (piezo) effect. Therefore, pay attention to warpage or bending of a board.

PACKAGE DRAWINGS

8-PIN PLASTIC SOP

JEITA Package code	RENESAS code	Previous code	MASS (TYP.) [g]
P-SOP8-0225-1.27	PRSP0008DL-A	S8GM-50-225B	0.08

Unit : mm



NOTE

Each lead centerline is located within 0.12 mm of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS
A	5.2 ^{+0.17} _{-0.20}
B	0.78 MAX
C	1.27 (T.P.)
D	0.42 ^{+0.08} _{-0.07}
E	0.1 ±0.1
F	1.59 ±0.21
G	1.49
H	6.5 ±0.3
I	4.4 ±0.15
J	1.1 ±0.2
K	0.17 ^{+0.08} _{-0.07}
L	0.6 ±0.2
M	0.12
N	0.10
P	3° ^{+7°} _{-3°}

Notice

- Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation or any other use of the circuits, software, and information in the design of your product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use of these circuits, software, or information.
- Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other claims involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to, the product data, drawings, charts, programs, algorithms, and application examples.
- No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
- You shall not alter, modify, copy, or reverse engineer any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copying or reverse engineering.
- Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The intended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.
"Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; industrial robots; etc.
"High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc.
Unless expressly designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not intended or authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems; surgical implantations; etc.), or may cause serious property damage (space system; undersea repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or any third parties arising from the use of any Renesas Electronics product that is inconsistent with any Renesas Electronics data sheet, user's manual or other Renesas Electronics document.
- When using Renesas Electronics products, refer to the latest product information (data sheets, user's manuals, application notes, "General Notes for Handling and Using Semiconductor Devices" in the reliability handbook, etc.), and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat dissipation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions, failure or accident arising out of the use of Renesas Electronics products outside of such specified ranges.
- Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics, such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Unless designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not subject to radiation resistance design. You are responsible for implementing safety measures to guard against the possibility of bodily injury, injury or damage caused by fire, and/or danger to the public in the event of a failure or malfunction of Renesas Electronics products, such as safety design for hardware and software, including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult and impractical, you are responsible for evaluating the safety of the final products or systems manufactured by you.
- Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. You are responsible for carefully and sufficiently investigating applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive, and using Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
- Renesas Electronics products and technologies shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall comply with any applicable export control laws and regulations promulgated and administered by the governments of any countries asserting jurisdiction over the parties or transactions.
- It is the responsibility of the buyer or distributor of Renesas Electronics products, or any other party who distributes, disposes of, or otherwise sells or transfers the product to a third party, to notify such third party in advance of the contents and conditions set forth in this document.
- This document shall not be reprinted, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics.
- Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products.
(Note 1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its directly or indirectly controlled subsidiaries.
(Note 2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

以下“注意事项”为从英语原稿翻译的中文译文，仅作为参考译文，英文版的“Notice”具有正式效力。

注意事项

- 本文档中所记载的关于电路、软件和其他相关信息仅用于说明半导体产品的操作和应用实例。用户如在产品或系统设计中应用本文档中的电路、软件和相关信息或将此等内容用于其他目的时，请自行负责。对于用户或第三方因使用上述电路、软件或信息而遭受的任何损失和损害，瑞萨电子概不承担任何责任。
- 瑞萨电子在此明确声明，对于因使用瑞萨电子产品或本文档中所述技术信息（包括但不限于产品数据、图、表、程序、算法、应用实例）而造成的与第三方专利、版权或其他知识产权相关的侵权或任何其他索赔，瑞萨电子不作任何保证并概不承担责任。
- 本文档所记载的内容不应视为对瑞萨电子或其他人所有的专利、版权或其他知识产权作出任何明示、默示或其它方式的许可及授权。
- 用户不得对瑞萨电子的任何产品进行全部或部分更改、修改、复制或反向工程。对于用户或第三方因上述更改、修改、复制或对瑞萨电子产品进行反向工程的行为而遭受的任何损失或损害，瑞萨电子不承担任何责任。
- 瑞萨电子产品根据其质量等级分为两个等级：“标准等级”和“高质量等级”。每种瑞萨电子产品的预期用途均取决于产品的质量等级，如下所示：
标准等级：计算机、办公设备、通讯设备、测试和测量设备、视听设备、家用电器、机械工具、个人电子设备、工业机器人等。
高质量等级：运输设备（汽车、火车、轮船等）、交通控制系统（交通信号灯）、大型通讯设备、关键金融终端系统、安全控制设备等。
除非是瑞萨电子产品数据表或其他瑞萨电子文档中明确指定为高可靠性产品或用于恶劣环境的产品，否则瑞萨电子产品不能用于、亦未授权用于可能对人类生命造成直接威胁的产品或系统及可能造成人身伤害的产品或系统（人工生命维持装置或系统、植埋于体内的装置等）中，或者可能造成重大财产损失的产品或系统（太空系统、海底增音机、核能控制系统、飞机控制系统、关键装置系统、军用设备等）中。对于用户或任何第三方因使用不符合瑞萨电子产品数据表、使用说明书或其他瑞萨电子文档的瑞萨电子产品而产生的任何故障、失效或事故，瑞萨电子概不承担任何责任。
- 使用瑞萨电子产品时，请参阅最新产品信息（数据表、使用说明书、应用指南、可靠性手册中的“半导体元件处理和使用一般注意事项”等），并确保使用条件在瑞萨电子指定的最大额定值、电源工作电压范围、散热特性、安装条件等范围内使用。对于在上述指定范围之外使用瑞萨电子产品而产生的任何故障、失效或事故，瑞萨电子概不承担任何责任。
- 虽然瑞萨电子一直致力于提高瑞萨电子产品的质量和可靠性，但是，半导体产品有其自身的具体特性，如一定的故障发生率以及在某些使用条件下会发生故障等。除非是瑞萨电子产品数据表或其他瑞萨电子文档中指定为高可靠性产品或用于恶劣环境的导弹，否则瑞萨电子产品未进行防辐射设计，以避免因瑞萨电子产品失效或发生故障而造成身体伤害、火灾导致伤害或损害和/或其他对公众构成危险事故。例如进行软硬件安全设计（包括但不限于冗余设计、防火控制以及故障预防等）、适当的老化处理或其他适当的措施等。由于对微机电软件单独进行评估非常困难且不实际，所以请用户自行负责最终产品或系统进行安全评估。
- 关于环境保护方面的详细内容，例如每种瑞萨电子产品的环境兼容性等，请与瑞萨电子的营业部门联系。用户负责仔细并充分查阅对管制物质的使用或含量进行管理的所有适用法律法规（包括但不限于《欧盟RoHS指令》），并在使用瑞萨电子产品时遵守所有适用法律法规。对于因用户未遵守相应法律法规而导致的损害或损失，瑞萨电子概不承担任何责任。
- 不可将瑞萨电子产品和技术用于或者嵌入日本国内或海外相应的法律法规所禁止生产、使用及销售的任何产品或系统中。也不可将瑞萨电子产品和技术用于(1)与大规模杀伤性武器（例如核武器、化学武器、生物武器或运送此类武器的导弹，包括无人机(UAV)）的开发、设计、制造、使用、存储等相关的任何目的；(2)与常规武器的开发、设计、制造或使用相关的任何目的；(3)扰乱国际和平与安全的任何其他目的，并且不可向任何第三方销售、出口、租赁、转让、或让与瑞萨电子产品或技术，无论直接或间接知悉或者有理由知悉该第三方或任何其他方将从从事上述活动。用户必须遵守对方或交易行使司法管辖权的任意国家/地区政府所公布和管理的任何适用出口管制法律法规。
- 瑞萨电子产品的买方或分销商，或者分销、处置产品、或以其他方式向第三方出售或转让产品的任何其他方有责任事先向所述第三方通知本文档规定的内容和条件。
- 在事先未得到瑞萨电子书面认可的情况下，不得以任何形式部分或全部再版、转载或复制本文档。
- 如果对本文档所记载的信息或瑞萨电子产品有任何疑问，请向瑞萨电子的营业部门咨询。
(注1) 瑞萨电子：在本文档中指瑞萨电子株式会社及其控股子公司。
(注2) 瑞萨电子产品：指瑞萨电子开发或生产的任何产品。

(Rev.4.0-1 November 2017)



SALES OFFICES

Renesas Electronics Corporation

<http://www.renesas.com>

Refer to "<http://www.renesas.com/>" for the latest and detailed information.

Renesas Electronics Corporation
TOYOSU FORESIA, 3-2-24 Toyosu, Koto-ku, Tokyo 135-0061, Japan

Renesas Electronics America Inc.
1001 Murphy Ranch Road, Milpitas, CA 95035, U.S.A.
Tel: +1-408-432-8888, Fax: +1-408-434-5351

Renesas Electronics Canada Limited
9251 Yonge Street, Suite 8309 Richmond Hill, Ontario Canada L4C 9T3
Tel: +1-905-237-2004

Renesas Electronics Europe GmbH
Arcadiastrasse 10, 40472 Düsseldorf, Germany
Tel: +49-211-6503-0, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
Room 101-T01, Floor 1, Building 7, Yard No. 7, 8th Street, Shangdi, Haidian District, Beijing 100085, China
Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 301, Tower A, Central Towers, 555 Langao Road, Putuo District, Shanghai 200333, China
Tel: +86-21-2226-0888, Fax: +86-21-2226-0599

Renesas Electronics Hong Kong Limited
Unit 1601-1611, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2265-6688, Fax: +852-2886-9022

Renesas Electronics Taiwan Co., Ltd.
13F, No. 363, Fu Shing North Road, Taipei 10543, Taiwan
Tel: +886-2-8175-9600, Fax: +886-2-8175-9670

Renesas Electronics Singapore Pte. Ltd.
80 Bendemeer Road, Unit #03-02 Hyflux Innovation Centre, Singapore 339949
Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd.
Unit No 3A-1 Level 3A Tower 8 UOA Business Park, No 1 Jalan Pengaturcara U1/51A, Seksyen U1, 40150 Shah Alam, Selangor, Malaysia
Tel: +60-3-5022-1298, Fax: +60-3-5022-1290

Renesas Electronics India Pvt. Ltd.
No.777C, 100 Feet Road, HAL 2nd Stage, Indiranagar, Bangalore 560 038, India
Tel: +91-80-67208700

Renesas Electronics Korea Co., Ltd.
17F, KAMCO Yangjae Tower, 262, Gangnam-daero, Gangnam-gu, Seoul, 06265 Korea
Tel: +82-2-558-3737, Fax: +82-2-558-5338

© 2019 Renesas Electronics Corporation. All rights reserved.

Colophon 8.0