

Power management (dual digital transistors)

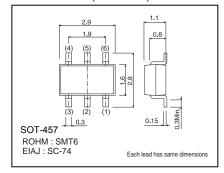
Features

- 1) Two digital class transistors in a SMT package.
- 2) Up to 500mA can be driven.
- 3) Low VcE(sat) of drive transistors for low power dissipation.

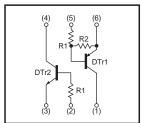
● Package, marking, and packaging specifications

Part No.	IMD10A
Package	SMT6
Marking	D10
Code	T108
Basic ordering unit (pieces)	3000

●Dimensions (Unit : mm)



◆Absolute maximum ratings (Ta=25°C)



●Equivalent circuit

DTr₁

Parameter	Symbol	Limits	Unit
Supply voltage	Vcc	-50	V
Input voltage	Vin	−5 to +5	V
Collector current	Ic	-500	mA

DTr_2

Parameter	Symbol	Limits	Unit
Collector-base voltage	Vсво	50	V
Collector-emitter voltage	VCEO	50	V
Emitter-base voltage	Vево	5	V
Collector current	Ic	100	mA

Total

Parameter	Symbol	Limits	Unit
Power dissipation	Pd	300(TOTAL)	mW *
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

^{* 200}mW per element must not be exceeded.

●Electrical characteristics (Ta=25°C)

DTr-

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Land to all and	VI(off)	_	_	-0.3	V	Vcc= -5V , Io= -100μA
Input voltage	VI(on)	-1.5	_	_]	Vo= -0.3V , Io= -100mA
Output voltage	V _{O(on)}	_	-0.1	-0.3	V	lo= -100mA , l= -5mA
Input current	lı	_	_	-25	mA	V₁= —2V
Output current	IO(off)	_	_	-0.5	μΑ	Vcc= -50V , Vi=0V
DC current gain	Gı	68	_	_	_	lo= -100mA , Vo= -5V
Transition frequency	f⊤	_	200	_	MHz	Vc==-10V , I==50mA , f=100MHz *
Input resistance	R ₁	70	100	130	Ω	_
Resistance ratio	R ₂ / R ₁	80	100	120	_	_

st Transition frequency of the device.

DTr₂

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	50	_	_	V	Ic=50μA
Collector-emitter breakdown voltage	BVceo	50	_	_	V	Ic=1mA
Emitter-base breakdown voltage	ВVево	5	_	_	V	Iε=50μA
Collector cutoff current	Ісво	_	_	0.5	μА	Vcb=50V
Emitter cutoff current	ІЕВО	_	_	0.5	μА	V _{EB} =4V
Collector-emitter saturation voltage	VCE(sat)	_	_	0.3	V	Ic=10mA , I _B =1mA
DC current transfer ratio	hfe	100	250	600	_	VcE=5V , Ic=1mA
Transition frequency	f⊤	_	250	_	MHz	Vc=10V , I=-5mA , f=100MHz *
Input resistance	R ₁	7	10	13	kΩ	_

st Transition frequency of the device.

Electrical characteristic curves

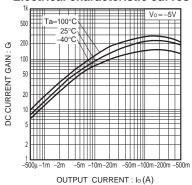


Fig.1 DC current gain vs.
Output current characteristics

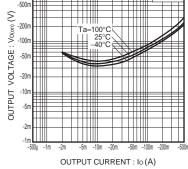


Fig.2 Output voltage vs.
Output current characteristics

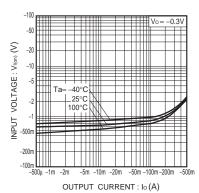


Fig.3 Input voltage vs. Output current (ON characteristics)

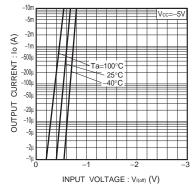


Fig.4 Output current vs.
Input voltage (OFF characteristics)

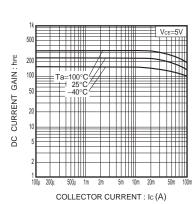


Fig.5 DC current gain vs. Collector current

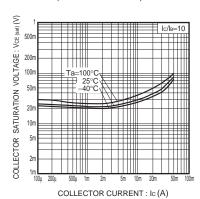


Fig.6 Collector-emitter saturation voltage vs. Collector current

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