R5F1026AGSP

Product Spec

Basic Information | Memory | CPU (Bit, Clock, DMA, External Bus etc) | Timers | Analog | Interfaces | Operating Conditions

Basic Information	
Product Status	Mass Production
Family	RL78
Series	RL78/G1x
Group	RL78/G12

Memory	
Program memory	16 KB Flash memory
RAM	1.5 KB
Data Flash	2 KB

CPU (Bit, Clock, DMA, External Bus etc)	
CPU	RL78 (16-bit)
Max. Frequency	24 MHz
On-chip Oscillator Freq.	High-Speed: 1, 4, 8, 12, 16, 24MHz Low-speed: 15kHz
Power-On Reset	YES
Low Voltage Detection	YES
DMA Remarks	DMAC x 2 ch
External Interrupt Pins	10
I/O Ports	18

Timers	
8-bit Timers	4 ch
16-bit Timers	4 ch
Watchdog Timers	1 ch
Other Timers	12-bit Interval Timer x 1 ch
PWM Output	3

Analog	
A/D Converters	10-bit x 11 ch

Interfaces	
CSIs	2 ch
UARTs	1 ch
I2Cs	3 ch

Operating Conditions	
Operating Voltage	2.4 to 5.5 V
Operating Temperature	-40 to 105 ... (Ambient Temperature)

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Order Information

Orderable Part No.	Date of PLP Termination	Production Status	Packing
R5F1026AGSP#V0	Mar. 2030	Mass Production	Tube
R5F1026AGSP#X0	Mar. 2030	Mass Production	Embossed Tape

: Product participating in the Product Longevity Program More information on the Product Longevity Program

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Package

Package	
Pins	20
Pin pitch	0.65 mm
Package Type	LSSOP
Body Size	4.4x6.5 mm^2
Package drawing	p20ma-65-naa

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Package	
Mount pad drawing	fig0004e
Mounting height	1.45 mm
Mass	0.1 g
Renesas code	PLSP0020JB-A (old: P20MA-65-NAA-1)
JEITA code	P-LSSOP20-4.4x6.5-0.65

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