



PHPT61003PY

100 V, 3A PNP high power bipolar transistor

12 August 2026

Product data sheet

1. General description

PNP high power bipolar transistor in a SOT669 (LFAK56) Surface-Mounted Device (SMD) power plastic package.

NPN complement: PHPT61003NY

2. Features and benefits

- High thermal power dissipation capability
- Suitable for high temperature applications up to 175 °C
- Reduced Printed-Circuit Board (PCB) requirements comparing to transistors in DPAK
- High energy efficiency due to less heat generation

3. Applications

- Power management
- Loadswitch
- Linear mode voltage regulator
- Backlighting applications

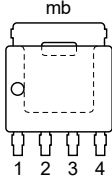
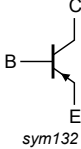
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	-100	V
I_C	collector current		-	-	-3	A
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms	-	-	-8	A
R_{CEsat}	collector-emitter saturation resistance	$I_C = -2$ A; $I_B = -200$ mA; pulsed; $t_p \leq 300$ μ s; $\delta \leq 0.02$; $T_{amb} = 25$ °C	-	110	180	m Ω

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter	 LFPAK56; Power-SO8 (SOT669)	
2	E	emitter		
3	E	emitter		
4	B	base		
mb	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PHPT61003PY	LFPAK56; Power-SO8	plastic, single-ended surface-mounted package; 4 terminals	SOT669

7. Marking

Table 4. Marking codes

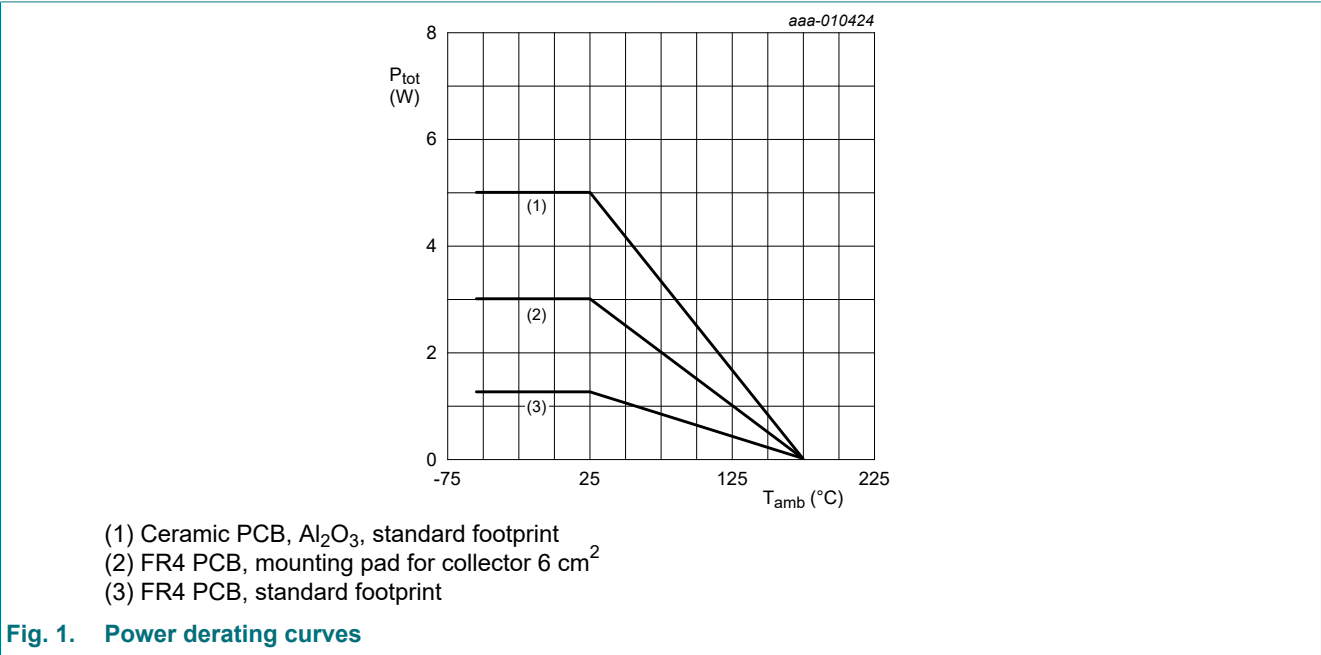
Type number	Marking code
PHPT61003PY	1003PAB

8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-100	V
V _{CEO}	collector-emitter voltage	open base		-	-100	V
V _{EBO}	emitter-base voltage	open collector		-	-8	V
I _C	collector current			-	-3	A
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-8	A
I _B	base current			-	-0.5	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	1.25	W
			[2]	-	3	W
			[3]	-	5	W
			[4]	-	25	W
T _j	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB); single-sided copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB; single-sided copper; tin-plated mounting pad for collector 6 cm².
- [3] Device mounted on an ceramic PCB; Al₂O₃; standard footprint.
- [4] Power dissipation from junction to mounting base.



9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	115	K/W
			[2]	-	-	50	K/W
			[3]	-	-	30	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	6	K/W

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB); single-sided copper; tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB; single-sided copper; tin-plated and mounting pad for collector 6 cm².
[3] Device mounted on an ceramic PCB; Al₂O₃; standard footprint.

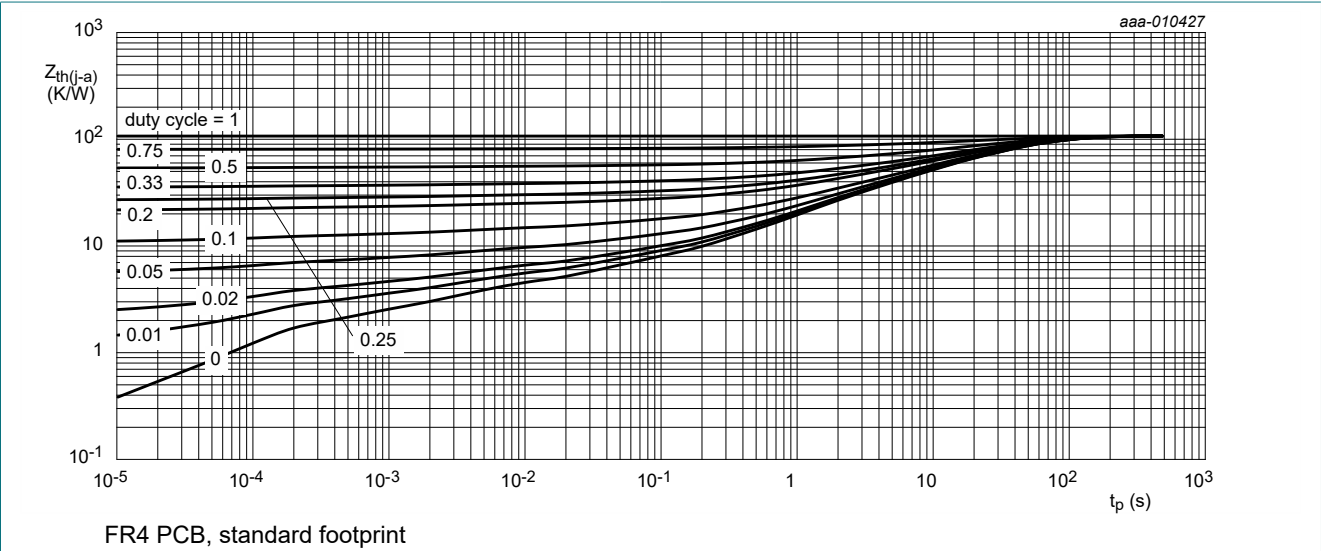


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

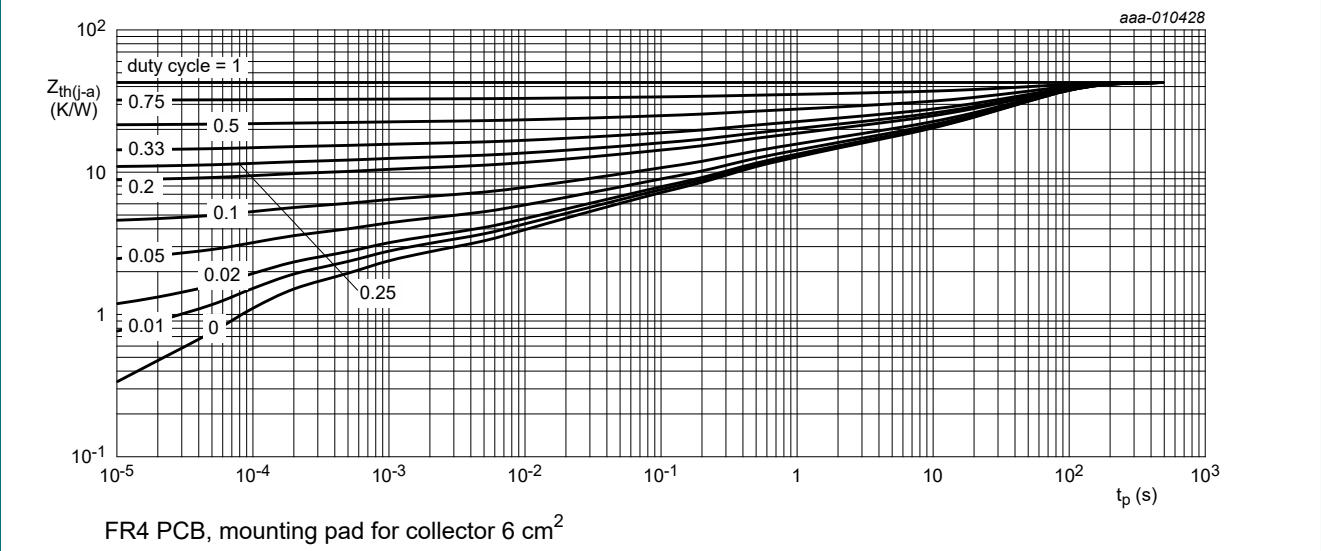


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = -80 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-100	nA
		V _{CB} = -80 V; I _E = 0 A; T _j = 150 °C	-	-	-50	µA
I _{CES}	collector-emitter cut-off current	V _{CE} = -80 V; V _{BE} = 0 V; T _{amb} = 25 °C	-	-	-100	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = -8 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-100	nA
h _{FE}	DC current gain	V _{CE} = -10 V; I _C = -500 mA; T _{amb} = 25 °C	150	220	-	
		V _{CE} = -10 V; I _C = -1 A; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C; pulsed	80	210	-	
		V _{CE} = -10 V; I _C = -2 A; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C; pulsed	20	100	-	
		V _{CE} = -10 V; I _C = -3 A; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C	10	40	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = -500 mA; I _B = -50 mA; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C	-	-70	-110	mV
		I _C = -2 A; I _B = -200 mA; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C	-	-220	-360	mV
R _{CEsat}	collector-emitter saturation resistance	I _C = -2 A; I _B = -200 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C	-	110	180	mΩ
V _{BEsat}	base-emitter saturation voltage	I _C = -1 A; I _B = -50 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C	-	-0.91	-1	V
		I _C = -2 A; I _B = -200 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C	-	-1.02	-1.2	V
V _{BEon}	base-emitter turn-on voltage	V _{CE} = -2 V; I _C = -100 mA; T _{amb} = 25 °C	-	-0.68	-0.9	V
t _d	delay time	V _{CC} = -12.5 V; I _C = -1 A; I _{Bon} = -50 mA; I _{Boff} = 50 mA; T _{amb} = 25 °C	-	20	-	ns
t _r	rise time		-	180	-	ns
t _{on}	turn-on time		-	200	-	ns
t _s	storage time		-	350	-	ns
t _f	fall time		-	220	-	ns
t _{off}	turn-off time		-	570	-	ns
f _T	transition frequency		-	125	-	MHz
C _c	collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	30	-	pF

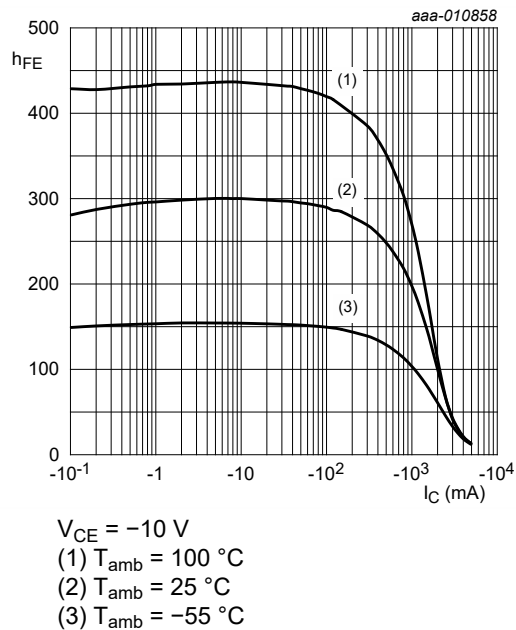


Fig. 4. DC current gain as a function of collector current; typical values

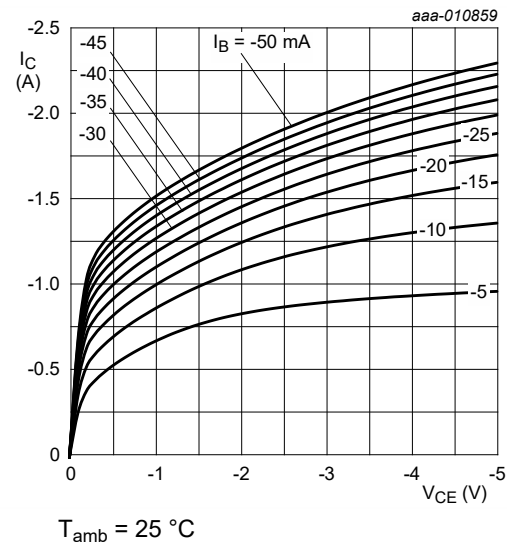


Fig. 5. Collector current as a function of collector-emitter voltage; typical values

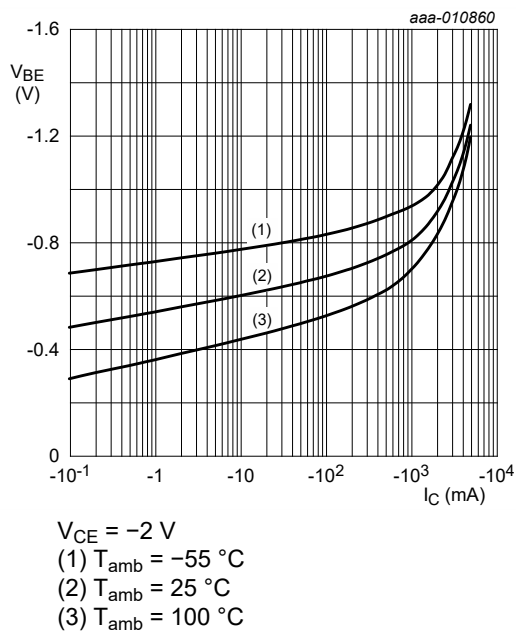


Fig. 6. Base-emitter voltage as a function of collector current; typical values

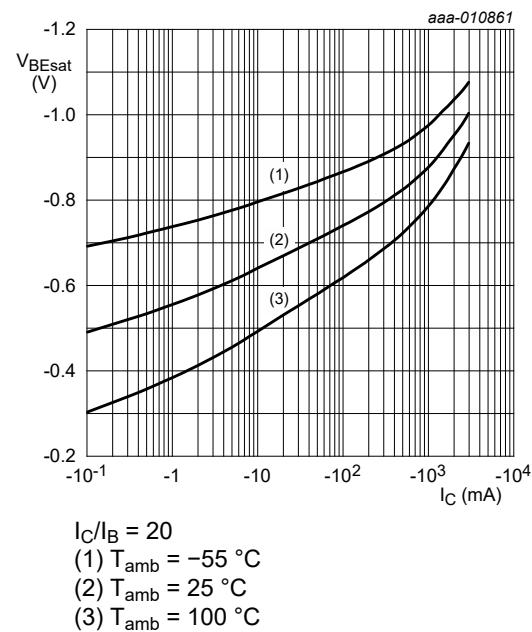


Fig. 7. Base-emitter saturation voltage as a function of collector current; typical values

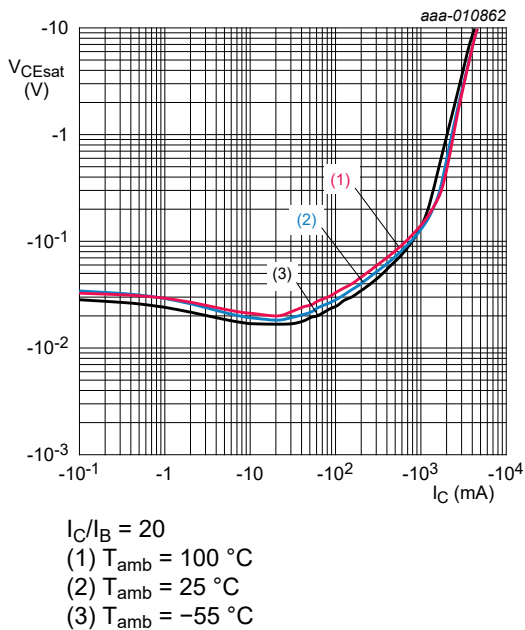


Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

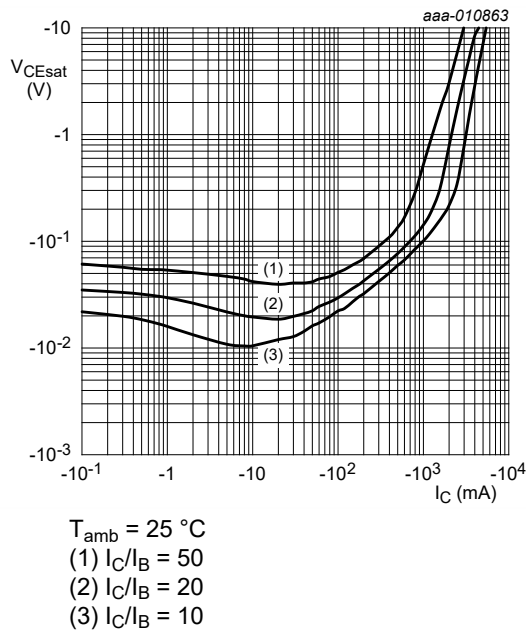


Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values

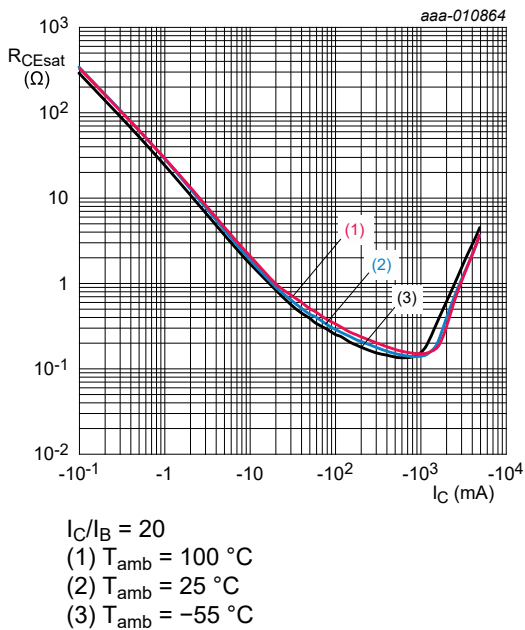


Fig. 10. Collector-emitter saturation resistance as a function of collector current; typical values

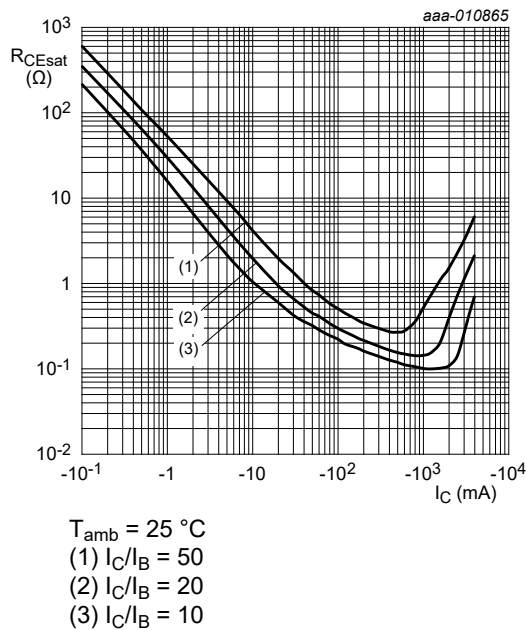


Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values

11. Test information

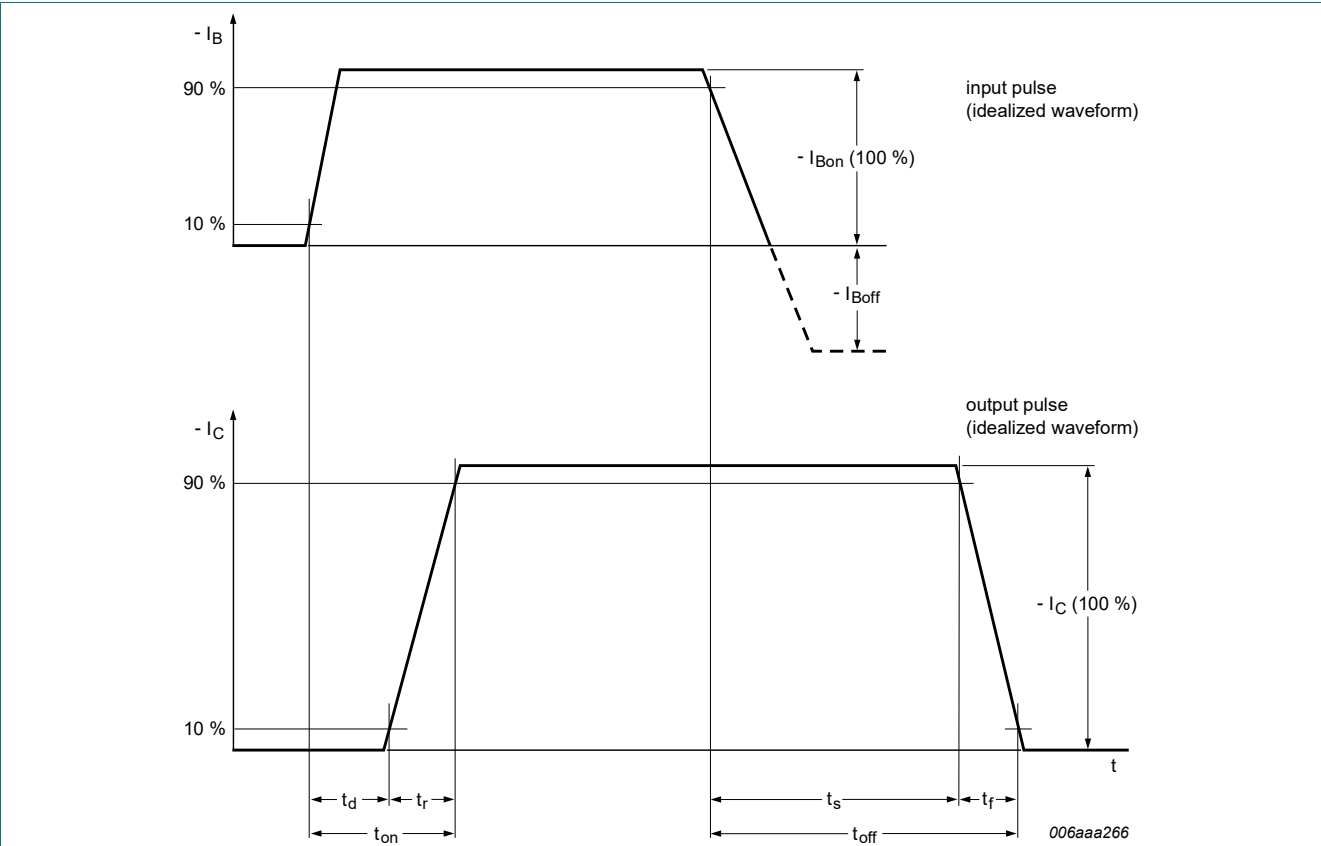


Fig. 12. Transistor switching time definition

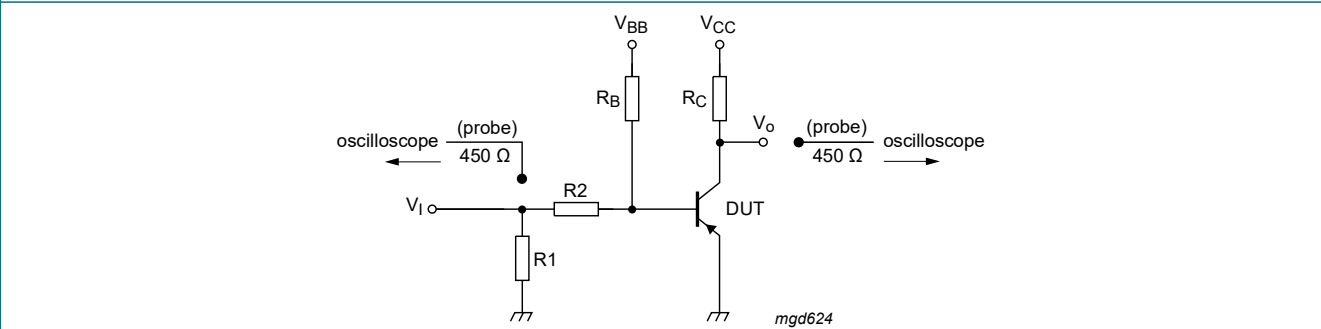


Fig. 13. Test circuit for switching times

12. Package outline

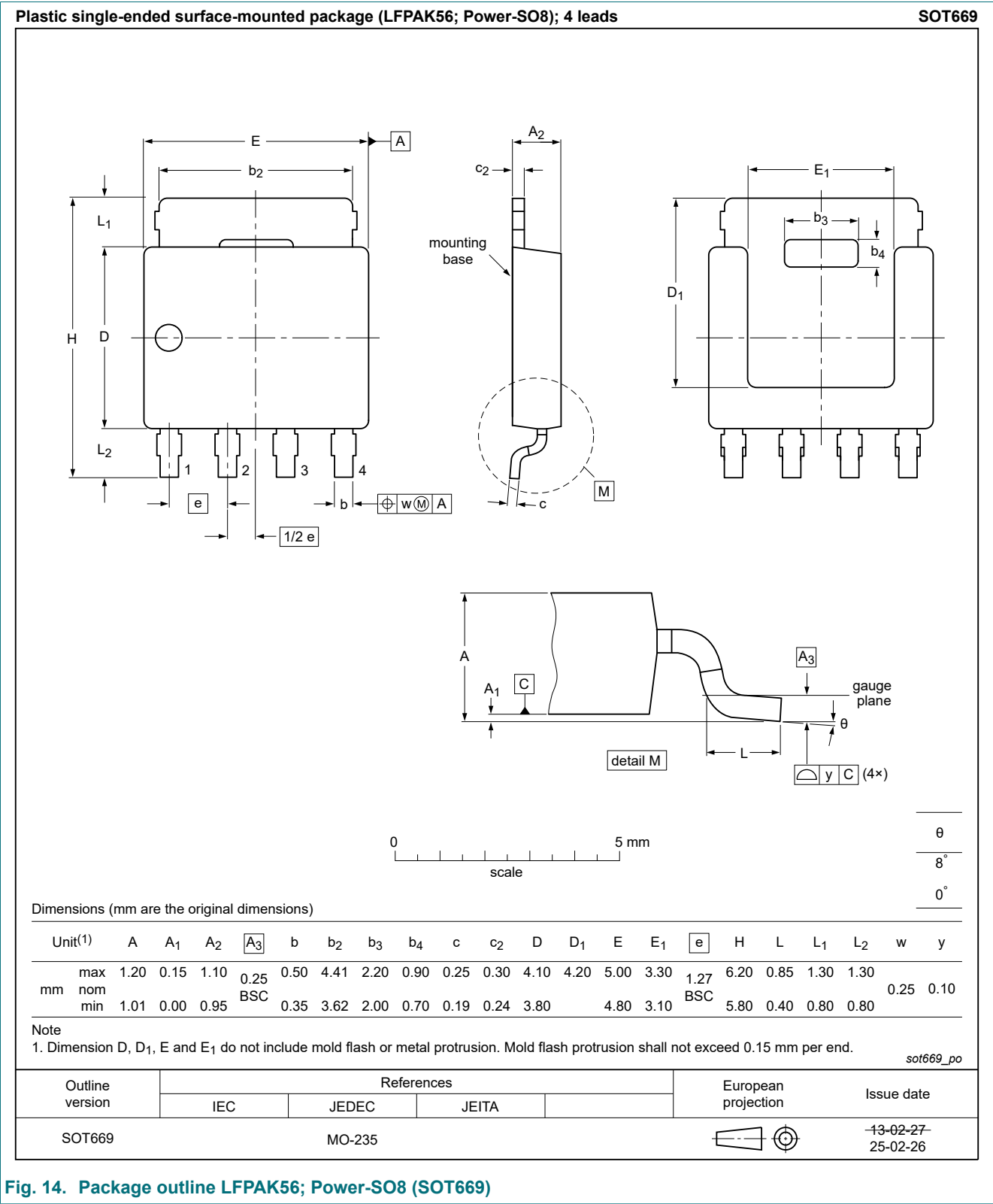
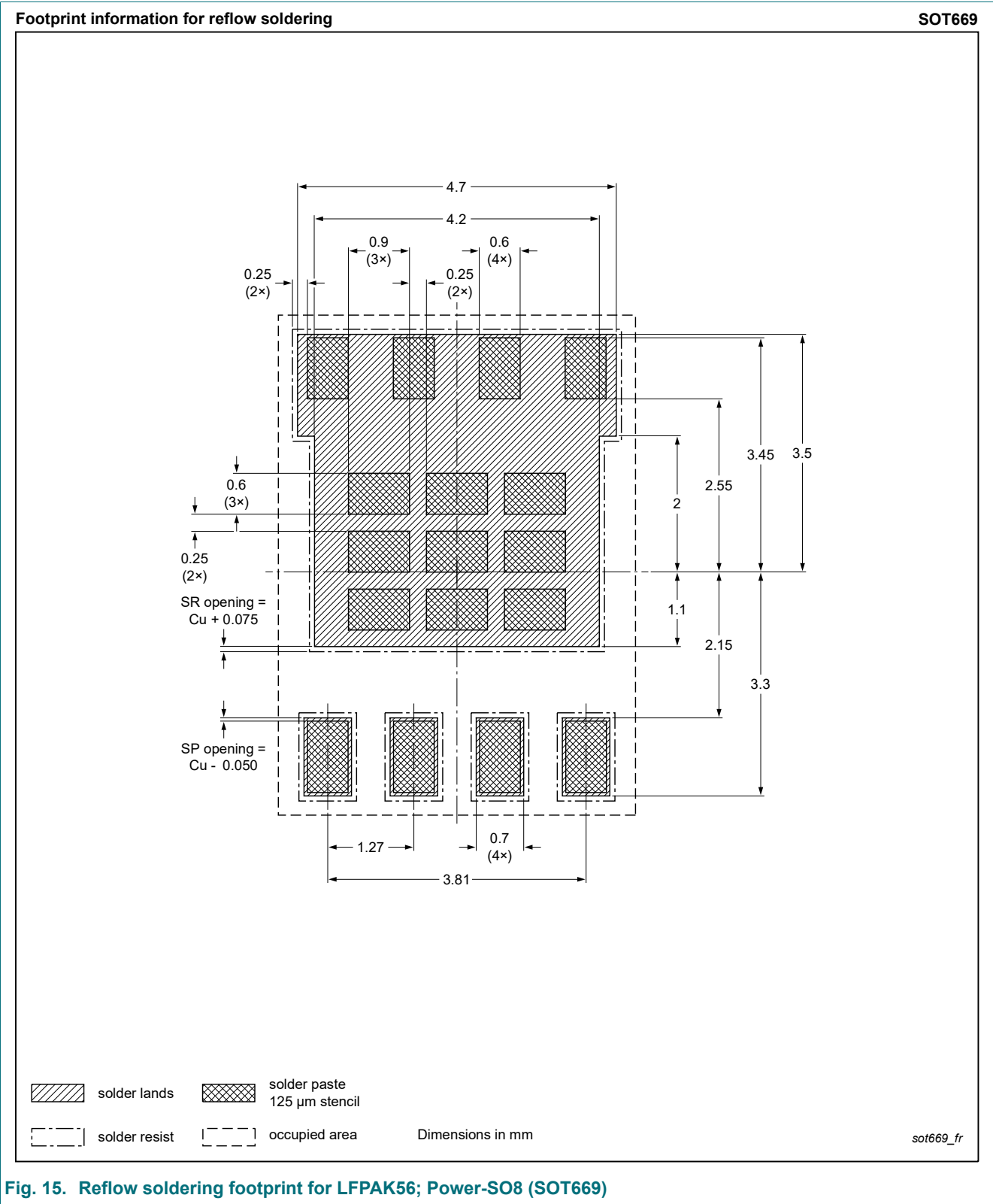


Fig. 14. Package outline LFAK56; Power-SO8 (SOT669)

13. Soldering



Wave soldering footprint information for LFPAK56 package

SOT669

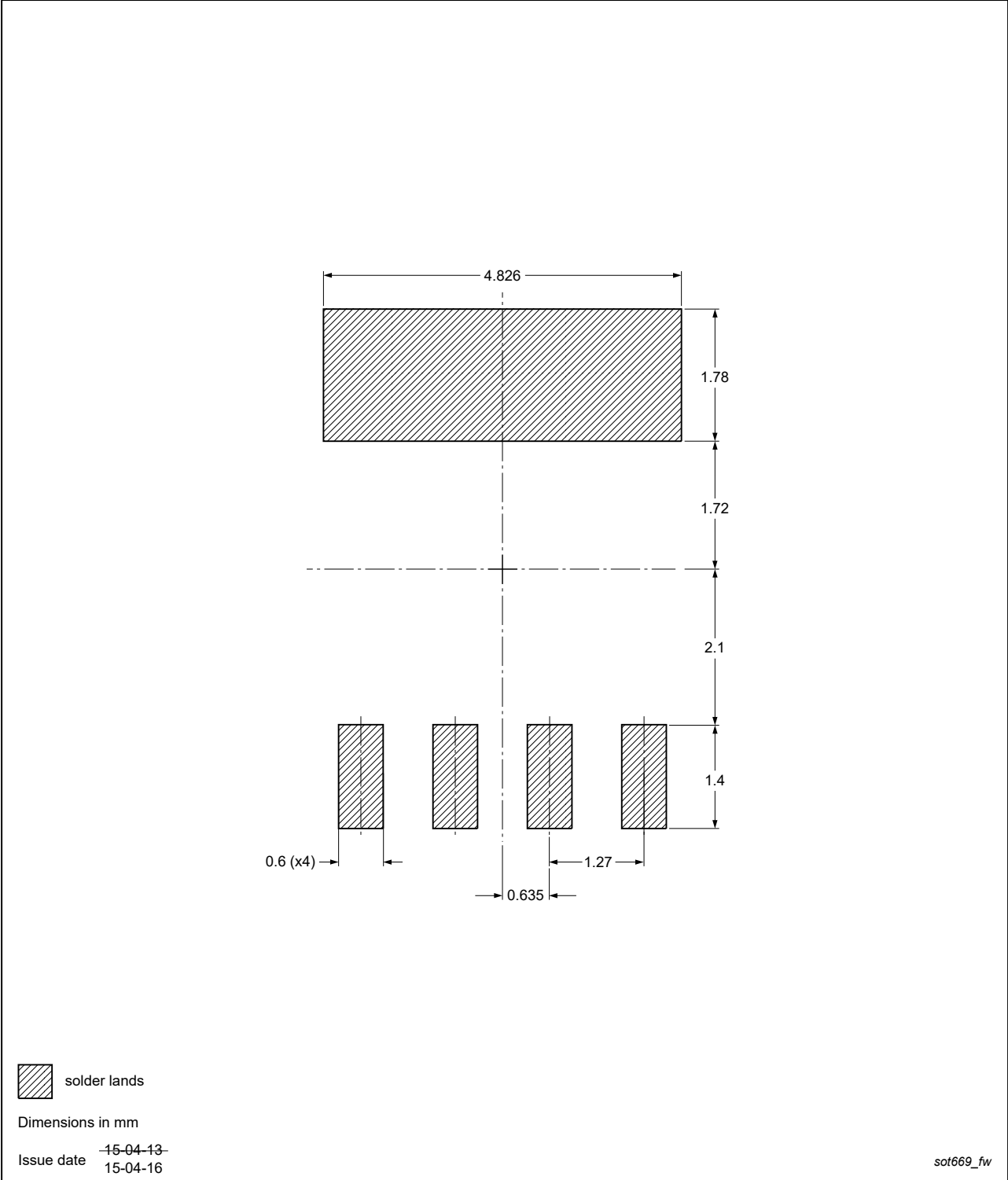


Fig. 16. Wave soldering footprint for LFPAK56; Power-SO8 (SOT669)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PHPT61003PY v.2	20260812	Product data sheet	-	PHPT61003PY v.1
Modifications:	- Editorial update of figures 5, 6 and 7. - Product(s) changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s).			
PHPT61003PY v.1	20140113	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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