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Intel® JHL6540 Thunderbolt™ 3 Controller

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Specifications

Essentials

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Specifications	
Essentials	
Status	Launched
Launch Date	Q2'16
Embedded Options Available	No
TDP	2.2 W
Recommended Customer Price	
Datasheet	Link
Conflict Free	Yes
I/O Specifications	
# of Ports	Dual
PCI Express Configurations ‡	Gen 3
Display Port	1.2
Package Specifications	
Package Size	10.7x10.7mm
Low Halogen Options Available	See MDDS

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PCN/MDDS Information

PCN/MDDS Information Not Available

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Refer to Datasheet for formal definitions of product properties and features.

"Announced" SKUs are not yet available. Please refer to the Launch Date for market availability.

‡ This feature may not be available on all computing systems. Please check with the system vendor to determine if your system delivers this feature, or reference the system specifications (motherboard, processor, chipset, power supply, HDD, graphics controller, memory, BIOS, drivers, virtual machine monitor-VMM, platform software, and/or operating system) for feature compatibility. Functionality, performance, and other benefits of this feature may vary depending on system configuration.

"Conflict free" and "conflict-free" means "DRC conflict free", which is defined by the U.S. Securities and Exchange Commission rules to mean products that do not contain conflict minerals (tin, tantalum, tungsten and/or gold) that directly or indirectly finance or benefit armed groups in the Democratic Republic of the Congo (DRC) or adjoining countries. Intel also uses the term "conflict-free" in a broader sense to refer to suppliers, supply chains, smelters and refiners whose sources of conflict minerals do not finance conflict in the DRC or adjoining countries. Intel processors manufactured before January 1, 2013 are not confirmed conflict free. The conflict free designation refers only to product manufactured after that date. For Intel Boxed Processors, the conflict free designation refers to the processor only, not to any additional included accessories, such as heatsinks/coolers.

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System and Maximum TDP is based on worst case scenarios. Actual TDP may be lower if not all I/Os for chipsets are used.

Low Halogen: Applies only to brominated and chlorinated flame retardants (BFRs/CFRs) and PVC in the final product. Intel components as well as purchased components on the finished assembly meet JS-709 requirements, and the PCB / substrate meet IEC 61249-2-21 requirements. The replacement of halogenated flame retardants and/or PVC may not be better for the environment.

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