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EVAL-SDP-I-FMC EVAL-SDP-I-FMC





SDP-I-FMC

SDP-I-FMC Interposer







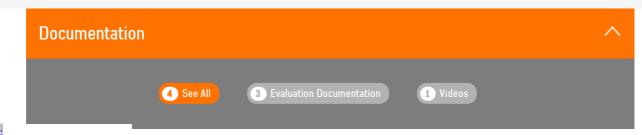


Product Details

The SDP-I-FMC enables easy connection of a wide variety of FPGA evaluation boards to the range of daughter boards from ADI's System Demonstration Platform (SDP) family. The SDP-I-FMC has a Low Pin Count (LPC) FMC connector and a 120 pin connector to connect to SDP daughter boards. The SDP-I-FMC can only be used with 3.3VIO FPGA evaluation boards.

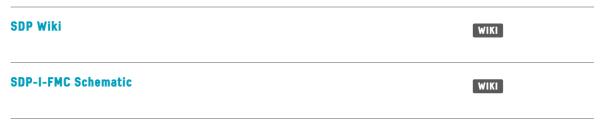
Example projects for prototyping with an FPGA evaluation board, the SDP-I-FMC, and an SDP daughter evaluation board can be found on the ADI wiki.

The System Demonstration Platform is a collection of controller boards, interposer boards, and daughter boards, used for easy, low cost evaluation of ADI components and reference circuits. For a general overview of the entire platform visit the... Show More..



SDP Facilitates Quick Prototyping and Evaluation

Read our Analog Dialog Article for more information on the SDP



System Demonstration Platform Simplifies Evaluation

The System Demonstration Platform (SDP) is a low-cost, reusable and versatile evaluation system. It is comprised of...



Example Project for SDP daughter boards on ADI wiki



WIKI

Evaluation Boards

Buy

Pricing displayed is based on 1-piece.

Model	Description	Price	RoHS
SDP-FMC-IB1Z Production	SDP to FMC Interposer Board		Yes
Back Ad	d to cart Select a country Y	Chec	k Inventory

Pricing displayed is based on 1-piece. The

Ahead of What's Possible

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4,700+

125,000

Years

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