



60V +175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BV _{DSS}	Rds(on) max	ID MAX Tc = +25°C
60V	1.5 m Ω @ V _{GS} = 10V	225A

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP, and is ideal for use in:

- Engine Management Systems
- Body Control Electronics
- DC-DC Converters

PowerDI5060-8 (SWP) (Standard)



Top View Bottom View

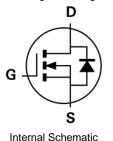
Features

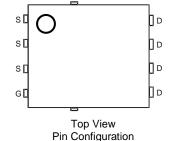
- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- High-Conversion Efficiency
- Low RDS(ON) Minimizes On State Losses
- Low Input Capacitance
- Fast Switching Speed
- Wettable Flank for Improved Optical Inspection
- Lead-Free Finish: RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMTH61M5SPSWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: PowerDI[®]5060-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (©3)
- Weight: 0.097 grams (Approximate)





Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH61M5SPSWQ-13	PowerDI5060-8 (SWP) (Standard)	2500 / Tape & Reel

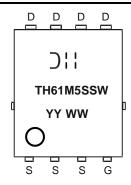
Notes:

1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.

Pin1

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



⊃¦¦ = Manufacturer's Marking TH61M5SSW = Product Type Marking Code YYWW or YYWW= Date Code Marking YY or YY= Year (ex: 20 = 2020) WW = Week (01 to 53)

PowerDI is a registered trademark of Diodes Incorporated.



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V_{DSS}	60	V		
Gate-Source Voltage	Vgss	±20	V		
Continuous Durin Compant VI 40V/(Nata C)	Tc = +25°C	- I _D	225	^	
Continuous Drain Current, V _{GS} = 10V (Note 6)	T _C = +100°C		160	A	
Maximum Continuous Body Diode Forward Current (Note 6)	Is	225	Α		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	IDM	900	Α		
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%	Ism	900	Α		
Avalanche Current, L = 1mH		I _{AS}	35.8	Α	
Avalanche Energy, L = 1mH		Eas	640.8	mJ	

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	3.2	W
Thermal Resistance, Junction to Ambient (Note 5)		RθJA	47	°C/W
Total Power Dissipation (Note 6)	PD	167	W	
Thermal Resistance, Junction to Case (Note 6)		R _θ JC	0.9	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +175	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

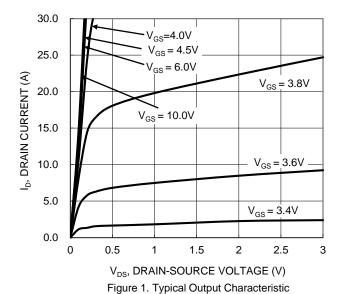
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	60		_	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current	IDSS	I		1	μA	V _{DS} = 48V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	2	_	4	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	RDS(ON)	l	1.1	1.5	mΩ	V _{GS} = 10V, I _D = 30A	
Diode Forward Voltage	VsD	I	0.7	1.2	V	V _G S = 0V, I _S = 20A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss		8306	_		$V_{DS} = 30V$, $V_{GS} = 0V$, $f = 1MHz$	
Output Capacitance	Coss		2735	_	pF		
Reverse Transfer Capacitance	Crss	1	184	_			
Gate Resistance	Rg		3.0	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg	_	130.6	_		V _{DS} = 30V, I _D = 30A, V _{GS} = 10V	
Gate-Source Charge	Qgs	_	30.4	_	nC		
Gate-Drain Charge	Q_{gd}	_	28.1	_		VGS = 10V	
Turn-On Delay Time	td(ON)	_	11.3	_		$V_{DD} = 30V, V_{GS} = 10V,$ $I_{D} = 30A, R_{g} = 3\Omega$	
Turn-On Rise Time	t _R	_	28.5	_	ns		
Turn-Off Delay Time	tD(OFF)	1	86.2	_	115		
Turn-Off Fall Time	tF	1	47.6	_			
Body Diode Reverse Recovery Time	t _{RR}	1	70.4	_	ns	I _F = 30A, di/dt = 100A/µs	
Body Diode Reverse Recovery Charge	Q _{RR}	_	127	_	nC	- 30A, αναί = 100A/μ5	

Notes: 5. Device mounted on FR-4 substrate PCB, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

^{6.} Thermal resistance from junction to soldering point (on the exposed drain pad).7. Short duration pulse test used to minimize self-heating effect.

^{8.} Guaranteed by design. Not subject to product testing.





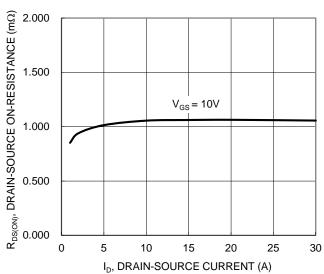


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

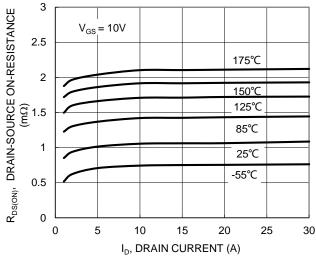


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

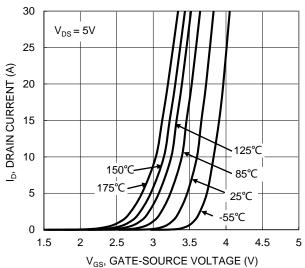


Figure 2. Typical Transfer Characteristic

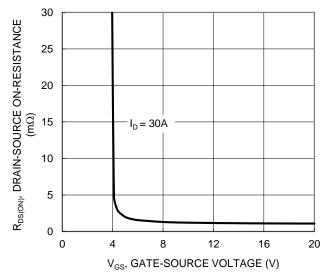


Figure 4. Typical Transfer Characteristic

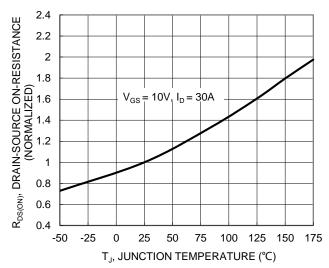


Figure 6. On-Resistance Variation with Temperature





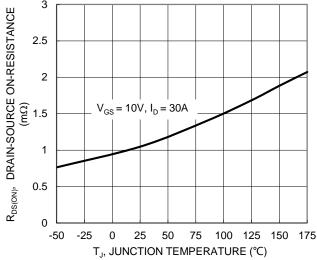


Figure 7. On-Resistance Variation with Temperature

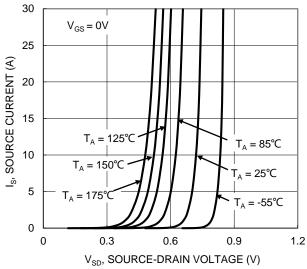


Figure 9. Diode Forward Voltage vs. Current

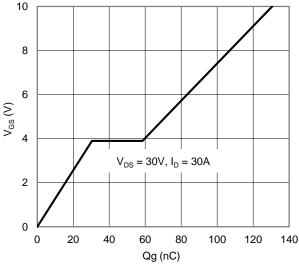


Figure 11. Gate Charge

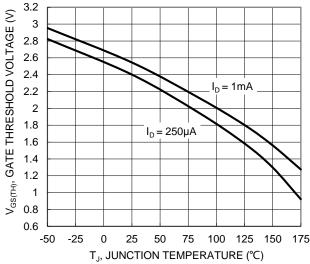


Figure 8. Gate Threshold Variation vs. Junction Temperature

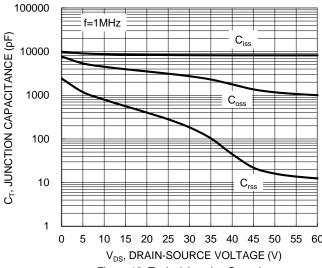
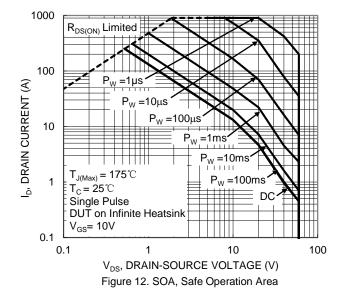


Figure 10. Typical Junction Capacitance





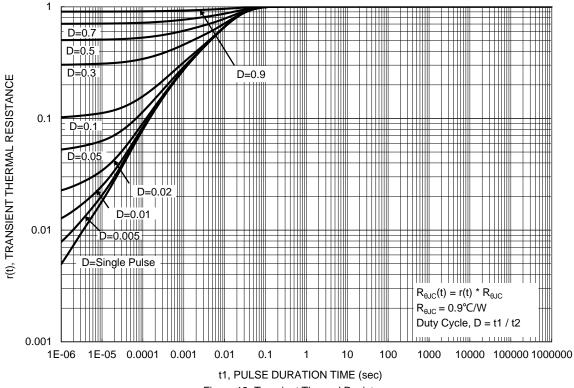


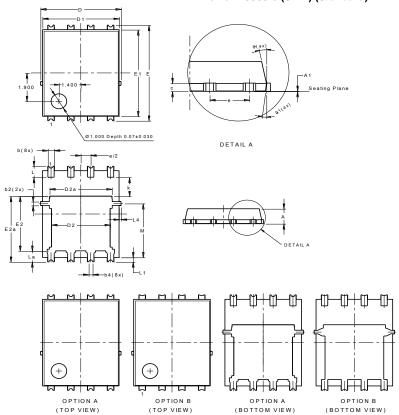
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8 (SWP) (Standard)

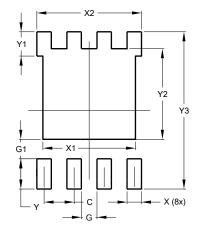


PowerDI5060-8 (SWP)					
(Standard)					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A1	0	0.05			
b	0.30	0.50	0.41		
b2	0.20	0.35	0.25		
b4	().25REF	•		
С	0.230	0.330	0.277		
D	5	.15 BS0)		
D1	4.70	5.10	4.90		
D2	3.56	3.96	3.76		
D2a	3.78	4.18	3.98		
E	6	.40 BS0)		
E1	5.60	6.00	5.80		
E2	3.46	3.86	3.66		
E2a	4.195	4.595	4.395		
е	1	1.27BSC)		
k	1.05		-		
L	0.635	0.835	0.735		
La	0.635	0.835	0.735		
L1	0.200	0.400	0.300		
L4	0.025	0.225	0.125		
M	3.205	4.005	3.605		
θ	10°	12°	11°		
θ1	6°	8°	7°		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8 (SWP) (Standard)



Dimensions	Value		
Dilliensions	(in mm)		
С	1.270		
G	0.660		
G1	0.820		
Х	0.610		
X1	4.100		
X2	4.420		
Y	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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