NPN Silicon Planar Epitaxial Transistor PZT651

This NPN Silicon Epitaxial transistor is designed for use in industrial and consumer applications. The device is housed in the SOT−223 package which is designed for medium power surface mount applications.

SOT−223 package ensures level mounting, resulting in improved thermal conduction, and allows visual inspection of soldered joints. The formed leads absorb thermal stress during soldering, eliminating the possibility of damage to the die.

Features

- High Current
- The SOT−223 Package can be Soldered Using Wave or Reflow
- PNP Complement is PZT751T1G
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC−Q101 Qualified and PPAP Capable
- These Devices are Pb−Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Collector−Emitter Voltage	V_{CEO}	60	Vdc
Collector−Base Voltage	V_{CBO}	80	Vdc
Emitter−Base Voltage	V_{EBO}	5.0	Vdc
Collector Current	I_C	2.0	Adc
Total Power Dissipation @ T_A = 25°C (Note 1) Derate above 25°C	P_D	 0.8 6.4	W mW/°C
Storage Temperature Range	T_{stg}	−65 to 150	°C
Junction Temperature	T_J	150	°C

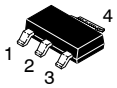
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance from Junction−to−Ambient in Free Air	$R_{\theta JA}$	156	°C/W
Maximum Temperature for Soldering Purposes Time in Solder Bath	T_L	260 10	°C Sec

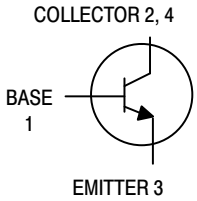
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Device mounted on a FR−4 glass epoxy printed circuit board using minimum recommended footprint.

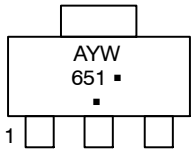
SOT−223 PACKAGE HIGH CURRENT NPN SILICON TRANSISTOR SURFACE MOUNT



SOT−223
CASE 318E−04
STYLE 1



MARKING DIAGRAM



A = Assembly Location
Y = Year
WW = Work Week
▪ = Pb−Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping†
PZT651T1G	SOT−223 (Pb−Free)	1,000 / Tape & Reel
SPZT651T1G	SOT−223 (Pb−Free)	1,000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristics	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector−Emitter Breakdown Voltage (I_C = 10 mAdc, I_B = 0)	$V_{(BR)CEO}$	60	–	Vdc
Collector−Emitter Breakdown Voltage (I_C = 100 μAdc, I_E = 0)	$V_{(BR)CBO}$	80	–	Vdc
Emitter−Base Breakdown Voltage (I_E = 10 μAdc, I_C = 0)	$V_{(BR)EBO}$	5.0	–	Vdc
Base−Emitter Cutoff Current (V_{EB} = 4.0 Vdc)	I_{EBO}	–	0.1	μAdc
Collector−Base Cutoff Current (V_{CB} = 80 Vdc, I_E = 0)	I_{CBO}	–	100	nAdc
ON CHARACTERISTICS (Note 2)				
DC Current Gain	h_{FE}			–
(I_C = 50 mAdc, V_{CE} = 2.0 Vdc)		75	–	
(I_C = 500 mAdc, V_{CE} = 2.0 Vdc)		75	–	
(I_C = 1.0 Adc, V_{CE} = 2.0 Vdc)		75	–	
(I_C = 2.0 Adc, V_{CE} = 2.0 Vdc)		40	–	
Collector−Emitter Saturation Voltages	$V_{CE(sat)}$			Vdc
(I_C = 2.0 Adc, I_B = 200 mAdc)		–	0.5	
(I_C = 1.0 Adc, I_B = 100 mAdc)		–	0.3	
Base−Emitter Voltages (I_C = 1.0 Adc, V_{CE} = 2.0 Vdc)	$V_{BE(on)}$	–	1.0	Vdc
Base−Emitter Saturation Voltage (I_C = 1.0 Adc, I_B = 100 mAdc)	$V_{BE(sat)}$	–	1.2	Vdc
Current−Gain — Bandwidth (I_C = 50 mAdc, V_{CE} = 5.0 Vdc, f = 100 MHz)	f_T	75	–	MHz

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle = 2.0%

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TYPICAL CHARACTERISTICS

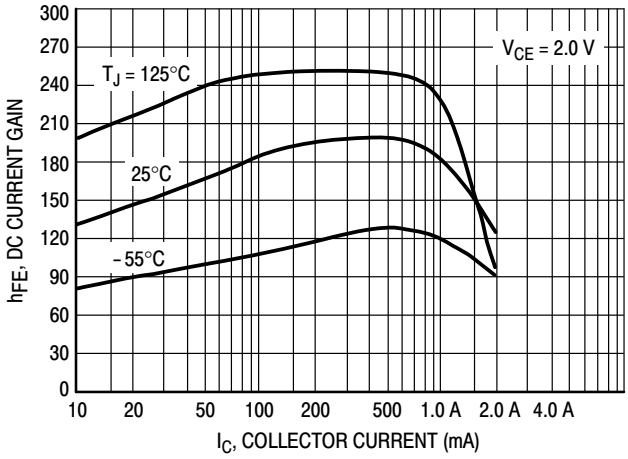

Figure 1. Typical DC Current Gain

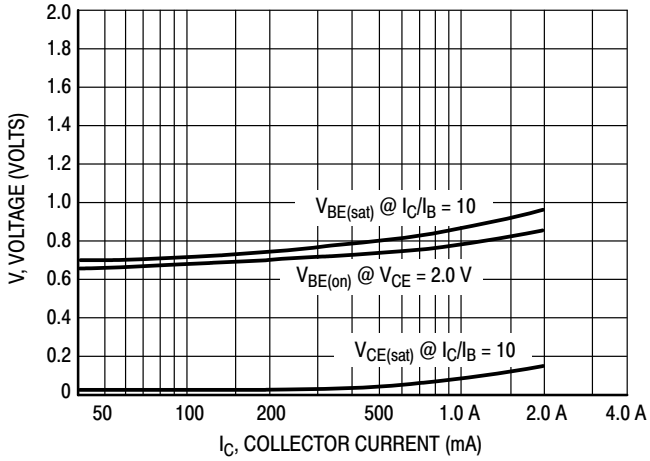

Figure 2. On Voltages

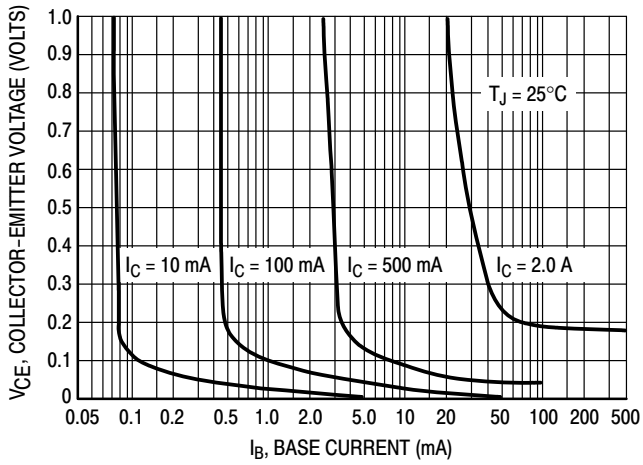

Figure 3. Collector Saturation Region

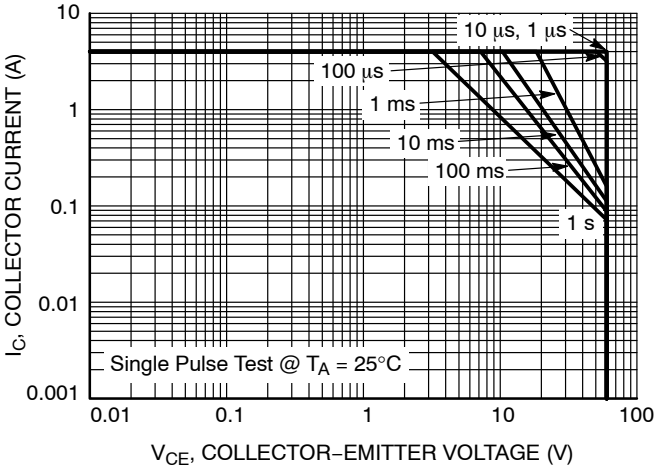

Figure 4. Safe Operating Area

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