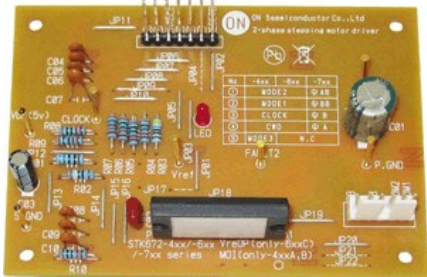


## STK672-440BNGEVB: Motor Driver, 2-Phase, Microstepping, Unipolar Evaluation Board

The STK672-440BNGEVB is an Evaluation Board for the STK672-440A-E, a hybrid IC for unipolar, 2-phase stepping motor driver with PWM current control. The device includes a built-in microstepping controller and is based on a unipolar constant-current PWM system. The STK672-440A-E supports application simplification and standardization by providing a built-in 4 phase distribution stepping motor controller. It supports five excitation methods: 2 phase, 1-2 phase, W1-2 phase, 2W1-2 phase, and 4W1-2 phase excitations, and can provide control of the basic stepping angle of the stepping motor divided into 1/16 step units. It also allows the motor speed to be controlled with only a clock signal. The use of this hybrid IC allows designers to implement systems that provide high motor torques, low vibration levels, low noise, fast response, and high-efficiency drive. In addition the STK672-440A-E has various protective function against over-current and over-heat.



### Features and Applications

#### Features

- Built-in micro stepping controller
- Built-in protective function against over-current and over-heat
- Built-in current detection resistors

#### Applications

- Computing & Peripherals
- Industrial
- Medical

Evaluation/Development Tool Information					
Product	Status	Compliance	Short Description	Parts Used	Action
STK672-440BNGEVB	Active	Pb-free	Motor Driver, 2-Phase, Microstepping, Unipolar Evaluation Board	STK672-440BN-E	<a href="#">Contact Local Sales Office</a>

Technical Documents			
Type	Document Title	Document ID/Size	Rev
Eval Board: BOM	STK672-440BNGEVB Bill of Materials ROHS Compliant	STK672-440BNGEVB_BOM_ROHS - 40 KB	0
Eval Board: Schematic	STK672-440BNGEVB Schematic	STK672-440BNGEVB_SCHEMATIC - 150 KB	0
Eval Board: Test Procedure	STK672-440BNGEVB Test Procedure	STK672-440BNGEVB_TEST_PROCEDURE - 382 KB	0
Eval Board: Gerber	STK672-440BNGEVB Gerber Layout Files (Zip Format)	STK672-440BNGEVB_GERBER - 69 KB	0

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