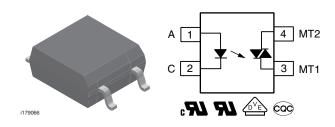


# Optocoupler, Phototriac Output, Non-Zero Crossing, 1.5 kV/µs dV/dt, 600 V



#### **FEATURES**

- High static dV/dt > 1.5 kV/µs
- Input sensitivity I<sub>FT</sub> = 5 mA and 10 mA
- On-state RMS current I<sub>T(RMS)</sub> = 70 mA
- · 600 V peak off-state blocking voltage
- Isolation test voltage 3750 V<sub>RMS</sub>
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





ROHS
COMPLIANT
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#### **LINKS TO ADDITIONAL RESOURCES**













## **DESCRIPTION**

The VOM3052 and VOM3053 phototriac consist a GaAs IRLED optically coupled to a photosensitive non-zero crossing TRIAC packaged in a SOP-4 package. It has a LED trigger current of 5 mA for VOM3053 and 10 mA for VOM3052

The VOM3052 and VOM3053 phototriac isolate low-voltage logic from 120  $V_{AC}$ , 240  $V_{AC}$ , and 380  $V_{AC}$  lines to control resistive, inductive, or capacitive loads including motors, solenoids, high current thyristors or TRIAC and relays.

#### **APPLICATIONS**

- Consumer appliances
- Triac drives
- Solid-state relays
- · Industrial controls
- Office equipment
- · Lighting controls

#### **AGENCY APPROVALS**

- UL
- cUI
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- CQC GB4943.1

ORDERING INFORMATION				
V O M	3 0 5 X	T SOP-4		
	PART NUMBER	7.21 mm		
AGENCY CERTIFIED / PACKAGE	TRIGGER CURRENT IFT			
UL, cUL, CQC	5 mA	10 mA		
SOP-4	VOM3053T	VOM3052T		
SOP-4 180° orientation	VOM3053T3	-		
VDE, UL, cUL, CQC	5 mA	10 mA		
SOP-4	VOM3053-X001T	VOM3052-X001T		

#### Notes

- For additional information on the available options refer to option information
- The product is available only on tape and reel

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT		
INPUT							
Reverse voltage			$V_R$	6	V		
Forward current			I <sub>F</sub>	60	mA		
Peak surge current	100 μs, 200 pps		I <sub>FSM</sub>	0.5	А		
Power dissipation			P <sub>diss</sub>	100	mW		
OUTPUT							
Peak off-state voltage			$V_{DRM}$	600	V		
RMS on-state current			I <sub>T(RMS)</sub>	70	mA		
Peak non-repetitive surge current	PW = 100 ms, 120 pps		I <sub>TSM</sub>	1	Α		
Power dissipation			P <sub>diss</sub>	200	mW		
COUPLER							
Isolation test voltage	t = 1 min		V <sub>ISO</sub>	3750	V <sub>RMS</sub>		
Power dissipation			P <sub>tot</sub>	300	mW		
Storage temperature range			T <sub>stg</sub>	-55 to +150	°C		
Ambient temperature range			T <sub>amb</sub>	-40 to +100	°C		
Soldering temperature			T <sub>sld</sub>	260	°C		

#### Notes

Stresses in excess of the Absolute Maximum Ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to Absolute
Maximum Ratings for extended periods of the time can adversely affect reliability.

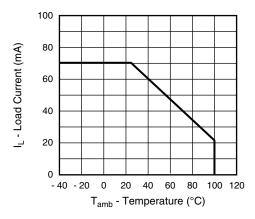


Fig. 1 - Recommended Operating Condition



<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	I <sub>F</sub> = 10 mA		$V_{F}$	-	1.2	1.5	V
Reverse current	V <sub>R</sub> = 6 V		I <sub>R</sub>	ı	-	10	μΑ
Input capacitance	$V_F = 0 V$ , $f = 1 MHz$		Cı	-	25	-	pF
ОUТРUТ	OUTPUT						
Off-state current	$V_D = V_{DRM}$		I <sub>DRM</sub>	-	-	100	nA
On-state voltage	I <sub>T</sub> = 100 mA		$V_{TM}$	-	-	3	V
Critical rate of rise off-state voltage	V <sub>D</sub> = 0.67 V <sub>DRM</sub> , T <sub>J</sub> = 25 °C		dV/dt <sub>cr</sub>	1500	-	-	V/µs
Critical rate of rise of voltage at current commutation			dV/dt <sub>crq</sub>	-	0.13	-	V/µs
COUPLER							
LED trigger current,	V <sub>D</sub> = 3 V	VOM3053	I <sub>FT</sub>	-	-	5	mA
current required to latch output		VOM3052	I <sub>FT</sub>	-	-	10	mA
Capacitance (input - output)	f = 1 MHz, V <sub>IO</sub> = 0 V		C <sub>IO</sub>	-	0.8	-	pF
Peak off-state voltage	I <sub>C</sub> = 100 μA		$V_{DRM}$	600	-	-	V
Holding current			I <sub>hold</sub>	-	0.3	-	mA

#### Note

Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements.

SAFETY AND INSULATION RATINGS						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Climatic classification (according to IEC 68 part 1)		-	40 / 100 / 21	-		
Pollution degree (DIN VDE 0109)		-	2	-		
Comparative tracking index	CTI	175	-	399		
Peak transient overvoltage	V <sub>IOTM</sub>	-	-	6000	V <sub>peak</sub>	
Peak insulation voltage	V <sub>IORM</sub>	-	-	707	V <sub>peak</sub>	
Isolation resistance at T <sub>amb</sub> = 100 °C, V <sub>DC</sub> = 500 V	R <sub>IO</sub>	10 <sup>11</sup>	-	=	Ω	
Isolation resistance at T <sub>amb</sub> = 25 °C, V <sub>DC</sub> = 500 V	R <sub>IO</sub>	10 <sup>12</sup>	-	-	Ω	
Safety rating - power	P <sub>SO</sub>	-	-	400	mW	
Safety rating - input current	I <sub>SI</sub>	-	-	150	mA	
Safety rating - temperature	T <sub>SI</sub>	-	-	165	°C	
Creepage distance		5	-	=	mm	
Clearance distance		5	-	-	mm	
Insulation thickness	DTI	0.4	-	-	mm	

## **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

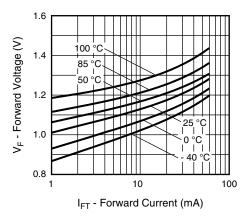


Fig. 2 - Forward Current vs. Forward Voltage

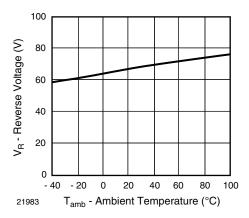


Fig. 3 - Reverse Voltage vs. Ambient Temperature

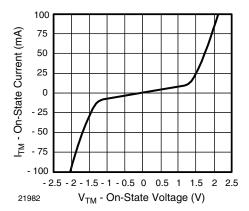


Fig. 4 - On-State Current vs. On-State Voltage

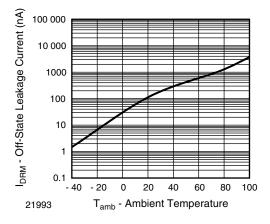


Fig. 5 - Off-State Leakage Current vs. Ambient Temperature

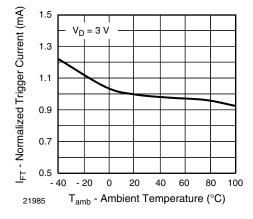


Fig. 6 - Normalized Trigger Current vs. Ambient Temperature

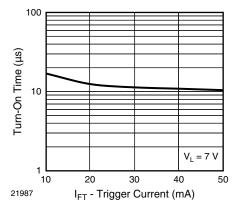


Fig. 7 - Trigger Current vs. Turn-On Time



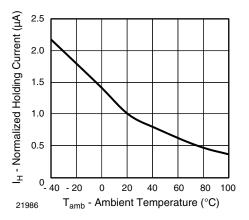


Fig. 8 - Normalized Holding Current vs. Ambient Temperature

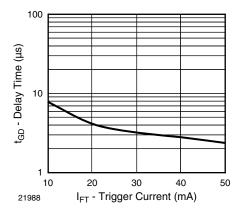


Fig. 9 - Trigger Current vs. Delay Time

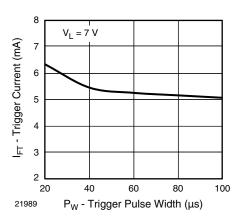


Fig. 10 - Trigger Current vs. Trigger Pulse Width

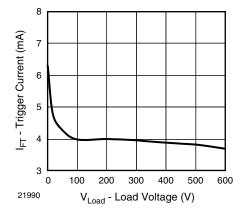
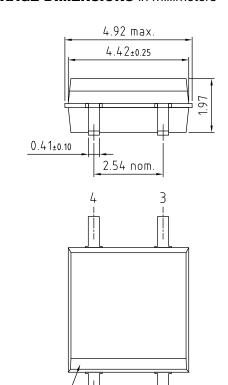
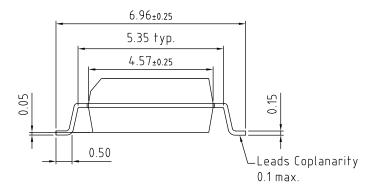
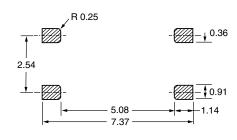


Fig. 11 - Trigger Current vs. Load Voltage

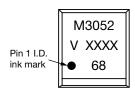
## **PACKAGE DIMENSIONS** in millimeters







## **PACKAGE MARKING** (example)



#### Note

PIN ONE I.D

• XXXX = LMC (lot marking code)

### **TAPE AND REEL PACKAGING**

Dimensions in millimeters

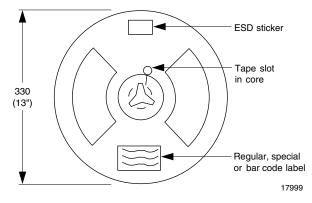


Fig. 12 - Tape and Reel Shipping Medium, 2000 Units per Reel



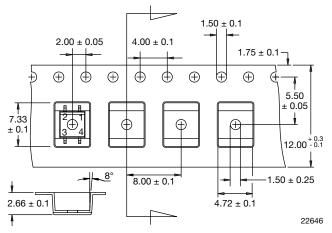


Fig. 13 - Tape Dimensions

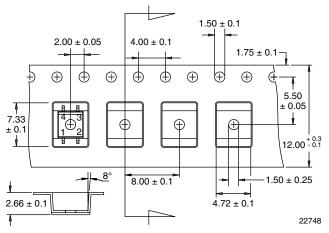


Fig. 14 - Tape Dimensions (order code T3)

#### **SOLDER PROFILE**

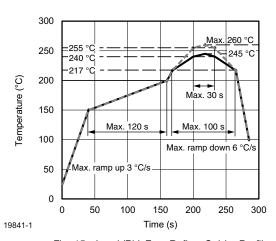


Fig. 15 - Lead (Pb)-Free Reflow Solder Profile According to J-STD-020

#### HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2

Floor life: unlimited

Conditions:  $T_{amb}$  < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020





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