GP1A91LR/GP1A91LC

Subminiature OPIC **Photointerrupter**

■ Features

- 1. Compact package $(3.7 \times 2.6 \times 3.1 \text{mm})$
- 2. Can be directly connected to C-MOS logic and microcomputer
- 3. Low voltage operation, low dissipation current (Operating supply voltage: 1.4 to 7.0V OFF-state consumption current: MAX. 0.5mA)
- 4. Gap width 1.2mm, slit width 0.23mm
- 5. General purpose

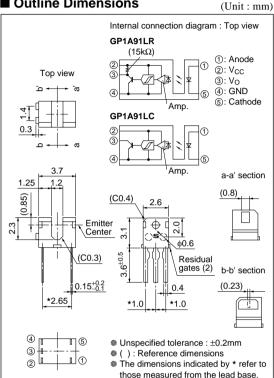
■ Applications

- 1. Cameras
- 2. CD-ROM drives

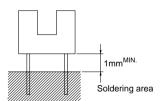
■ Absolute Maximum Ratings (Ta=25°							
	Parameter	Symbol	Rating	Unit			
Input	Forward current	IF	50	mA			
	Reverse voltage	VR	6	V			
	Power dissipation	P	75	mW			
Output	Supply voltage	Vcc	7.0	V			
	Low level Output current	Io	2.0	mA			
	Power dissipation	Po	80	mW			
Operating temperature		Topr	-25 to +85	°C			
Storage temperature		Tstg	-40 to +100	°C			
*1 Soldering temperature		Tsol	260	°C			

^{*1} The derating factors of absolute maximum ratings due to ambient temperature are shown in Fig.3, 4, 5

■ Outline Dimensions



*"OPIC" (Optical IC) is a trademark of the SHARP Corporation. An OPIC consists of a light-detecting element and signal-processing circuit integrated onto a single chip.



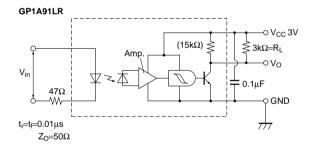
^{*2} For 5s or less

■ Electro-optical Characteristics (Ta=25°								
Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit	
Input -	Forward voltage	VF	I _F =5mA	-	1.15	1.25	V	
	Reverse current	Ir	V _R =3V	_	-	10	μΑ	
Output 1	Operating supply voltage	Vcc	_	1.4	_	7.0	V	
	Low level output voltage	Vol	Vcc=3V, IoL=1mA, IF=5mA	_	0.1	0.4	V	
	High level output voltage	Voh	Vcc=3V, I _F =0mA	2.9	_	_	V	
	Low level supply current	Iccl	Vcc=3V, I _F =5mA	_	0.7	1.2	mA	
	High level supply current	Іссн	Vcc=3V, I _F =0mA	_	0.3	0.5	mA	
Transfer *	*3 "High→Low" threshold input current	IFHL	Vcc=3V	_	1.2	3.5	mA	
	*4 Hysteresis	IFLH/IFHL	Vcc=3V	0.55	0.8	0.95	_	
	"Low-High" propagation delay time	tplh	Vcc=3V	_	10	30	μs	
		t PHL	I _F =5mA	_	3	15		
	Rise time	tr	$R_L=3k\Omega$ (GP1A91LR)	_	0.6	3.0		
	Fall time	tf	$R_L=2.4k\Omega$ (GP1A91LC)	_	0.2	1.0		

^{*3 1&}lt;sub>FHL</sub> represents forward current when output goes from High to Low.

GP1A91LC---R_L (15k Ω) is applied to Vcc-Vo (Condition during measureing response time : R_L=2.4k Ω)

Fig.1 Test Circuit for Response Time



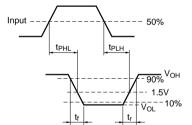
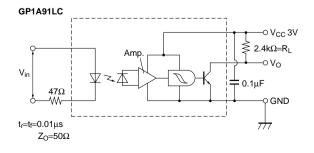
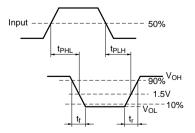


Fig.2 Test Circuit for Response Time





^{*4} Hysteresis stands for IFLH/IFHL

Fig.3 Forward Current vs. Ambient Temperature

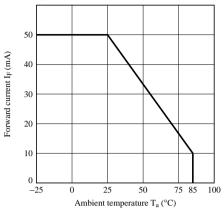


Fig.5 Output Power Dissipation vs.
Ambient Temperature

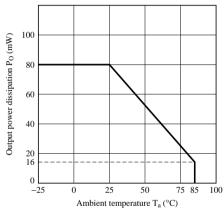


Fig.7 Relative Threshold Input Current vs. Supply Voltage

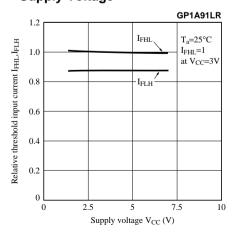


Fig.4 Output Current vs. Ambient Temperature

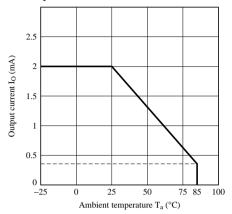


Fig.6 Forward Current vs. Forward Voltage

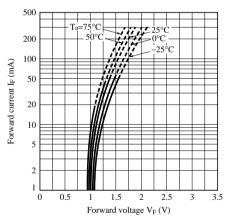


Fig.8 Relative Threshold Input Current vs. Supply Voltage

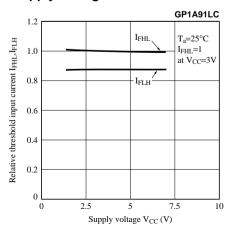


Fig.9 Relative Threshold Input Current vs.

Ambient Temperature

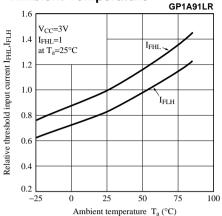


Fig.11 Low Level, High Level Supply Current vs. Supply Voltage (1)

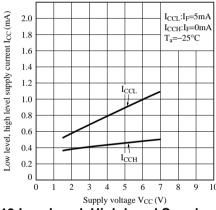


Fig.13 Low Level, High Level Supply Current vs. Supply Voltage (2)

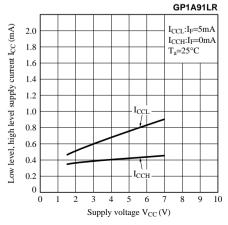


Fig.10 Relative Threshold Input Current vs.
Ambient Temperature

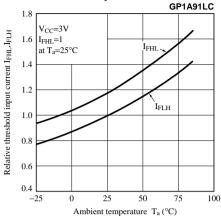


Fig.12 Low Level, High Level Supply Current vs. Supply Voltage (1)

GP1A91LC

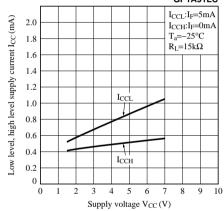


Fig.14 Low Level, High Level Supply Current vs. Supply Voltage (2)

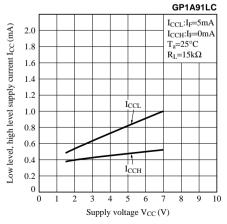


Fig.15 Low Level, High Level Supply Current vs. Supply Voltage (3)

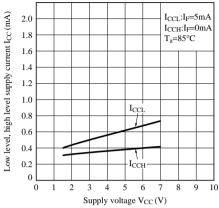


Fig.17 Low Level Output Voltage vs. Low Level Output Current GP1A91LR

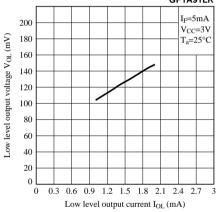


Fig.19 Low Level Output Voltage vs.
Ambient Temperature

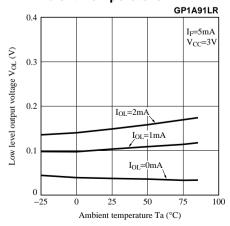


Fig.16 Low Level, High Level Supply Current vs. Supply Voltage (3)

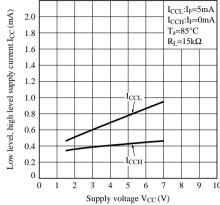


Fig.18 Low Level Output Voltage vs. Low Level Output Current GP1A91LC

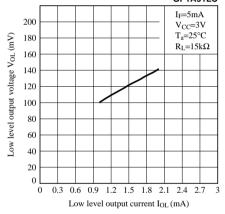


Fig.20 Low Level Output Voltage vs. Ambient Temperature

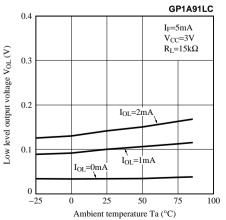


Fig.21 Rise Time, Fall Time vs. Load Resistance

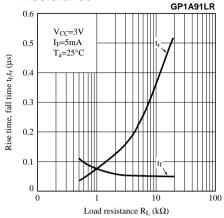


Fig.23 Propagation Delay Time vs. Forward Current

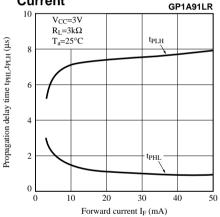


Fig.25 Low, High Level Output vs. Shield Distance (1) (Typical Value)

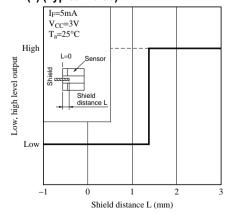


Fig.22 Rise Time, Fall Time vs. Load Resistance

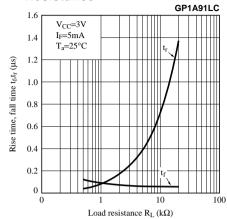


Fig.24 Propagation Delay Time vs. Forward Current

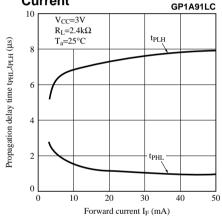
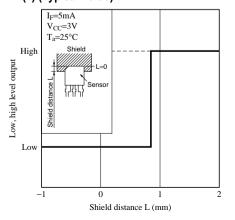


Fig.26 Low, High Level Output vs. Shield Distance (2) (Typical Value)



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