

Product Summary

BV _{DSS}	R _{Ds(ON)} Max	I _D Max T _A = +25°C
-30V	13.5mΩ @ V _{GS} = -10V	-11.4A
	27mΩ @ V _{GS} = -5V	-10.8A
	35mΩ @ V _{GS} = -4.5V	-9.5A

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{Ds(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

Applications

- General purpose interfacing switches
- Power-management functions

Features

- Low Gate Threshold Voltage
- Low On-Resistance
- ESD Protected Gate
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](#) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>

Mechanical Data

- Package: U-DFN2020-6
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208^(e)
- Weight: 0.008 grams (Approximate)

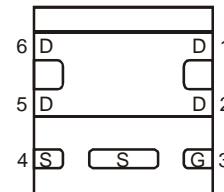
U-DFN2020-6 (Type E)



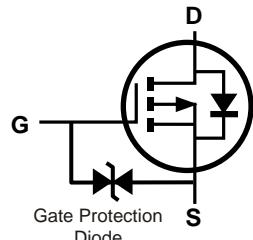
Top View



Bottom View



Pin Out
Bottom View



Equivalent Circuit

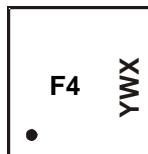
Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMP3014SFDE-7	U-DFN2020-6 (Type E)	3,000	Tape & Reel
DMP3014SFDE-13	U-DFN2020-6 (Type E)	10,000	Tape & Reel

Notes:

- EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3).compliant. All applicable RoHS exemptions applied.
- See http://www.diodes.com/quality/lead_free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



F4 = Product Type Marking Code

YWX = Date Code Marking

Y = Year (ex: 4 = 2024)

W = Week (ex: a = Week 27; z Represents Week 52 and 53)

X = Internal Code (ex: U = Monday)

Date Code Key

Year	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034
Code	3	4	5	6	7	8	9	0	1	2	3	4

Week	1-26	27-52	53
Code	A-Z	a-z	z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	T	U	V	W	X	Y	Z

DMP3014SFDE

Document number: DS45524 Rev. 4 - 2

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit	
Drain-Source Voltage			V_{DSS}	-30	V	
Gate-Source Voltage			V_{GSS}	± 25	V	
Continuous Drain Current, $V_{GS} = -10\text{V}$ (Note 6)	Steady State	$T_A = +25^\circ\text{C}$	I_D	-11.4	A	
		$T_A = +70^\circ\text{C}$		-9.1		
Continuous Source-Drain Diode Current (Note 6)			I_S	-3.2	A	
Pulsed Drain Current (10 μs Pulse, Duty Cycle = 1%)			I_{DM}	-65	A	
Avalanche Current ($L = 0.1\text{mH}$) (Note 8)			I_{AS}	-27	A	
Avalanche Energy ($L = 0.1\text{mH}$) (Note 8)			E_{AS}	38	mJ	

Thermal Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Total Power Dissipation (Note 5)			P_D	0.8	W
Thermal Resistance, Junction to Ambient (Note 5)			$R_{\theta JA}$	150	$^\circ\text{C/W}$
Total Power Dissipation (Note 6)			P_D	2.2	W
Thermal Resistance, Junction to Ambient (Note 6)			$R_{\theta JA}$	54	$^\circ\text{C/W}$
Thermal Resistance, Junction to Case (Note 7)			$R_{\theta JC}$	8	
Operating and Storage Temperature Range			T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 9)						
Drain-Source Breakdown Voltage	BV_{DSS}	-30	—	—	V	$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	-1	μA	$V_{DS} = -30\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	—	—	± 5	μA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 9)						
Gate Threshold Voltage	$V_{GS(TH)}$	-1.2	—	-2.6	V	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	10.4	13.5	$\text{m}\Omega$	$V_{GS} = -10\text{V}, I_D = -11\text{A}$
			17	27		$V_{GS} = -5\text{V}, I_D = -8\text{A}$
			20	35		$V_{GS} = -4.5\text{V}, I_D = -5\text{A}$
Diode Forward Voltage	V_{SD}	—	-0.7	-1.2	V	$V_{GS} = 0\text{V}, I_S = -2\text{A}$
DYNAMIC CHARACTERISTICS (Note 10)						
Input Capacitance	C_{iss}	—	1015	—	pF	$V_{DS} = -15\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	163	—		
Reverse Transfer Capacitance	C_{rss}	—	112	—		
Gate Resistance	R_g	—	28	—	Ω	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Total Gate Charge ($V_{GS} = -10\text{V}$)	Q_g	—	18	—	nC	$V_{DS} = -10\text{V}, I_D = -8\text{A}$
Total Gate Charge ($V_{GS} = -4.5\text{V}$)	Q_g	—	9	—		
Gate-Source Charge	Q_{gs}	—	3.2	—		
Gate-Drain Charge	Q_{gd}	—	2.8	—	ns	$V_{DD} = -15\text{V}, V_{GS} = -10\text{V}, R_g = 6\Omega, I_D = -9.5\text{A}$
Turn-On Delay Time	$t_{D(ON)}$	—	4	—		
Turn-On Rise Time	t_R	—	26	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	66	—		
Turn-Off Fall Time	t_F	—	70	—		

Notes:

5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
7. Thermal resistance from junction to soldering point (on the exposed drain pad).
8. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25^\circ\text{C}$.
9. Short duration pulse test used to minimize self-heating effect.
10. Guaranteed by design. Not subject to product testing.

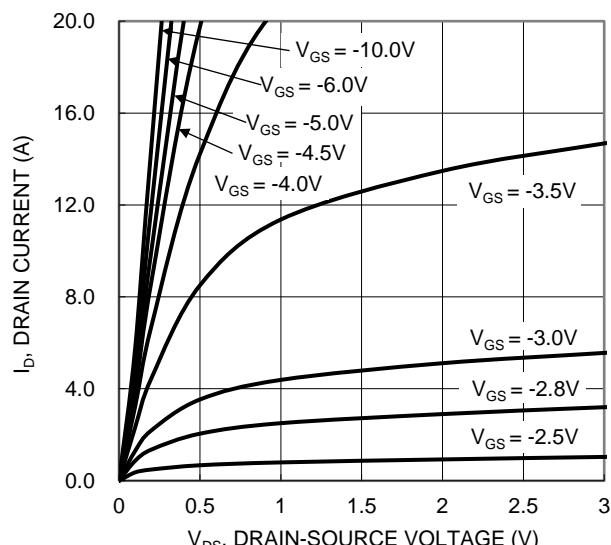


Figure 1. Typical Output Characteristic

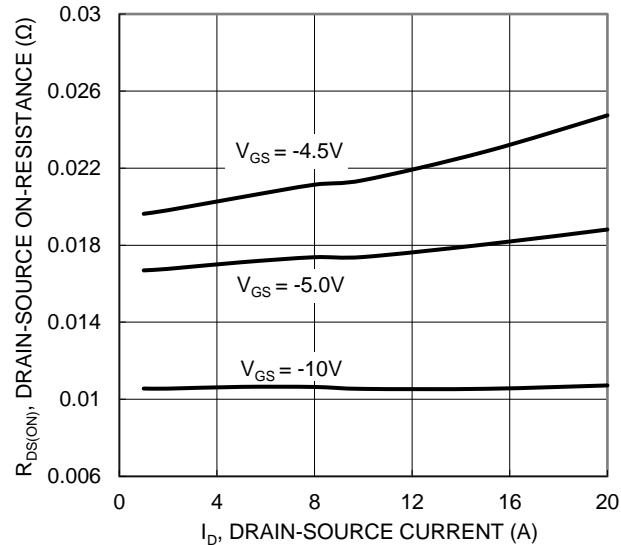


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

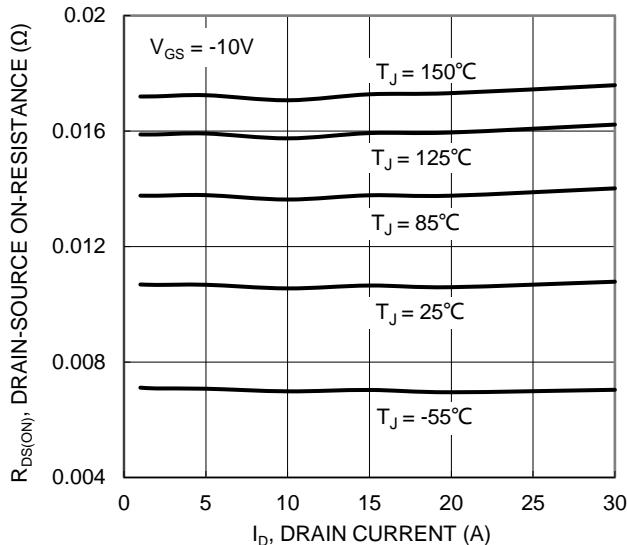


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

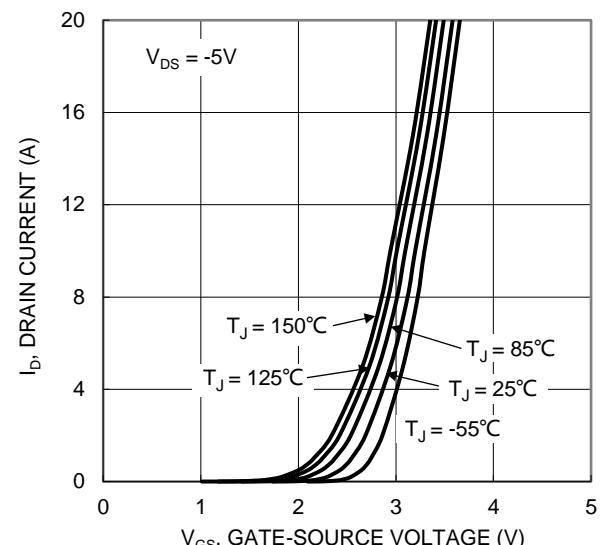


Figure 2. Typical Transfer Characteristic

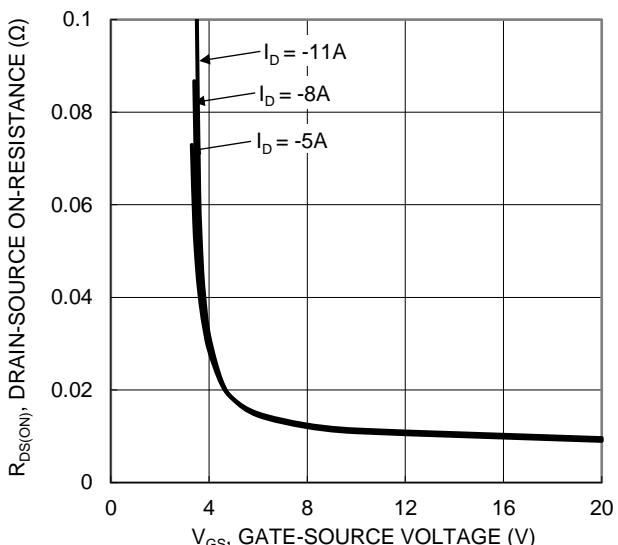


Figure 4. Typical Transfer Characteristic

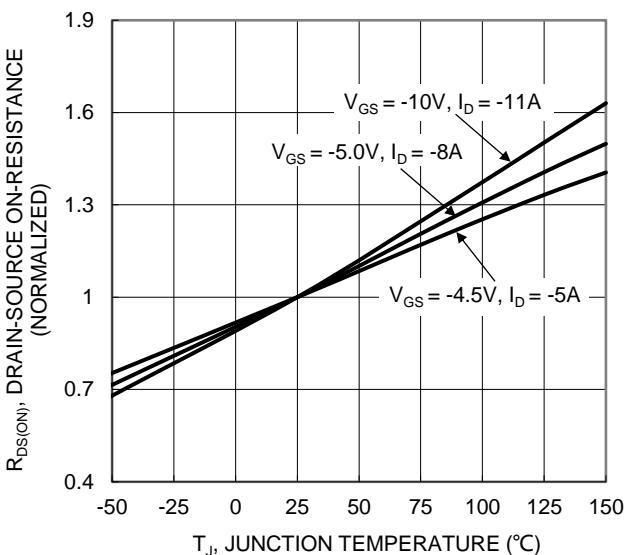


Figure 6. On-Resistance Variation with Junction Temperature

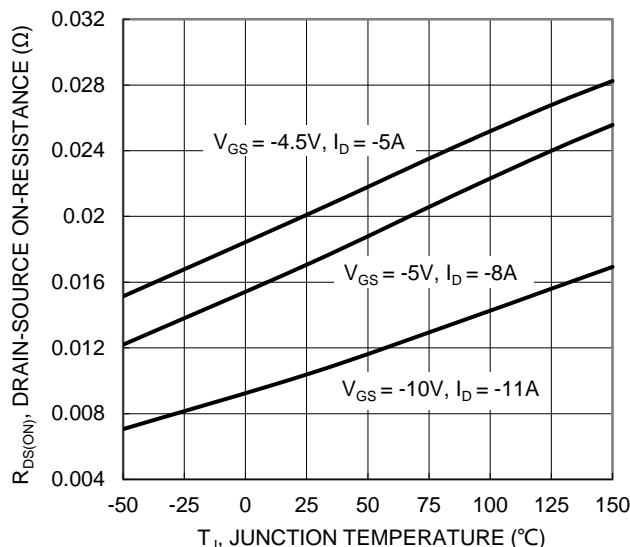


Figure 7. On-Resistance Variation with Junction Temperature

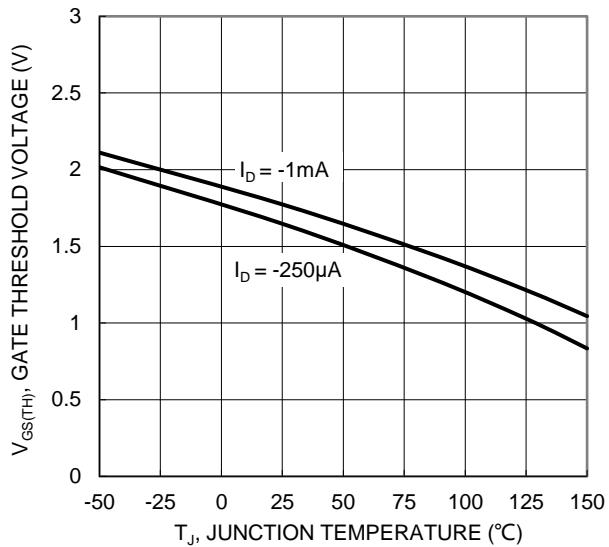


Figure 8. Gate Threshold Variation vs. Junction Temperature

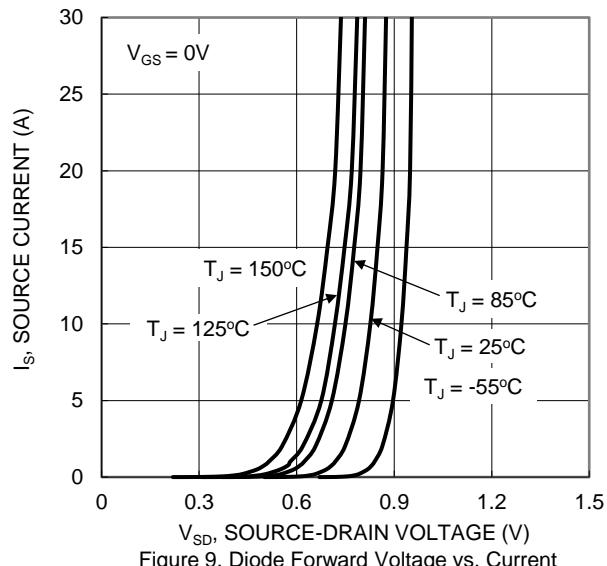


Figure 9. Diode Forward Voltage vs. Current

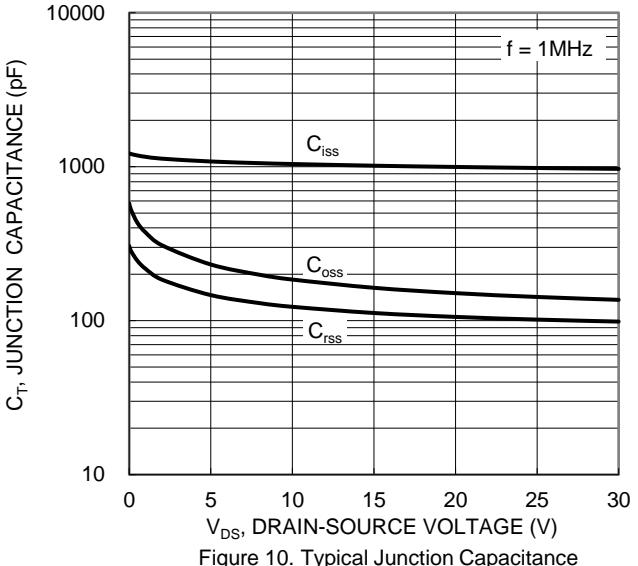


Figure 10. Typical Junction Capacitance

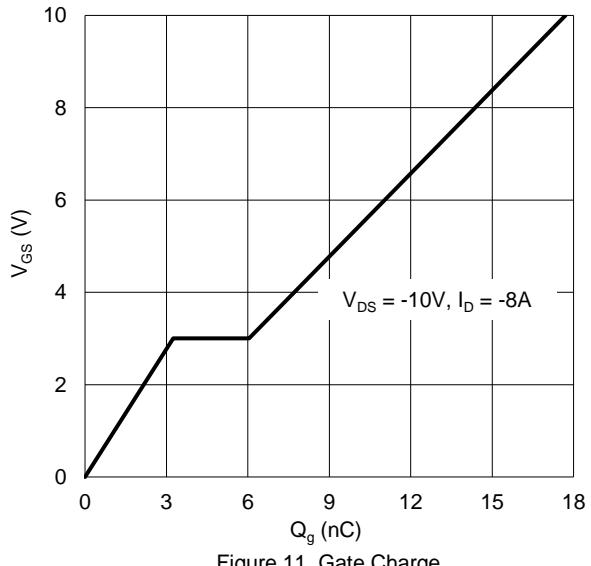


Figure 11. Gate Charge

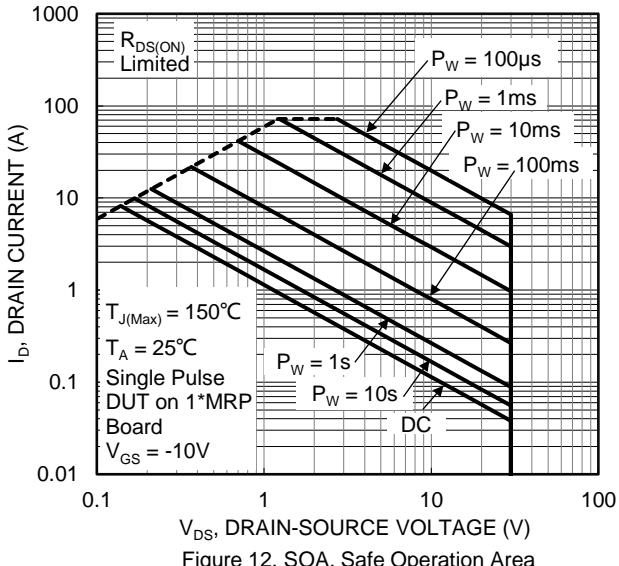


Figure 12. SOA, Safe Operation Area

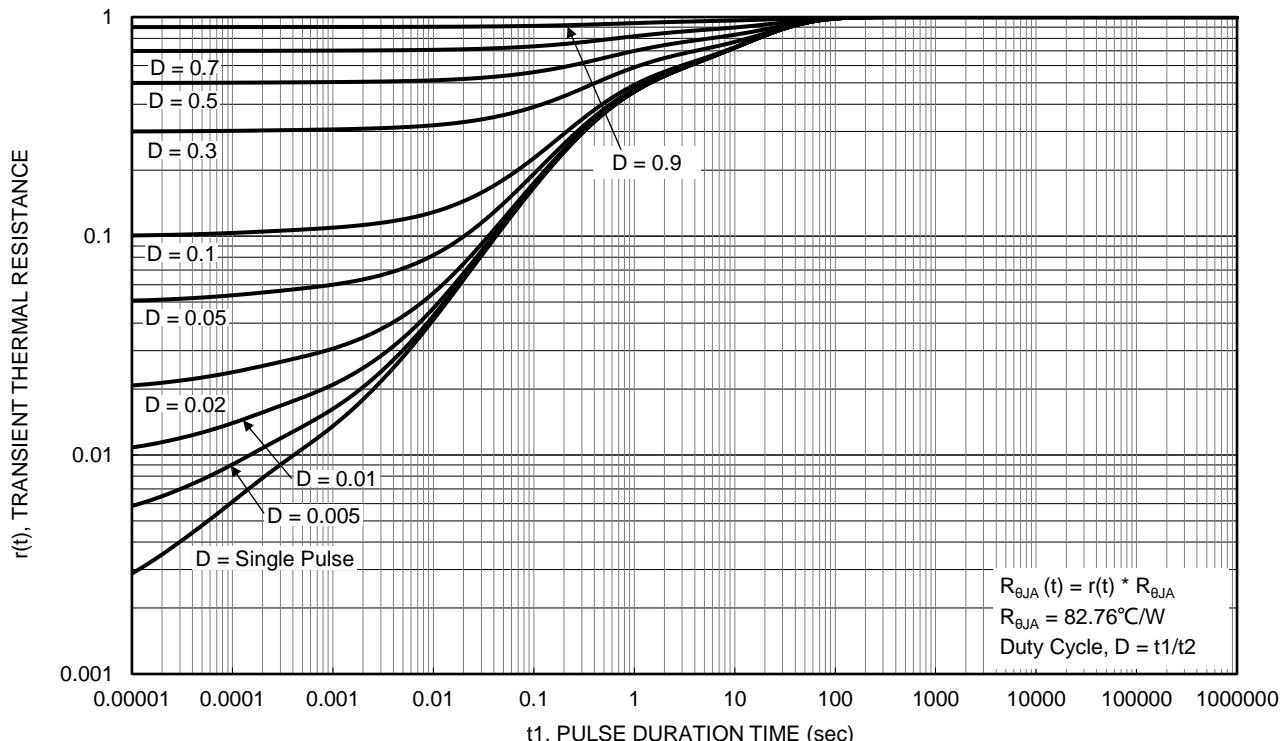
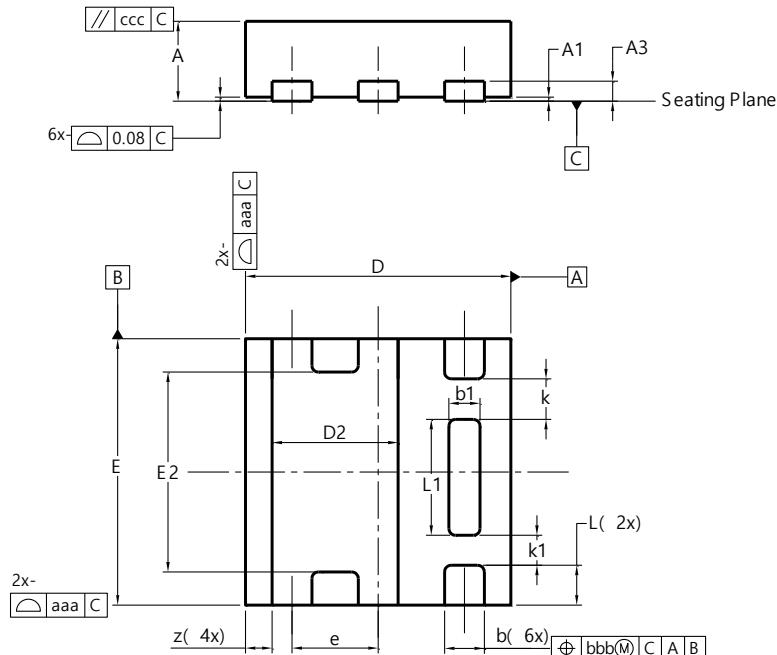


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN2020-6 (Type E)



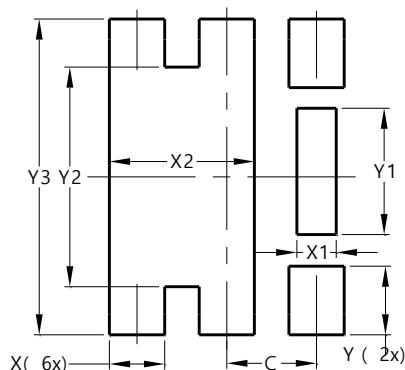
U-DFN2020-6 Type E			
Dim	Min	Max	Typ
A	0.57	0.63	0.60
A1	0.00	0.05	0.03
A3	—	—	0.15
b	0.25	0.35	0.30
b1	0.185	0.285	0.235
D	1.95	2.05	2.00
D2	0.85	1.05	0.95
E	1.95	2.05	2.00
E2	1.40	1.60	1.50
e	—	—	0.65
L	0.25	0.35	0.30
L1	0.82	0.92	0.87
k	—	—	0.305
k1	—	—	0.225
Z	—	—	0.20

All Dimensions in mm

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN2020-6 (Type E)



Dimensions	Value (in mm)
C	0.650
X	0.400
X1	0.285
X2	1.050
Y	0.500
Y1	0.920
Y2	1.600
Y3	2.300

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